#& RESOURCE LIST

#- Resource Type Assembly S/N Revision PRD HLA-SN FPGArev

#: 1 SYSN n/a MY04604955 n/a n/a n/a n/a n/a n/a n/a n/a

#: 1 SYST n/a N\_CBQn9Xd8YwZaV7wh9F0w n/a n/a n/a n/a n/a n/a n/a n/a

#: 1 CCG n/a ZaPG4UAIMb-geCfqrlQVK0 n/a n/a n/a n/a n/a n/a n/a n/a

#: 2 CCG n/a efNdpvRTSv94imTMkyao3M n/a n/a n/a n/a n/a n/a n/a n/a

#: 3 CCG n/a 8CGqZ3Tm2bDX8BhPvJBZ4w n/a n/a n/a n/a n/a n/a n/a n/a

#: 4 CCG n/a 9fHV8iDqzGmQ41e-Tz-jQ0 n/a n/a n/a n/a n/a n/a n/a n/a

#@ Computer Interface Card

#: 1 CICD E8001-66443 JP-20843 005 951 n/a n/a n/a n/a n/a 0x80

#@ Active Control board in CC1

#: 11 CTRL E8001-66424 JP-20318 012 948 n/a n/a n/a n/a n/a 0x8C

#@ Active Control board in CC2

#: 21 CTRL E8001-66424 JP-20287 012 948 n/a n/a n/a n/a n/a 0x8C

#@ Passive Control board in CC3

#: 12 CTRL E8001-66422 JP-7868 004 940 n/a n/a n/a n/a n/a n/a

#@ Passive Control board in CC4

#: 22 CTRL E8001-66422 JP-7838 004 940 n/a n/a n/a n/a n/a n/a

#@ Clock board in CC1 slot 1

#: 11 CLK E8002-66415 JP-46363 122 951 E8002-66615 109 JP 951 046363 0x8003

#: 11 PI16 E8002-66416 JP-40756 104 947 E8002-66615 109 JP 951 046363 n/a

#: 11 PI17 E8002-66417 JP-38213 104 951 E8002-66615 109 JP 951 046363 n/a

#@ Clock board in CC2 slot 1

#: 21 CLK E8002-66415 JP-46332 122 951 E8002-66615 109 JP 951 046332 0x8003

#: 21 PI16 E8002-66416 JP-40905 104 951 E8002-66615 109 JP 951 046332 n/a

#: 21 PI17 E8002-66417 JP-38208 104 951 E8002-66615 109 JP 951 046332 n/a

#@ Clock board in CC3 slot 1

#: 12 CLK E8002-66415 JP-46371 122 951 E8002-66615 109 JP 951 046371 0x8003

#: 12 PI16 E8002-66416 JP-40819 104 950 E8002-66615 109 JP 951 046371 n/a

#: 12 PI17 E8002-66417 JP-38189 104 951 E8002-66615 109 JP 951 046371 n/a

#@ Clock board in CC4 slot 1

#: 22 CLK E8002-66415 JP-46345 122 951 E8002-66615 109 JP 951 046345 0x8003

#: 22 PI16 E8002-66416 JP-40840 104 950 E8002-66615 109 JP 951 046345 n/a

#: 22 PI17 E8002-66417 JP-38161 104 951 E8002-66615 109 JP 951 046345 n/a

#@ Wiring board

#: 11 WIRM E8002-66420 JP-6311 009 951 E8001-65270 054 JP 001 003922 0x75

#: 12 WIRM E8002-66420 JP-6311 009 951 E8001-65270 054 JP 001 003922 n/a

#: 21 WIRM E8002-66420 JP-6311 009 951 E8001-65270 054 JP 001 003922 n/a

#: 22 WIRM E8002-66420 JP-6311 009 951 E8001-65270 054 JP 001 003922 n/a

#@ DUT Interface Assy

#: 1 DUIF E8029-60132 HR-1230 018 943 n/a n/a n/a n/a n/a n/a

#@ Backplane board in CC1

#: 11 BCKP E8001-66432 JP-28051 010 950 n/a n/a n/a n/a n/a n/a

#@ Backplane board in CC2

#: 21 BCKP E8001-66432 JP-27956 010 948 n/a n/a n/a n/a n/a n/a

#@ Backplane board in CC3

#: 12 BCKP E8001-66432 JP-28046 010 950 n/a n/a n/a n/a n/a n/a

#@ Backplane board in CC4

#: 22 BCKP E8001-66432 JP-28088 010 950 n/a n/a n/a n/a n/a n/a

#@ PS1600 board in CC1 slot 2, Control-Segment-Slot-Nr 101

#: 125 CHBD E8030-66401 ZZ-40793 016 924 E8030-66620 010 ZZ 928 012924 0x25A,0x38

#: 125 REL E8030-66402 ZZ-41586 015 924 E8030-66620 010 ZZ 928 012924 0x358

#: 1251 CHMO E8030-66423 ZZ-529729 005 920 E8030-66620 010 ZZ 928 012924 n/a

#: 126 CHBD E8030-66401 ZZ-40793 016 924 E8030-66620 010 ZZ 928 012924 0x25A,0x38

#: 126 REL E8030-66402 ZZ-41586 015 924 E8030-66620 010 ZZ 928 012924 0x358

#: 1261 CHMO E8030-66423 ZZ-534776 005 926 E8030-66620 010 ZZ 928 012924 n/a

#: 127 CHBD E8030-66401 ZZ-40793 016 924 E8030-66620 010 ZZ 928 012924 0x25A,0x38

#: 127 REL E8030-66402 ZZ-41586 015 924 E8030-66620 010 ZZ 928 012924 0x358

#: 1271 CHMO E8030-66423 ZZ-529541 005 920 E8030-66620 010 ZZ 928 012924 n/a

#: 128 CHBD E8030-66401 ZZ-40793 016 924 E8030-66620 010 ZZ 928 012924 0x25A,0x38

#: 128 REL E8030-66402 ZZ-41586 015 924 E8030-66620 010 ZZ 928 012924 0x358

#: 1281 CHMO E8030-66423 ZZ-534514 005 926 E8030-66620 010 ZZ 928 012924 n/a

#: 129 CHBD E8030-66401 ZZ-40793 016 924 E8030-66620 010 ZZ 928 012924 0x25A,0x38

#: 129 REL E8030-66402 ZZ-41586 015 924 E8030-66620 010 ZZ 928 012924 0x358

#: 1291 CHMO E8030-66423 ZZ-529602 005 920 E8030-66620 010 ZZ 928 012924 n/a

#: 130 CHBD E8030-66401 ZZ-40793 016 924 E8030-66620 010 ZZ 928 012924 0x25A,0x38

#: 130 REL E8030-66402 ZZ-41586 015 924 E8030-66620 010 ZZ 928 012924 0x358

#: 1301 CHMO E8030-66423 ZZ-534774 005 926 E8030-66620 010 ZZ 928 012924 n/a

#: 131 CHBD E8030-66401 ZZ-40793 016 924 E8030-66620 010 ZZ 928 012924 0x25A,0x38

#: 131 REL E8030-66402 ZZ-41586 015 924 E8030-66620 010 ZZ 928 012924 0x358

#: 1311 CHMO E8030-66423 ZZ-534598 005 926 E8030-66620 010 ZZ 928 012924 n/a

#: 132 CHBD E8030-66401 ZZ-40793 016 924 E8030-66620 010 ZZ 928 012924 0x25A,0x38

#: 132 REL E8030-66402 ZZ-41586 015 924 E8030-66620 010 ZZ 928 012924 0x358

#: 1321 CHMO E8030-66423 ZZ-534460 005 926 E8030-66620 010 ZZ 928 012924 n/a

#@ PS1600 board in CC1 slot 5, Control-Segment-Slot-Nr 104

#: 117 CHBD E8030-66401 ZZ-36225 015 849 E8030-66620 009 ZZ 903 008580 0x25A,0x38

#: 117 REL E8030-66402 ZZ-36196 014 846 E8030-66620 009 ZZ 903 008580 0x358

#: 1171 CHMO E8030-66423 ZZ-515313 005 849 E8030-66620 009 ZZ 903 008580 n/a

#: 118 CHBD E8030-66401 ZZ-36225 015 849 E8030-66620 009 ZZ 903 008580 0x25A,0x38

#: 118 REL E8030-66402 ZZ-36196 014 846 E8030-66620 009 ZZ 903 008580 0x358

#: 1181 CHMO E8030-66423 ZZ-59164 005 849 E8030-66620 009 ZZ 903 008580 n/a

#: 119 CHBD E8030-66401 ZZ-36225 015 849 E8030-66620 009 ZZ 903 008580 0x25A,0x38

#: 119 REL E8030-66402 ZZ-36196 014 846 E8030-66620 009 ZZ 903 008580 0x358

#: 1191 CHMO E8030-66423 ZZ-59166 005 849 E8030-66620 009 ZZ 903 008580 n/a

#: 120 CHBD E8030-66401 ZZ-36225 015 849 E8030-66620 009 ZZ 903 008580 0x25A,0x38

#: 120 REL E8030-66402 ZZ-36196 014 846 E8030-66620 009 ZZ 903 008580 0x358

#: 1201 CHMO E8030-66423 ZZ-59527 005 850 E8030-66620 009 ZZ 903 008580 n/a

#: 121 CHBD E8030-66401 ZZ-36225 015 849 E8030-66620 009 ZZ 903 008580 0x25A,0x38

#: 121 REL E8030-66402 ZZ-36196 014 846 E8030-66620 009 ZZ 903 008580 0x358

#: 1211 CHMO E8030-66423 ZZ-515321 005 849 E8030-66620 009 ZZ 903 008580 n/a

#: 122 CHBD E8030-66401 ZZ-36225 015 849 E8030-66620 009 ZZ 903 008580 0x25A,0x38

#: 122 REL E8030-66402 ZZ-36196 014 846 E8030-66620 009 ZZ 903 008580 0x358

#: 1221 CHMO E8030-66423 ZZ-59165 005 849 E8030-66620 009 ZZ 903 008580 n/a

#: 123 CHBD E8030-66401 ZZ-36225 015 849 E8030-66620 009 ZZ 903 008580 0x25A,0x38

#: 123 REL E8030-66402 ZZ-36196 014 846 E8030-66620 009 ZZ 903 008580 0x358

#: 1231 CHMO E8030-66423 ZZ-515327 005 849 E8030-66620 009 ZZ 903 008580 n/a

#: 124 CHBD E8030-66401 ZZ-36225 015 849 E8030-66620 009 ZZ 903 008580 0x25A,0x38

#: 124 REL E8030-66402 ZZ-36196 014 846 E8030-66620 009 ZZ 903 008580 0x358

#: 1241 CHMO E8030-66423 ZZ-515324 005 849 E8030-66620 009 ZZ 903 008580 n/a

#@ DCS-DPS128 in CC1 slot 8, Control-Segment-Slot-Nr 107

#: 304 CHBD E8023-66401 ZZ-18430 009 942 E8023-66610 019 ZZ 945 009557 0x21

#: 304 REL E8023-66402 ZZ-19290 013 942 E8023-66610 019 ZZ 945 009557 0x19

#: 3041 CHMO E8023-66403 ZZ-91590 017 942 E8023-66610 019 ZZ 945 009557 0x54

#: 736 CHBD E8023-66401 ZZ-18430 009 942 E8023-66610 019 ZZ 945 009557 0x21

#: 736 REL E8023-66402 ZZ-19290 013 942 E8023-66610 019 ZZ 945 009557 0x19

#: 7361 CHMO E8023-66403 ZZ-90712 017 938 E8023-66610 019 ZZ 945 009557 0x54

#: 308 CHBD E8023-66401 ZZ-18430 009 942 E8023-66610 019 ZZ 945 009557 0x21

#: 308 REL E8023-66402 ZZ-19290 013 942 E8023-66610 019 ZZ 945 009557 0x19

#: 3081 CHMO E8023-66403 ZZ-90460 017 938 E8023-66610 019 ZZ 945 009557 0x54

#: 740 CHBD E8023-66401 ZZ-18430 009 942 E8023-66610 019 ZZ 945 009557 0x21

#: 740 REL E8023-66402 ZZ-19290 013 942 E8023-66610 019 ZZ 945 009557 0x19

#: 7401 CHMO E8023-66403 ZZ-91627 017 942 E8023-66610 019 ZZ 945 009557 0x54

#@ DCS-DPS128 in CC1 slot 9, Control-Segment-Slot-Nr 108

#: 345 CHBD E8023-66401 ZZ-15840 008 922 E8023-66610 018 ZZ 927 008241 0x21

#: 345 REL E8023-66402 ZZ-15306 010 916 E8023-66610 018 ZZ 927 008241 0x19

#: 3451 CHMO E8023-66403 ZZ-77444 017 920 E8023-66610 018 ZZ 927 008241 0x54

#: 346 CHBD E8023-66401 ZZ-15840 008 922 E8023-66610 018 ZZ 927 008241 0x21

#: 346 REL E8023-66402 ZZ-15306 010 916 E8023-66610 018 ZZ 927 008241 0x19

#: 3461 CHMO E8023-66403 ZZ-80397 017 924 E8023-66610 018 ZZ 927 008241 0x54

#: 457 CHBD E8023-66401 ZZ-15840 008 922 E8023-66610 018 ZZ 927 008241 0x21

#: 457 REL E8023-66402 ZZ-15306 010 916 E8023-66610 018 ZZ 927 008241 0x19

#: 4571 CHMO E8023-66403 ZZ-81587 017 925 E8023-66610 018 ZZ 927 008241 0x54

#: 458 CHBD E8023-66401 ZZ-15840 008 922 E8023-66610 018 ZZ 927 008241 0x21

#: 458 REL E8023-66402 ZZ-15306 010 916 E8023-66610 018 ZZ 927 008241 0x19

#: 4581 CHMO E8023-66403 ZZ-82279 017 926 E8023-66610 018 ZZ 927 008241 0x54

#@ PS1600 board in CC2 slot 2, Control-Segment-Slot-Nr 201

#: 209 CHBD E8030-66401 ZZ-34917 015 840 E8030-66620 009 ZZ 844 007597 0x25A,0x38

#: 209 REL E8030-66402 ZZ-34439 014 834 E8030-66620 009 ZZ 844 007597 0x358

#: 2091 CHMO E8030-66423 ZZ-512493 005 843 E8030-66620 009 ZZ 844 007597 n/a

#: 210 CHBD E8030-66401 ZZ-34917 015 840 E8030-66620 009 ZZ 844 007597 0x25A,0x38

#: 210 REL E8030-66402 ZZ-34439 014 834 E8030-66620 009 ZZ 844 007597 0x358

#: 2101 CHMO E8030-66423 ZZ-54450 005 843 E8030-66620 009 ZZ 844 007597 n/a

#: 211 CHBD E8030-66401 ZZ-34917 015 840 E8030-66620 009 ZZ 844 007597 0x25A,0x38

#: 211 REL E8030-66402 ZZ-34439 014 834 E8030-66620 009 ZZ 844 007597 0x358

#: 2111 CHMO E8030-66423 ZZ-54322 005 843 E8030-66620 009 ZZ 844 007597 n/a

#: 212 CHBD E8030-66401 ZZ-34917 015 840 E8030-66620 009 ZZ 844 007597 0x25A,0x38

#: 212 REL E8030-66402 ZZ-34439 014 834 E8030-66620 009 ZZ 844 007597 0x358

#: 2121 CHMO E8030-66423 ZZ-54455 005 843 E8030-66620 009 ZZ 844 007597 n/a

#: 213 CHBD E8030-66401 ZZ-34917 015 840 E8030-66620 009 ZZ 844 007597 0x25A,0x38

#: 213 REL E8030-66402 ZZ-34439 014 834 E8030-66620 009 ZZ 844 007597 0x358

#: 2131 CHMO E8030-66423 ZZ-511875 005 839 E8030-66620 009 ZZ 844 007597 n/a

#: 214 CHBD E8030-66401 ZZ-34917 015 840 E8030-66620 009 ZZ 844 007597 0x25A,0x38

#: 214 REL E8030-66402 ZZ-34439 014 834 E8030-66620 009 ZZ 844 007597 0x358

#: 2141 CHMO E8030-66423 ZZ-512530 005 843 E8030-66620 009 ZZ 844 007597 n/a

#: 215 CHBD E8030-66401 ZZ-34917 015 840 E8030-66620 009 ZZ 844 007597 0x25A,0x38

#: 215 REL E8030-66402 ZZ-34439 014 834 E8030-66620 009 ZZ 844 007597 0x358

#: 2151 CHMO E8030-66423 ZZ-54453 005 843 E8030-66620 009 ZZ 844 007597 n/a

#: 216 CHBD E8030-66401 ZZ-34917 015 840 E8030-66620 009 ZZ 844 007597 0x25A,0x38

#: 216 REL E8030-66402 ZZ-34439 014 834 E8030-66620 009 ZZ 844 007597 0x358

#: 2161 CHMO E8030-66423 ZZ-54451 005 843 E8030-66620 009 ZZ 844 007597 n/a

#@ PS1600 board in CC2 slot 3, Control-Segment-Slot-Nr 202

#: 409 CHBD E8030-66401 ZZ-35651 015 846 E8030-66620 009 ZZ 850 008280 0x25A,0x38

#: 409 REL E8030-66402 ZZ-35891 014 844 E8030-66620 009 ZZ 850 008280 0x358

#: 4091 CHMO E8030-66423 ZZ-58136 005 849 E8030-66620 009 ZZ 850 008280 n/a

#: 410 CHBD E8030-66401 ZZ-35651 015 846 E8030-66620 009 ZZ 850 008280 0x25A,0x38

#: 410 REL E8030-66402 ZZ-35891 014 844 E8030-66620 009 ZZ 850 008280 0x358

#: 4101 CHMO E8030-66423 ZZ-58138 005 849 E8030-66620 009 ZZ 850 008280 n/a

#: 411 CHBD E8030-66401 ZZ-35651 015 846 E8030-66620 009 ZZ 850 008280 0x25A,0x38

#: 411 REL E8030-66402 ZZ-35891 014 844 E8030-66620 009 ZZ 850 008280 0x358

#: 4111 CHMO E8030-66423 ZZ-513718 005 845 E8030-66620 009 ZZ 850 008280 n/a

#: 412 CHBD E8030-66401 ZZ-35651 015 846 E8030-66620 009 ZZ 850 008280 0x25A,0x38

#: 412 REL E8030-66402 ZZ-35891 014 844 E8030-66620 009 ZZ 850 008280 0x358

#: 4121 CHMO E8030-66423 ZZ-58141 005 849 E8030-66620 009 ZZ 850 008280 n/a

#: 413 CHBD E8030-66401 ZZ-35651 015 846 E8030-66620 009 ZZ 850 008280 0x25A,0x38

#: 413 REL E8030-66402 ZZ-35891 014 844 E8030-66620 009 ZZ 850 008280 0x358

#: 4131 CHMO E8030-66423 ZZ-513509 005 845 E8030-66620 009 ZZ 850 008280 n/a

#: 414 CHBD E8030-66401 ZZ-35651 015 846 E8030-66620 009 ZZ 850 008280 0x25A,0x38

#: 414 REL E8030-66402 ZZ-35891 014 844 E8030-66620 009 ZZ 850 008280 0x358

#: 4141 CHMO E8030-66423 ZZ-58137 005 849 E8030-66620 009 ZZ 850 008280 n/a

#: 415 CHBD E8030-66401 ZZ-35651 015 846 E8030-66620 009 ZZ 850 008280 0x25A,0x38

#: 415 REL E8030-66402 ZZ-35891 014 844 E8030-66620 009 ZZ 850 008280 0x358

#: 4151 CHMO E8030-66423 ZZ-58143 005 849 E8030-66620 009 ZZ 850 008280 n/a

#: 416 CHBD E8030-66401 ZZ-35651 015 846 E8030-66620 009 ZZ 850 008280 0x25A,0x38

#: 416 REL E8030-66402 ZZ-35891 014 844 E8030-66620 009 ZZ 850 008280 0x358

#: 4161 CHMO E8030-66423 ZZ-58139 005 849 E8030-66620 009 ZZ 850 008280 n/a

#@ PS1600 board in CC2 slot 4, Control-Segment-Slot-Nr 203

#: 109 CHBD E8030-66401 ZZ-31721 015 818 E8030-66620 009 ZZ 822 005218 0x25A,0x38

#: 109 REL E8030-66402 ZZ-31360 014 815 E8030-66620 009 ZZ 822 005218 0x358

#: 1091 CHMO E8030-66423 ZZ-39642 005 815 E8030-66620 009 ZZ 822 005218 n/a

#: 110 CHBD E8030-66401 ZZ-31721 015 818 E8030-66620 009 ZZ 822 005218 0x25A,0x38

#: 110 REL E8030-66402 ZZ-31360 014 815 E8030-66620 009 ZZ 822 005218 0x358

#: 1101 CHMO E8030-66423 ZZ-40797 005 817 E8030-66620 009 ZZ 822 005218 n/a

#: 111 CHBD E8030-66401 ZZ-31721 015 818 E8030-66620 009 ZZ 822 005218 0x25A,0x38

#: 111 REL E8030-66402 ZZ-31360 014 815 E8030-66620 009 ZZ 822 005218 0x358

#: 1111 CHMO E8030-66423 ZZ-39380 005 815 E8030-66620 009 ZZ 822 005218 n/a

#: 112 CHBD E8030-66401 ZZ-31721 015 818 E8030-66620 009 ZZ 822 005218 0x25A,0x38

#: 112 REL E8030-66402 ZZ-31360 014 815 E8030-66620 009 ZZ 822 005218 0x358

#: 1121 CHMO E8030-66423 ZZ-41046 005 818 E8030-66620 009 ZZ 822 005218 n/a

#: 113 CHBD E8030-66401 ZZ-31721 015 818 E8030-66620 009 ZZ 822 005218 0x25A,0x38

#: 113 REL E8030-66402 ZZ-31360 014 815 E8030-66620 009 ZZ 822 005218 0x358

#: 1131 CHMO E8030-66423 ZZ-39448 005 815 E8030-66620 009 ZZ 822 005218 n/a

#: 114 CHBD E8030-66401 ZZ-31721 015 818 E8030-66620 009 ZZ 822 005218 0x25A,0x38

#: 114 REL E8030-66402 ZZ-31360 014 815 E8030-66620 009 ZZ 822 005218 0x358

#: 1141 CHMO E8030-66423 ZZ-503972 005 817 E8030-66620 009 ZZ 822 005218 n/a

#: 115 CHBD E8030-66401 ZZ-31721 015 818 E8030-66620 009 ZZ 822 005218 0x25A,0x38

#: 115 REL E8030-66402 ZZ-31360 014 815 E8030-66620 009 ZZ 822 005218 0x358

#: 1151 CHMO E8030-66423 ZZ-38658 005 814 E8030-66620 009 ZZ 822 005218 n/a

#: 116 CHBD E8030-66401 ZZ-31721 015 818 E8030-66620 009 ZZ 822 005218 0x25A,0x38

#: 116 REL E8030-66402 ZZ-31360 014 815 E8030-66620 009 ZZ 822 005218 0x358

#: 1161 CHMO E8030-66423 ZZ-40796 005 817 E8030-66620 009 ZZ 822 005218 n/a

#@ DCS-DPS128 in CC2 slot 9, Control-Segment-Slot-Nr 208

#: 461 CHBD E8023-66401 ZZ-18525 009 942 E8023-66610 019 ZZ 945 009496 0x21

#: 461 REL E8023-66402 ZZ-18930 013 942 E8023-66610 019 ZZ 945 009496 0x19

#: 4611 CHMO E8023-66403 ZZ-82860 017 927 E8023-66610 019 ZZ 945 009496 0x54

#: 462 CHBD E8023-66401 ZZ-18525 009 942 E8023-66610 019 ZZ 945 009496 0x21

#: 462 REL E8023-66402 ZZ-18930 013 942 E8023-66610 019 ZZ 945 009496 0x19

#: 4621 CHMO E8023-66403 ZZ-83577 017 928 E8023-66610 019 ZZ 945 009496 0x54

#: 343 CHBD E8023-66401 ZZ-18525 009 942 E8023-66610 019 ZZ 945 009496 0x21

#: 343 REL E8023-66402 ZZ-18930 013 942 E8023-66610 019 ZZ 945 009496 0x19

#: 3431 CHMO E8023-66403 ZZ-83932 017 929 E8023-66610 019 ZZ 945 009496 0x54

#: 344 CHBD E8023-66401 ZZ-18525 009 942 E8023-66610 019 ZZ 945 009496 0x21

#: 344 REL E8023-66402 ZZ-18930 013 942 E8023-66610 019 ZZ 945 009496 0x19

#: 3441 CHMO E8023-66403 ZZ-83558 017 928 E8023-66610 019 ZZ 945 009496 0x54

#@ PS1600 board in CC3 slot 2, Control-Segment-Slot-Nr 110

#: 101 CHBD E8030-66401 ZZ-41262 016 926 E8030-66620 010 ZZ 928 013050 0x25A,0x38

#: 101 REL E8030-66402 ZZ-41420 015 924 E8030-66620 010 ZZ 928 013050 0x358

#: 1011 CHMO E8030-66423 ZZ-527192 005 918 E8030-66620 010 ZZ 928 013050 n/a

#: 102 CHBD E8030-66401 ZZ-41262 016 926 E8030-66620 010 ZZ 928 013050 0x25A,0x38

#: 102 REL E8030-66402 ZZ-41420 015 924 E8030-66620 010 ZZ 928 013050 0x358

#: 1021 CHMO E8030-66423 ZZ-76630 005 925 E8030-66620 010 ZZ 928 013050 n/a

#: 103 CHBD E8030-66401 ZZ-41262 016 926 E8030-66620 010 ZZ 928 013050 0x25A,0x38

#: 103 REL E8030-66402 ZZ-41420 015 924 E8030-66620 010 ZZ 928 013050 0x358

#: 1031 CHMO E8030-66423 ZZ-535203 005 927 E8030-66620 010 ZZ 928 013050 n/a

#: 104 CHBD E8030-66401 ZZ-41262 016 926 E8030-66620 010 ZZ 928 013050 0x25A,0x38

#: 104 REL E8030-66402 ZZ-41420 015 924 E8030-66620 010 ZZ 928 013050 0x358

#: 1041 CHMO E8030-66423 ZZ-76533 005 925 E8030-66620 010 ZZ 928 013050 n/a

#: 105 CHBD E8030-66401 ZZ-41262 016 926 E8030-66620 010 ZZ 928 013050 0x25A,0x38

#: 105 REL E8030-66402 ZZ-41420 015 924 E8030-66620 010 ZZ 928 013050 0x358

#: 1051 CHMO E8030-66423 ZZ-527836 005 920 E8030-66620 010 ZZ 928 013050 n/a

#: 106 CHBD E8030-66401 ZZ-41262 016 926 E8030-66620 010 ZZ 928 013050 0x25A,0x38

#: 106 REL E8030-66402 ZZ-41420 015 924 E8030-66620 010 ZZ 928 013050 0x358

#: 1061 CHMO E8030-66423 ZZ-76675 005 925 E8030-66620 010 ZZ 928 013050 n/a

#: 107 CHBD E8030-66401 ZZ-41262 016 926 E8030-66620 010 ZZ 928 013050 0x25A,0x38

#: 107 REL E8030-66402 ZZ-41420 015 924 E8030-66620 010 ZZ 928 013050 0x358

#: 1071 CHMO E8030-66423 ZZ-530632 005 922 E8030-66620 010 ZZ 928 013050 n/a

#: 108 CHBD E8030-66401 ZZ-41262 016 926 E8030-66620 010 ZZ 928 013050 0x25A,0x38

#: 108 REL E8030-66402 ZZ-41420 015 924 E8030-66620 010 ZZ 928 013050 0x358

#: 1081 CHMO E8030-66423 ZZ-76534 005 925 E8030-66620 010 ZZ 928 013050 n/a

#@ DCS-DPS128 in CC3 slot 4, Control-Segment-Slot-Nr 112

#: 319 CHBD E8023-66401 ZZ-17631 009 934 E8023-66600 019 ZZ 937 005527 0x21

#: 319 REL E8023-66402 ZZ-18183 013 934 E8023-66600 019 ZZ 937 005527 0x19

#: 3191 CHMO E8023-66403 ZZ-86835 017 932 E8023-66600 019 ZZ 937 005527 0x54

#: 323 CHBD E8023-66401 ZZ-17631 009 934 E8023-66600 019 ZZ 937 005527 0x21

#: 323 REL E8023-66402 ZZ-18183 013 934 E8023-66600 019 ZZ 937 005527 0x19

#: 3231 CHMO E8023-66403 ZZ-86857 017 932 E8023-66600 019 ZZ 937 005527 0x54

#: 320 CHBD E8023-66401 ZZ-17631 009 934 E8023-66600 019 ZZ 937 005527 0x21

#: 320 REL E8023-66402 ZZ-18183 013 934 E8023-66600 019 ZZ 937 005527 0x19

#: 3201 CHMO E8023-66403 ZZ-86870 017 932 E8023-66600 019 ZZ 937 005527 0x54

#: 324 CHBD E8023-66401 ZZ-17631 009 934 E8023-66600 019 ZZ 937 005527 0x21

#: 324 REL E8023-66402 ZZ-18183 013 934 E8023-66600 019 ZZ 937 005527 0x19

#: 3241 CHMO E8023-66403 ZZ-86862 017 932 E8023-66600 019 ZZ 937 005527 0x54

#: 311 CHBD E8023-66401 ZZ-17631 009 934 E8023-66600 019 ZZ 937 005527 0x21

#: 311 REL E8023-66402 ZZ-18183 013 934 E8023-66600 019 ZZ 937 005527 0x19

#: 3111 CHMO E8023-66403 ZZ-86882 017 932 E8023-66600 019 ZZ 937 005527 0x54

#: 315 CHBD E8023-66401 ZZ-17631 009 934 E8023-66600 019 ZZ 937 005527 0x21

#: 315 REL E8023-66402 ZZ-18183 013 934 E8023-66600 019 ZZ 937 005527 0x19

#: 3151 CHMO E8023-66403 ZZ-86797 017 932 E8023-66600 019 ZZ 937 005527 0x54

#: 312 CHBD E8023-66401 ZZ-17631 009 934 E8023-66600 019 ZZ 937 005527 0x21

#: 312 REL E8023-66402 ZZ-18183 013 934 E8023-66600 019 ZZ 937 005527 0x19

#: 3121 CHMO E8023-66403 ZZ-86852 017 932 E8023-66600 019 ZZ 937 005527 0x54

#: 316 CHBD E8023-66401 ZZ-17631 009 934 E8023-66600 019 ZZ 937 005527 0x21

#: 316 REL E8023-66402 ZZ-18183 013 934 E8023-66600 019 ZZ 937 005527 0x19

#: 3161 CHMO E8023-66403 ZZ-86830 017 932 E8023-66600 019 ZZ 937 005527 0x54

#@ DCS-DPS128 in CC3 slot 5, Control-Segment-Slot-Nr 113

#: 459 CHBD E8023-66401 ZZ-18456 009 942 E8023-66610 019 ZZ 945 009501 0x21

#: 459 REL E8023-66402 ZZ-19216 013 942 E8023-66610 019 ZZ 945 009501 0x19

#: 4591 CHMO E8023-66403 ZZ-87085 017 934 E8023-66610 019 ZZ 945 009501 0x54

#: 460 CHBD E8023-66401 ZZ-18456 009 942 E8023-66610 019 ZZ 945 009501 0x21

#: 460 REL E8023-66402 ZZ-19216 013 942 E8023-66610 019 ZZ 945 009501 0x19

#: 4601 CHMO E8023-66403 ZZ-88222 017 934 E8023-66610 019 ZZ 945 009501 0x54

#: 341 CHBD E8023-66401 ZZ-18456 009 942 E8023-66610 019 ZZ 945 009501 0x21

#: 341 REL E8023-66402 ZZ-19216 013 942 E8023-66610 019 ZZ 945 009501 0x19

#: 3411 CHMO E8023-66403 ZZ-79190 017 921 E8023-66610 019 ZZ 945 009501 0x54

#: 342 CHBD E8023-66401 ZZ-18456 009 942 E8023-66610 019 ZZ 945 009501 0x21

#: 342 REL E8023-66402 ZZ-19216 013 942 E8023-66610 019 ZZ 945 009501 0x19

#: 3421 CHMO E8023-66403 ZZ-84783 017 930 E8023-66610 019 ZZ 945 009501 0x54

#@ PS1600 board in CC3 slot 6, Control-Segment-Slot-Nr 114

#: 201 CHBD E8030-66401 ZZ-35114 015 842 E8030-66620 009 ZZ 844 007714 0x25A,0x38

#: 201 REL E8030-66402 ZZ-35102 014 839 E8030-66620 009 ZZ 844 007714 0x358

#: 2011 CHMO E8030-66423 ZZ-54244 005 843 E8030-66620 009 ZZ 844 007714 n/a

#: 202 CHBD E8030-66401 ZZ-35114 015 842 E8030-66620 009 ZZ 844 007714 0x25A,0x38

#: 202 REL E8030-66402 ZZ-35102 014 839 E8030-66620 009 ZZ 844 007714 0x358

#: 2021 CHMO E8030-66423 ZZ-54280 005 843 E8030-66620 009 ZZ 844 007714 n/a

#: 203 CHBD E8030-66401 ZZ-35114 015 842 E8030-66620 009 ZZ 844 007714 0x25A,0x38

#: 203 REL E8030-66402 ZZ-35102 014 839 E8030-66620 009 ZZ 844 007714 0x358

#: 2031 CHMO E8030-66423 ZZ-54266 005 843 E8030-66620 009 ZZ 844 007714 n/a

#: 204 CHBD E8030-66401 ZZ-35114 015 842 E8030-66620 009 ZZ 844 007714 0x25A,0x38

#: 204 REL E8030-66402 ZZ-35102 014 839 E8030-66620 009 ZZ 844 007714 0x358

#: 2041 CHMO E8030-66423 ZZ-54278 005 843 E8030-66620 009 ZZ 844 007714 n/a

#: 205 CHBD E8030-66401 ZZ-35114 015 842 E8030-66620 009 ZZ 844 007714 0x25A,0x38

#: 205 REL E8030-66402 ZZ-35102 014 839 E8030-66620 009 ZZ 844 007714 0x358

#: 2051 CHMO E8030-66423 ZZ-54250 005 843 E8030-66620 009 ZZ 844 007714 n/a

#: 206 CHBD E8030-66401 ZZ-35114 015 842 E8030-66620 009 ZZ 844 007714 0x25A,0x38

#: 206 REL E8030-66402 ZZ-35102 014 839 E8030-66620 009 ZZ 844 007714 0x358

#: 2061 CHMO E8030-66423 ZZ-54282 005 843 E8030-66620 009 ZZ 844 007714 n/a

#: 207 CHBD E8030-66401 ZZ-35114 015 842 E8030-66620 009 ZZ 844 007714 0x25A,0x38

#: 207 REL E8030-66402 ZZ-35102 014 839 E8030-66620 009 ZZ 844 007714 0x358

#: 2071 CHMO E8030-66423 ZZ-54258 005 843 E8030-66620 009 ZZ 844 007714 n/a

#: 208 CHBD E8030-66401 ZZ-35114 015 842 E8030-66620 009 ZZ 844 007714 0x25A,0x38

#: 208 REL E8030-66402 ZZ-35102 014 839 E8030-66620 009 ZZ 844 007714 0x358

#: 2081 CHMO E8030-66423 ZZ-54793 005 844 E8030-66620 009 ZZ 844 007714 n/a

#@ DCS-DPS128 in CC3 slot 8, Control-Segment-Slot-Nr 116

#: 317 CHBD E8023-66401 ZZ-17754 009 935 E8023-66600 019 ZZ 939 005620 0x21

#: 317 REL E8023-66402 ZZ-18334 013 935 E8023-66600 019 ZZ 939 005620 0x19

#: 3171 CHMO E8023-66403 ZZ-86395 017 932 E8023-66600 019 ZZ 939 005620 0x54

#: 321 CHBD E8023-66401 ZZ-17754 009 935 E8023-66600 019 ZZ 939 005620 0x21

#: 321 REL E8023-66402 ZZ-18334 013 935 E8023-66600 019 ZZ 939 005620 0x19

#: 3211 CHMO E8023-66403 ZZ-86336 017 932 E8023-66600 019 ZZ 939 005620 0x54

#: 302 CHBD E8023-66401 ZZ-17754 009 935 E8023-66600 019 ZZ 939 005620 0x21

#: 302 REL E8023-66402 ZZ-18334 013 935 E8023-66600 019 ZZ 939 005620 0x19

#: 3021 CHMO E8023-66403 ZZ-87156 017 934 E8023-66600 019 ZZ 939 005620 0x54

#: 306 CHBD E8023-66401 ZZ-17754 009 935 E8023-66600 019 ZZ 939 005620 0x21

#: 306 REL E8023-66402 ZZ-18334 013 935 E8023-66600 019 ZZ 939 005620 0x19

#: 3061 CHMO E8023-66403 ZZ-87052 017 934 E8023-66600 019 ZZ 939 005620 0x54

#: 318 CHBD E8023-66401 ZZ-17754 009 935 E8023-66600 019 ZZ 939 005620 0x21

#: 318 REL E8023-66402 ZZ-18334 013 935 E8023-66600 019 ZZ 939 005620 0x19

#: 3181 CHMO E8023-66403 ZZ-86334 017 932 E8023-66600 019 ZZ 939 005620 0x54

#: 322 CHBD E8023-66401 ZZ-17754 009 935 E8023-66600 019 ZZ 939 005620 0x21

#: 322 REL E8023-66402 ZZ-18334 013 935 E8023-66600 019 ZZ 939 005620 0x19

#: 3221 CHMO E8023-66403 ZZ-86339 017 932 E8023-66600 019 ZZ 939 005620 0x54

#: 303 CHBD E8023-66401 ZZ-17754 009 935 E8023-66600 019 ZZ 939 005620 0x21

#: 303 REL E8023-66402 ZZ-18334 013 935 E8023-66600 019 ZZ 939 005620 0x19

#: 3031 CHMO E8023-66403 ZZ-86417 017 932 E8023-66600 019 ZZ 939 005620 0x54

#: 307 CHBD E8023-66401 ZZ-17754 009 935 E8023-66600 019 ZZ 939 005620 0x21

#: 307 REL E8023-66402 ZZ-18334 013 935 E8023-66600 019 ZZ 939 005620 0x19

#: 3071 CHMO E8023-66403 ZZ-86680 017 932 E8023-66600 019 ZZ 939 005620 0x54

#@ MCE board in CC4 slot 2, Control-Segment-Slot-Nr 210

#: 2250 MCE E9722-66411 JP-5114 006 708 E9733-66610 012 JP 707 001744 0x10

#: 2251 MESE E9732-66413 JP-2532 004 703 E9733-66610 012 JP 707 001744 0x10

#: 2252 MESE E9732-66413 JP-2585 004 705 E9733-66610 012 JP 707 001744 0x10

#: 2253 MESE E9732-66413 JP-2532 004 703 E9733-66610 012 JP 707 001744 0x10

#: 2254 MESE E9732-66413 JP-2585 004 705 E9733-66610 012 JP 707 001744 0x10

#: 2255 MESE E9732-66413 JP-2512 004 703 E9733-66610 012 JP 707 001744 0x10

#: 2256 MESE E9732-66413 JP-2587 004 705 E9733-66610 012 JP 707 001744 0x10

#: 2257 MESE E9732-66413 JP-2512 004 703 E9733-66610 012 JP 707 001744 0x10

#: 2258 MESE E9732-66413 JP-2587 004 705 E9733-66610 012 JP 707 001744 0x10

#@ MCE board in CC4 slot 3, Control-Segment-Slot-Nr 211

#: 2260 MCE E9722-66411 JP-2051 005 214 E9733-66610 004 JP 217 001280 0x10

#: 2261 MESE E9732-66403 JP-1276 011 212 E9733-66610 004 JP 217 001280 0x10

#: 2262 MESE E9732-66403 JP-1315 011 213 E9733-66610 004 JP 217 001280 0x10

#: 2263 MESE E9732-66403 JP-1276 011 212 E9733-66610 004 JP 217 001280 0x10

#: 2264 MESE E9732-66403 JP-1315 011 213 E9733-66610 004 JP 217 001280 0x10

#: 2265 MESE E9732-66403 JP-1329 011 213 E9733-66610 004 JP 217 001280 0x10

#: 2266 MESE E9732-66403 JP-1363 011 213 E9733-66610 004 JP 217 001280 0x10

#: 2267 MESE E9732-66403 JP-1329 011 213 E9733-66610 004 JP 217 001280 0x10

#: 2268 MESE E9732-66403 JP-1363 011 213 E9733-66610 004 JP 217 001280 0x10

#@ MCE board in CC4 slot 4, Control-Segment-Slot-Nr 212

#: 2270 MCE E9722-66411 JP-2408 005 248 E9733-66610 008 JP 128 000010 0x10

#: 2271 MESE E9732-66403 JP-220 016 114 E9733-66610 008 JP 128 000010 0x10

#: 2272 MESE E9732-66403 JP-165 016 113 E9733-66610 008 JP 128 000010 0x10

#: 2273 MESE E9732-66403 JP-220 016 114 E9733-66610 008 JP 128 000010 0x10

#: 2274 MESE E9732-66403 JP-165 016 113 E9733-66610 008 JP 128 000010 0x10

#: 2275 MESE E9732-66403 JP-317 016 116 E9733-66610 008 JP 128 000010 0x10

#: 2276 MESE E9732-66403 JP-390 016 117 E9733-66610 008 JP 128 000010 0x10

#: 2277 MESE E9732-66403 JP-317 016 116 E9733-66610 008 JP 128 000010 0x10

#: 2278 MESE E9732-66403 JP-390 016 117 E9733-66610 008 JP 128 000010 0x10

#@ MCE board in CC4 slot 6, Control-Segment-Slot-Nr 214

#: 2290 MCE E9722-66411 JP-2309 005 239 E9733-66610 007 JP 239 001406 0x10

#: 2291 MESE E9732-66403 JP-2872 015 237 E9733-66610 007 JP 239 001406 0x10

#: 2292 MESE E9732-66403 JP-2502 015 235 E9733-66610 007 JP 239 001406 0x10

#: 2293 MESE E9732-66403 JP-2872 015 237 E9733-66610 007 JP 239 001406 0x10

#: 2294 MESE E9732-66403 JP-2502 015 235 E9733-66610 007 JP 239 001406 0x10

#: 2295 MESE E9732-66403 JP-2904 015 237 E9733-66610 007 JP 239 001406 0x10

#: 2296 MESE E9732-66403 JP-2941 015 237 E9733-66610 007 JP 239 001406 0x10

#: 2297 MESE E9732-66403 JP-2904 015 237 E9733-66610 007 JP 239 001406 0x10

#: 2298 MESE E9732-66403 JP-2941 015 237 E9733-66610 007 JP 239 001406 0x10

#@ MCE board in CC4 slot 7, Control-Segment-Slot-Nr 215

#: 2300 MCE E9722-66411 JP-1792 005 141 E9733-66610 008 JP 143 001046 0x10

#: 2301 MESE E9732-66403 JP-672 016 134 E9733-66610 008 JP 143 001046 0x10

#: 2302 MESE E9732-66403 JP-661 016 134 E9733-66610 008 JP 143 001046 0x10

#: 2303 MESE E9732-66403 JP-672 016 134 E9733-66610 008 JP 143 001046 0x10

#: 2304 MESE E9732-66403 JP-661 016 134 E9733-66610 008 JP 143 001046 0x10

#: 2305 MESE E9732-66403 JP-644 016 134 E9733-66610 008 JP 143 001046 0x10

#: 2306 MESE E9732-66403 JP-711 016 134 E9733-66610 008 JP 143 001046 0x10

#: 2307 MESE E9732-66403 JP-644 016 134 E9733-66610 008 JP 143 001046 0x10

#: 2308 MESE E9732-66403 JP-711 016 134 E9733-66610 008 JP 143 001046 0x10

#@ MCE board in CC4 slot 8, Control-Segment-Slot-Nr 216

#: 2310 MCE E9722-66411 JP-2038 005 214 E9733-66610 008 JP 222 001332 0x10

#: 2311 MESE E9732-66403 JP-2022 016 219 E9733-66610 008 JP 222 001332 0x10

#: 2312 MESE E9732-66403 JP-2050 016 219 E9733-66610 008 JP 222 001332 0x10

#: 2313 MESE E9732-66403 JP-2022 016 219 E9733-66610 008 JP 222 001332 0x10

#: 2314 MESE E9732-66403 JP-2050 016 219 E9733-66610 008 JP 222 001332 0x10

#: 2315 MESE E9732-66403 JP-2059 016 219 E9733-66610 008 JP 222 001332 0x10

#: 2316 MESE E9732-66403 JP-2057 016 219 E9733-66610 008 JP 222 001332 0x10

#: 2317 MESE E9732-66403 JP-2059 016 219 E9733-66610 008 JP 222 001332 0x10

#: 2318 MESE E9732-66403 JP-2057 016 219 E9733-66610 008 JP 222 001332 0x10

#@ MCE board in CC4 slot 9, Control-Segment-Slot-Nr 217

#: 2320 MCE E9722-66411 JP-2022 005 214 E9733-66610 004 JP 217 001242 0x10

#: 2321 MESE E9732-66403 JP-1414 011 213 E9733-66610 004 JP 217 001242 0x10

#: 2322 MESE E9732-66403 JP-1301 011 213 E9733-66610 004 JP 217 001242 0x10

#: 2323 MESE E9732-66403 JP-1414 011 213 E9733-66610 004 JP 217 001242 0x10

#: 2324 MESE E9732-66403 JP-1301 011 213 E9733-66610 004 JP 217 001242 0x10

#: 2325 MESE E9732-66403 JP-1347 011 213 E9733-66610 004 JP 217 001242 0x10

#: 2326 MESE E9732-66403 JP-1371 011 213 E9733-66610 004 JP 217 001242 0x10

#: 2327 MESE E9732-66403 JP-1347 011 213 E9733-66610 004 JP 217 001242 0x10

#: 2328 MESE E9732-66403 JP-1371 011 213 E9733-66610 004 JP 217 001242 0x10

### Diagnostic Run 1 executed at: Fri Jan 15 15:32:39 2021

# Current period is 100.00ns, capable Hardware: CPIO/DPS128HC

T AACU (MCA/MCB/MCC...) (ALG): Analog ACU test

# Entering T AACU (MCA/MCB/MCC...): 15- Jan-2021 15:32:56.356

T60: analog\_acu\_test

passed

# Leaving T AACU (MCA/MCB/MCC...): 15- Jan-2021 15:32:58.945

T AMCA (MCA/MCB/MCC...) (ALG): Multi site baseband analog test

# Entering T AMCA (MCA/MCB/MCC...): 15- Jan-2021 15:32:59.950

T AMCA: MCE 2250: Unit\_FPGA\_configuration\_test\_Unit\_1: exp = passed: meas = passed # Test item 1- 1- 1- 1

T AMCA: MCE 2250: Unit\_FPGA\_configuration\_test\_Unit\_2: exp = passed: meas = passed # Test item 2- 1- 1- 1

T AMCA: MCE 2250: Unit\_FPGA\_configuration\_test\_Unit\_3: exp = passed: meas = passed # Test item 3- 1- 1- 1

T AMCA: MCE 2250: Unit\_FPGA\_configuration\_test\_Unit\_4: exp = passed: meas = passed # Test item 4- 1- 1- 1

T AMCA: MCE 2250: Unit\_FPGA\_configuration\_test\_Unit\_5: exp = passed: meas = passed # Test item 5- 1- 1- 1

T AMCA: MCE 2250: Unit\_FPGA\_configuration\_test\_Unit\_6: exp = passed: meas = passed # Test item 6- 1- 1- 1

T AMCA: MCE 2250: Unit\_FPGA\_configuration\_test\_Unit\_7: exp = passed: meas = passed # Test item 7- 1- 1- 1

T AMCA: MCE 2250: Unit\_FPGA\_configuration\_test\_Unit\_8: exp = passed: meas = passed # Test item 8- 1- 1- 1

T AMCA: MCE 2250: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Data\_Bus\_Test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 0- 1- 2- 1

T AMCA: MCE 2250: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Data\_Bus\_Test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 1- 2- 2

T AMCA: MCE 2250: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 1- 2- 3

T AMCA: MESE 2251: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 1- 8- 1

T AMCA: MESE 2252: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 1- 8- 1

T AMCA: MESE 2253: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 1- 8- 1

T AMCA: MESE 2254: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 1- 8- 1

T AMCA: MESE 2255: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 1- 8- 1

T AMCA: MESE 2256: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 1- 8- 1

T AMCA: MESE 2257: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 1- 8- 1

T AMCA: MESE 2258: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 1- 8- 1

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_DGND\_B\_(U10144): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.016 V, 3.3% # Test item 0- 1- 3- 1

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_(U10145): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.018 V, 3.6% # Test item 1- 1- 3- 2

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_IF\_(U10145): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.033 V, 6.5% # Test item 2- 1- 3- 3

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_(U811): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.005 V, 1.0% # Test item 3- 1- 3- 4

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.976 V, 4.3% # Test item 0- 1- 3- 5

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 0.998 V, 12.1% # Test item 1- 1- 3- 6

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_B: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.989 V, 8.9% # Test item 2- 1- 3- 7

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_B: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.018 V, 7.7% # Test item 3- 1- 3- 8

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5VD\_B: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.984 V, 24.7% # Test item 4- 1- 3- 9

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_B: V\_exp = 1.020 V, [0.918 ... 1.122 V] : V\_meas = 1.037 V, 16.4% # Test item 5- 1- 3-10

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_TEMP\_BUSIF: V\_exp = 0.750 V, [0.500 ... 1.000 V] : V\_meas = 0.634 V, 46.5% # Test item 6- 1- 3-11

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5.5VA: V\_exp = 1.050 V, [0.945 ... 1.155 V] : V\_meas = 1.055 V, 4.8% # Test item 7- 1- 3-12

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5.5VA: V\_exp = -1.140 V, [-1.254 ... -1.026 V] : V\_meas = -1.125 V, 13.2% # Test item 8- 1- 3-13

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+10VA: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 1.003 V, 3.0% # Test item 9- 1- 3-14

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.2VA\_IF: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 1.003 V, 3.0% # Test item 10- 1- 3-15

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U13: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.977 V, 13.4% # Test item 11- 1- 3-16

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U24: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.975 V, 15.4% # Test item 12- 1- 3-17

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U57: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.978 V, 12.4% # Test item 13- 1- 3-18

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U68: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.984 V, 6.3% # Test item 14- 1- 3-19

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U13: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.001 V, 10.9% # Test item 15- 1- 3-20

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U24: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.004 V, 13.9% # Test item 16- 1- 3-21

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U57: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.006 V, 15.9% # Test item 17- 1- 3-22

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U68: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.001 V, 10.9% # Test item 18- 1- 3-23

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_F\_TG: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 0.999 V, 39.4% # Test item 19- 1- 3-24

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.5VD\_F\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.987 V, 13.0% # Test item 20- 1- 3-25

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_F: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.007 V, 31.7% # Test item 21- 1- 3-26

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.5VD\_F: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.979 V, 21.0% # Test item 22- 1- 3-27

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5VD\_F: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.956 V, 4.2% # Test item 23- 1- 3-28

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5VA\_F: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.958 V, 2.3% # Test item 24- 1- 3-29

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5VA\_F: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.967 V, 7.6% # Test item 25- 1- 3-30

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VA\_F: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.013 V, 26.2% # Test item 26- 1- 3-31

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.969 V, 11.2% # Test item 27- 1- 3-32

T AMCA: MCE 2250: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_IF\_IF2: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.972 V, 28.0% # Test item 28- 1- 3-33

T AMCA: MCE 2250: Unit\_bus\_data\_line\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 0- 1- 4- 1

T AMCA: MCE 2250: Unit\_bus\_data\_line\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 1- 4- 2

T AMCA: MCE 2250: Unit\_bus\_address/control\_line\_test\_WE\_line\_and\_bit\_0\_of\_addr.\_bus: Reg\_exp = 0x00030002: Reg\_meas = 0x00030002 # Test item 0- 1- 5- 1

T AMCA: MCE 2250: Unit\_bus\_address/control\_line\_test\_OE\_line\_\_unit\_select\_line\_for\_unit\_1--4\_and\_bit\_1\_of\_addr.\_bus: Reg\_exp = 0x0002F004: Reg\_meas = 0x0002F004 # Test item 1- 1- 5- 2

T AMCA: MCE 2250: Unit\_bus\_address/control\_line\_test\_Unit\_select\_line\_for\_unit\_5--8\_and\_bit\_2\_of\_addr.\_bus: Reg\_exp = 0x00020F08: Reg\_meas = 0x00020F08 # Test item 2- 1- 5- 3

T AMCA: MCE 2250: Unit\_bus\_address/control\_line\_test\_Reset: Reg\_exp = 0x00060000: Reg\_meas = 0x00060000 # Test item 3- 1- 5- 4

T AMCA: MCE 2250: Analog\_interface\_module\_EEPROM\_access\_test\_Analog\_Interface\_Module: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 0- 1- 6- 1

T AMCA: MCE 2250: Front\_module\_serial\_bus\_test\_1k\_Ohm\_Rref\_connect\_relay\_on\_test: Reg\_exp = 0x00000003: Reg\_meas = 0x00000003 # Test item 0- 1- 7- 1

T AMCA: MCE 2250: Temperature\_sensor\_test\_START: V\_exp = 0.912 V, [0.450 ... 1.375 V] : V\_meas = 0.747 V, 35.8% >> degree = 31.880degree # Test item 0- 2- 3- 1

T AMCA: MCE 2250: MCLK\_test\_START: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.026 V, 26.0% >> D\_MCLK\_DC = 0.932V, D\_MCLK\_DC\* = 0.958V # Test item 0- 2- 4- 1

T AMCA: MCE 2250: MCLK\_test\_STOP: V\_exp = 0.300 V, [0.200 ... 19.980 V] : V\_meas = 0.336 V, 0.2% >> D\_MCLK\_DC = 0.757V, D\_MCLK\_DC\* = 1.093V # Test item 0- 2- 4- 2

T AMCA: MCE 2250: DC\_rail\_path\_test: R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1000.833 Ohm, 0.8% # Test item 0- 2- 8- 1

T AMCA: MCE 2250: DC\_rail\_path\_test\_GND\_connection\_switch\_On: R\_exp = 5.000 Ohm, [0.000 ... 10.000 Ohm] : R\_meas = 0.833 Ohm, 83.3% # Test item 0- 2- 8- 2

T AMCA: MESE 2251: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 3- 1- 1

T AMCA: MESE 2252: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 3- 1- 1

T AMCA: MESE 2253: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 3- 1- 1

T AMCA: MESE 2254: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 3- 1- 1

T AMCA: MESE 2255: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 3- 1- 1

T AMCA: MESE 2256: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 3- 1- 1

T AMCA: MESE 2257: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 3- 1- 1

T AMCA: MESE 2258: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 3- 1- 1

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.994 V, 14.0% # Test item 1- 3- 2- 1

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.987 V, 6.9% # Test item 1- 3- 2- 2

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.036 V, 25.5% # Test item 1- 3- 2- 3

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.009 V, 1.0% # Test item 1- 3- 2- 4

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.980 V, 0.0% # Test item 1- 3- 2- 5

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.974 V, 26.0% # Test item 1- 3- 2- 6

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.978 V, 19.3% # Test item 1- 3- 2- 7

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.966 V, 5.7% # Test item 1- 3- 2- 8

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.016 V, 36.5% # Test item 2- 3- 2- 1

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.022 V, 42.6% # Test item 2- 3- 2- 2

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.055 V, 44.3% # Test item 2- 3- 2- 3

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.005 V, 5.0% # Test item 2- 3- 2- 4

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.967 V, 13.3% # Test item 2- 3- 2- 5

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.976 V, 24.0% # Test item 2- 3- 2- 6

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.970 V, 10.9% # Test item 2- 3- 2- 7

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.968 V, 7.8% # Test item 2- 3- 2- 8

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.007 V, 27.3% # Test item 3- 3- 2- 1

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.017 V, 37.5% # Test item 3- 3- 2- 2

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.052 V, 41.3% # Test item 3- 3- 2- 3

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.006 V, 4.0% # Test item 3- 3- 2- 4

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.970 V, 10.2% # Test item 3- 3- 2- 5

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.978 V, 22.0% # Test item 3- 3- 2- 6

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.968 V, 8.9% # Test item 3- 3- 2- 7

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.968 V, 7.8% # Test item 3- 3- 2- 8

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.997 V, 17.1% # Test item 4- 3- 2- 1

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.989 V, 8.9% # Test item 4- 3- 2- 2

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.036 V, 25.5% # Test item 4- 3- 2- 3

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.009 V, 1.0% # Test item 4- 3- 2- 4

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.974 V, 6.1% # Test item 4- 3- 2- 5

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.979 V, 21.0% # Test item 4- 3- 2- 6

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.967 V, 7.8% # Test item 4- 3- 2- 7

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.966 V, 5.7% # Test item 4- 3- 2- 8

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.022 V, 42.6% # Test item 5- 3- 2- 1

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.017 V, 37.5% # Test item 5- 3- 2- 2

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.051 V, 40.3% # Test item 5- 3- 2- 3

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.013 V, 3.0% # Test item 5- 3- 2- 4

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.971 V, 9.2% # Test item 5- 3- 2- 5

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.982 V, 18.0% # Test item 5- 3- 2- 6

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.971 V, 12.0% # Test item 5- 3- 2- 7

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.951 V, 9.9% # Test item 5- 3- 2- 8

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.011 V, 31.4% # Test item 6- 3- 2- 1

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.018 V, 38.5% # Test item 6- 3- 2- 2

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.065 V, 54.2% # Test item 6- 3- 2- 3

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.016 V, 5.9% # Test item 6- 3- 2- 4

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.980 V, 0.0% # Test item 6- 3- 2- 5

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.982 V, 18.0% # Test item 6- 3- 2- 6

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.970 V, 9.9% # Test item 6- 3- 2- 7

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.987 V, 27.6% # Test item 6- 3- 2- 8

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.018 V, 38.5% # Test item 7- 3- 2- 1

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.017 V, 37.5% # Test item 7- 3- 2- 2

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.065 V, 54.2% # Test item 7- 3- 2- 3

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.004 V, 5.9% # Test item 7- 3- 2- 4

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.976 V, 4.1% # Test item 7- 3- 2- 5

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.979 V, 21.0% # Test item 7- 3- 2- 6

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.978 V, 19.3% # Test item 7- 3- 2- 7

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.951 V, 9.9% # Test item 7- 3- 2- 8

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.019 V, 39.5% # Test item 8- 3- 2- 1

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.023 V, 43.6% # Test item 8- 3- 2- 2

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.058 V, 47.3% # Test item 8- 3- 2- 3

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.015 V, 5.0% # Test item 8- 3- 2- 4

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.982 V, 2.0% # Test item 8- 3- 2- 5

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.985 V, 15.0% # Test item 8- 3- 2- 6

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.980 V, 21.4% # Test item 8- 3- 2- 7

T AMCA: MCE 2250: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.953 V, 7.8% # Test item 8- 3- 2- 8

T AMCA: MESE 2251: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 3- 3- 1

T AMCA: MESE 2252: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 3- 3- 1

T AMCA: MESE 2253: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 3- 3- 1

T AMCA: MESE 2254: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 3- 3- 1

T AMCA: MESE 2255: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 3- 3- 1

T AMCA: MESE 2256: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 3- 3- 1

T AMCA: MESE 2257: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 3- 3- 1

T AMCA: MESE 2258: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 3- 3- 1

T AMCA: MESE 2251: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 1- 3- 4- 1

T AMCA: MESE 2251: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 3- 4- 2

T AMCA: MESE 2252: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 2- 3- 4- 1

T AMCA: MESE 2252: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 2- 3- 4- 2

T AMCA: MESE 2253: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 3- 3- 4- 1

T AMCA: MESE 2253: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 3- 3- 4- 2

T AMCA: MESE 2254: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 4- 3- 4- 1

T AMCA: MESE 2254: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 4- 3- 4- 2

T AMCA: MESE 2255: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 5- 3- 4- 1

T AMCA: MESE 2255: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 5- 3- 4- 2

T AMCA: MESE 2256: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 6- 3- 4- 1

T AMCA: MESE 2256: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 6- 3- 4- 2

T AMCA: MESE 2257: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 7- 3- 4- 1

T AMCA: MESE 2257: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 7- 3- 4- 2

T AMCA: MESE 2258: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 8- 3- 4- 1

T AMCA: MESE 2258: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 8- 3- 4- 2

T AMCA: MESE 2251: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 3- 5- 1

T AMCA: MESE 2252: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 3- 5- 1

T AMCA: MESE 2253: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 3- 5- 1

T AMCA: MESE 2254: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 3- 5- 1

T AMCA: MESE 2255: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 3- 5- 1

T AMCA: MESE 2256: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 3- 5- 1

T AMCA: MESE 2257: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 3- 5- 1

T AMCA: MESE 2258: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 3- 5- 1

T AMCA: MESE 2251: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 3- 5- 2

T AMCA: MESE 2252: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 3- 5- 2

T AMCA: MESE 2253: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 3- 5- 2

T AMCA: MESE 2254: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 3- 5- 2

T AMCA: MESE 2255: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 3- 5- 2

T AMCA: MESE 2256: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 3- 5- 2

T AMCA: MESE 2257: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 3- 5- 2

T AMCA: MESE 2258: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 3- 5- 2

T AMCA: MESE 2251: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 3- 5- 3

T AMCA: MESE 2252: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 3- 5- 3

T AMCA: MESE 2253: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 3- 5- 3

T AMCA: MESE 2254: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 3- 5- 3

T AMCA: MESE 2255: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 3- 5- 3

T AMCA: MESE 2256: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 3- 5- 3

T AMCA: MESE 2257: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 3- 5- 3

T AMCA: MESE 2258: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 3- 5- 3

T AMCA: MESE 2251: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 3- 5- 4

T AMCA: MESE 2252: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 3- 5- 4

T AMCA: MESE 2253: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 3- 5- 4

T AMCA: MESE 2254: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 3- 5- 4

T AMCA: MESE 2255: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 3- 5- 4

T AMCA: MESE 2256: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 3- 5- 4

T AMCA: MESE 2257: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 3- 5- 4

T AMCA: MESE 2258: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 3- 5- 4

T AMCA: MESE 2251: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 1- 3- 6- 1

T AMCA: MESE 2252: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 2- 3- 6- 1

T AMCA: MESE 2253: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 3- 3- 6- 1

T AMCA: MESE 2254: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 4- 3- 6- 1

T AMCA: MESE 2255: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 5- 3- 6- 1

T AMCA: MESE 2256: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 6- 3- 6- 1

T AMCA: MESE 2257: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 7- 3- 6- 1

T AMCA: MESE 2258: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 8- 3- 6- 1

T AMCA: MESE 2251: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 1- 3- 6- 2

T AMCA: MESE 2252: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 2- 3- 6- 2

T AMCA: MESE 2253: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 3- 3- 6- 2

T AMCA: MESE 2254: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 4- 3- 6- 2

T AMCA: MESE 2255: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 5- 3- 6- 2

T AMCA: MESE 2256: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 6- 3- 6- 2

T AMCA: MESE 2257: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 7- 3- 6- 2

T AMCA: MESE 2258: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 8- 3- 6- 2

T AMCA: MESE 2251: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 3- 7- 1

T AMCA: MESE 2252: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 3- 7- 1

T AMCA: MESE 2253: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 3- 7- 1

T AMCA: MESE 2254: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 3- 7- 1

T AMCA: MESE 2255: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 3- 7- 1

T AMCA: MESE 2256: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 3- 7- 1

T AMCA: MESE 2257: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 3- 7- 1

T AMCA: MESE 2258: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 3- 7- 1

T AMCA: MESE 2251: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 1-22- 1- 1

T AMCA: MESE 2251: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 1-22- 1- 2

T AMCA: MESE 2252: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 2-22- 1- 1

T AMCA: MESE 2252: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 2-22- 1- 2

T AMCA: MESE 2253: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 3-22- 1- 1

T AMCA: MESE 2253: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 3-22- 1- 2

T AMCA: MESE 2254: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 4-22- 1- 1

T AMCA: MESE 2254: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 4-22- 1- 2

T AMCA: MESE 2255: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 5-22- 1- 1

T AMCA: MESE 2255: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 5-22- 1- 2

T AMCA: MESE 2256: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 6-22- 1- 1

T AMCA: MESE 2256: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 6-22- 1- 2

T AMCA: MESE 2257: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 7-22- 1- 1

T AMCA: MESE 2257: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 7-22- 1- 2

T AMCA: MESE 2258: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 8-22- 1- 1

T AMCA: MESE 2258: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 8-22- 1- 2

T AMCA: MESE 2251: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.885 V, 5.3% # Test item 1- 4- 1- 1

T AMCA: MESE 2251: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.857 V, 4.4% # Test item 1- 4- 1- 2

T AMCA: MESE 2252: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.885 V, 5.4% # Test item 2- 4- 1- 1

T AMCA: MESE 2252: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.854 V, 5.6% # Test item 2- 4- 1- 2

T AMCA: MESE 2253: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.888 V, 6.1% # Test item 3- 4- 1- 1

T AMCA: MESE 2253: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.854 V, 5.5% # Test item 3- 4- 1- 2

T AMCA: MESE 2254: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.885 V, 5.4% # Test item 4- 4- 1- 1

T AMCA: MESE 2254: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.852 V, 6.4% # Test item 4- 4- 1- 2

T AMCA: MESE 2255: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.885 V, 5.1% # Test item 5- 4- 1- 1

T AMCA: MESE 2255: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.855 V, 5.4% # Test item 5- 4- 1- 2

T AMCA: MESE 2256: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.886 V, 5.7% # Test item 6- 4- 1- 1

T AMCA: MESE 2256: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.853 V, 5.9% # Test item 6- 4- 1- 2

T AMCA: MESE 2257: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.886 V, 5.6% # Test item 7- 4- 1- 1

T AMCA: MESE 2257: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.852 V, 6.2% # Test item 7- 4- 1- 2

T AMCA: MESE 2258: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.887 V, 5.9% # Test item 8- 4- 1- 1

T AMCA: MESE 2258: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.851 V, 6.8% # Test item 8- 4- 1- 2

T AMCA: MESE 2251: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.4% # Test item 1- 4- 2- 1

T AMCA: MESE 2251: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.016 V, 16.5% # Test item 1- 4- 2- 2

T AMCA: MESE 2251: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.980 V, 6.5% # Test item 1- 4- 2- 3

T AMCA: MESE 2252: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.5% # Test item 2- 4- 2- 1

T AMCA: MESE 2252: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.015 V, 15.2% # Test item 2- 4- 2- 2

T AMCA: MESE 2252: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.983 V, 5.6% # Test item 2- 4- 2- 3

T AMCA: MESE 2253: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.2% # Test item 3- 4- 2- 1

T AMCA: MESE 2253: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.017 V, 17.1% # Test item 3- 4- 2- 2

T AMCA: MESE 2253: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.982 V, 6.1% # Test item 3- 4- 2- 3

T AMCA: MESE 2254: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.4% # Test item 4- 4- 2- 1

T AMCA: MESE 2254: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.020 V, 20.3% # Test item 4- 4- 2- 2

T AMCA: MESE 2254: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.976 V, 8.1% # Test item 4- 4- 2- 3

T AMCA: MESE 2255: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.3% # Test item 5- 4- 2- 1

T AMCA: MESE 2255: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.017 V, 16.6% # Test item 5- 4- 2- 2

T AMCA: MESE 2255: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.980 V, 6.6% # Test item 5- 4- 2- 3

T AMCA: MESE 2256: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.1% # Test item 6- 4- 2- 1

T AMCA: MESE 2256: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.019 V, 19.1% # Test item 6- 4- 2- 2

T AMCA: MESE 2256: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.977 V, 7.8% # Test item 6- 4- 2- 3

T AMCA: MESE 2257: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.8% # Test item 7- 4- 2- 1

T AMCA: MESE 2257: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.019 V, 18.5% # Test item 7- 4- 2- 2

T AMCA: MESE 2257: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.978 V, 7.3% # Test item 7- 4- 2- 3

T AMCA: MESE 2251: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.877 mA, 41.0% # Test item 1- 4- 3- 1

T AMCA: MESE 2251: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.008 mA, 5.7% # Test item 1- 4- 3- 2

T AMCA: MESE 2251: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.850 mA, 33.3% # Test item 1- 4- 3- 3

T AMCA: MESE 2251: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.008 mA, 5.1% # Test item 1- 4- 3- 4

T AMCA: MESE 2252: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.881 mA, 39.5% # Test item 2- 4- 3- 1

T AMCA: MESE 2252: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.010 mA, 6.5% # Test item 2- 4- 3- 2

T AMCA: MESE 2252: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.849 mA, 33.5% # Test item 2- 4- 3- 3

T AMCA: MESE 2252: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.008 mA, 5.3% # Test item 2- 4- 3- 4

T AMCA: MESE 2253: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.882 mA, 39.3% # Test item 3- 4- 3- 1

T AMCA: MESE 2253: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.010 mA, 7.0% # Test item 3- 4- 3- 2

T AMCA: MESE 2253: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.848 mA, 33.8% # Test item 3- 4- 3- 3

T AMCA: MESE 2253: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.008 mA, 5.1% # Test item 3- 4- 3- 4

T AMCA: MESE 2254: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.880 mA, 40.0% # Test item 4- 4- 3- 1

T AMCA: MESE 2254: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.010 mA, 6.8% # Test item 4- 4- 3- 2

T AMCA: MESE 2254: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.845 mA, 34.5% # Test item 4- 4- 3- 3

T AMCA: MESE 2254: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.006 mA, 4.2% # Test item 4- 4- 3- 4

T AMCA: MESE 2255: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.878 mA, 40.5% # Test item 5- 4- 3- 1

T AMCA: MESE 2255: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.011 mA, 7.5% # Test item 5- 4- 3- 2

T AMCA: MESE 2255: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.846 mA, 34.1% # Test item 5- 4- 3- 3

T AMCA: MESE 2255: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.006 mA, 3.9% # Test item 5- 4- 3- 4

T AMCA: MESE 2256: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.882 mA, 39.5% # Test item 6- 4- 3- 1

T AMCA: MESE 2256: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.013 mA, 8.7% # Test item 6- 4- 3- 2

T AMCA: MESE 2256: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.842 mA, 35.1% # Test item 6- 4- 3- 3

T AMCA: MESE 2256: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.004 mA, 2.6% # Test item 6- 4- 3- 4

T AMCA: MESE 2257: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.883 mA, 39.0% # Test item 7- 4- 3- 1

T AMCA: MESE 2257: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.012 mA, 8.0% # Test item 7- 4- 3- 2

T AMCA: MESE 2257: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.844 mA, 34.7% # Test item 7- 4- 3- 3

T AMCA: MESE 2257: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.006 mA, 3.7% # Test item 7- 4- 3- 4

T AMCA: MESE 2258: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.882 mA, 39.3% # Test item 8- 4- 3- 1

T AMCA: MESE 2258: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.011 mA, 7.5% # Test item 8- 4- 3- 2

T AMCA: MESE 2258: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.841 mA, 35.4% # Test item 8- 4- 3- 3

T AMCA: MESE 2258: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.005 mA, 3.6% # Test item 8- 4- 3- 4

T AMCA: MESE 2251: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.994 V, 1.9% # Test item 1- 4- 4- 1

T AMCA: MESE 2251: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.275 V, 10.3% # Test item 1- 4- 4- 2

T AMCA: MESE 2251: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.012 V, 2.7% # Test item 1- 4- 4- 3

T AMCA: MESE 2251: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.272 V, 0.6% # Test item 1- 4- 4- 4

T AMCA: MESE 2252: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.992 V, 2.7% # Test item 2- 4- 4- 1

T AMCA: MESE 2252: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.273 V, 3.8% # Test item 2- 4- 4- 2

T AMCA: MESE 2252: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.017 V, 3.8% # Test item 2- 4- 4- 3

T AMCA: MESE 2252: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.275 V, 12.6% # Test item 2- 4- 4- 4

T AMCA: MESE 2253: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.993 V, 2.5% # Test item 3- 4- 4- 1

T AMCA: MESE 2253: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.273 V, 2.0% # Test item 3- 4- 4- 2

T AMCA: MESE 2253: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.018 V, 4.1% # Test item 3- 4- 4- 3

T AMCA: MESE 2253: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.273 V, 4.8% # Test item 3- 4- 4- 4

T AMCA: MESE 2254: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.991 V, 3.0% # Test item 4- 4- 4- 1

T AMCA: MESE 2254: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.276 V, 15.9% # Test item 4- 4- 4- 2

T AMCA: MESE 2254: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.012 V, 2.7% # Test item 4- 4- 4- 3

T AMCA: MESE 2254: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.273 V, 2.5% # Test item 4- 4- 4- 4

T AMCA: MESE 2255: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.992 V, 2.7% # Test item 5- 4- 4- 1

T AMCA: MESE 2255: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.273 V, 4.3% # Test item 5- 4- 4- 2

T AMCA: MESE 2255: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.012 V, 2.7% # Test item 5- 4- 4- 3

T AMCA: MESE 2255: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.272 V, 1.3% # Test item 5- 4- 4- 4

T AMCA: MESE 2256: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.991 V, 3.0% # Test item 6- 4- 4- 1

T AMCA: MESE 2256: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.275 V, 12.2% # Test item 6- 4- 4- 2

T AMCA: MESE 2256: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.012 V, 2.6% # Test item 6- 4- 4- 3

T AMCA: MESE 2256: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.271 V, 3.1% # Test item 6- 4- 4- 4

T AMCA: MESE 2257: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.991 V, 3.1% # Test item 7- 4- 4- 1

T AMCA: MESE 2257: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.275 V, 11.7% # Test item 7- 4- 4- 2

T AMCA: MESE 2257: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.013 V, 2.8% # Test item 7- 4- 4- 3

T AMCA: MESE 2257: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.272 V, 1.3% # Test item 7- 4- 4- 4

T AMCA: MESE 2258: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.992 V, 2.8% # Test item 8- 4- 4- 1

T AMCA: MESE 2258: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.277 V, 16.8% # Test item 8- 4- 4- 2

T AMCA: MESE 2258: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.014 V, 3.0% # Test item 8- 4- 4- 3

T AMCA: MESE 2258: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.272 V, 0.6% # Test item 8- 4- 4- 4

T AMCA: MESE 2251: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.997 kOhm, 2.6% # Test item 1- 4- 5- 1

T AMCA: MESE 2252: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.997 kOhm, 3.3% # Test item 2- 4- 5- 1

T AMCA: MESE 2253: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.997 kOhm, 3.2% # Test item 3- 4- 5- 1

T AMCA: MESE 2254: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.996 kOhm, 3.7% # Test item 4- 4- 5- 1

T AMCA: MESE 2255: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.997 kOhm, 3.2% # Test item 5- 4- 5- 1

T AMCA: MESE 2256: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.997 kOhm, 3.4% # Test item 6- 4- 5- 1

T AMCA: MESE 2257: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.996 kOhm, 3.5% # Test item 7- 4- 5- 1

T AMCA: MESE 2258: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.997 kOhm, 3.5% # Test item 8- 4- 5- 1

T AMCA: MCE 2250: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_1: R\_exp = 10220.000 Ohm, [9198.000 ... 11242.000 Ohm] : R\_meas = 10028.419 Ohm, 18.7% >> vMeas = 2.886V, vOffset = -0.123V, usedUnit = 1 # Test item 0- 5- 1- 1

T AMCA: MCE 2250: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_1\_(with\_GND): R\_exp = 59.000 Ohm, [49.000 ... 69.000 Ohm] : R\_meas = 60.467 Ohm, 14.7% >> vMeas = 0.185V, vOffset = 0.004V, usedUnit = 1 # Test item 0- 5- 1- 2

T AMCA: MCE 2250: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_2: R\_exp = 10220.000 Ohm, [9198.000 ... 11242.000 Ohm] : R\_meas = 10030.098 Ohm, 18.6% >> vMeas = 2.888V, vOffset = -0.121V, usedUnit = 1 # Test item 0- 5- 1- 3

T AMCA: MCE 2250: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_2\_(with\_GND): R\_exp = 59.000 Ohm, [49.000 ... 69.000 Ohm] : R\_meas = 60.467 Ohm, 14.7% >> vMeas = 0.185V, vOffset = 0.004V, usedUnit = 1 # Test item 0- 5- 1- 4

T AMCA: MCE 2250: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_\_0.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 1

T AMCA: MCE 2250: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_\_1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 2

T AMCA: MCE 2250: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_-1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 3

T AMCA: MCE 2250: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_\_0.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 4

T AMCA: MCE 2250: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_\_1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 5

T AMCA: MCE 2250: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_-1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 6

T AMCA: MESE 2251: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.501 V, 0.5% >> vOffset = -0.009V # Test item 1- 2- 9- 1

T AMCA: MESE 2251: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.003 V, 25.2% >> vOffset = 0.003V # Test item 1- 2- 9- 2

T AMCA: MESE 2252: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.501 V, 0.6% >> vOffset = -0.012V # Test item 2- 2- 9- 1

T AMCA: MESE 2252: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.003 V, 25.2% >> vOffset = -0.000V # Test item 2- 2- 9- 2

T AMCA: MESE 2253: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.501 V, 0.9% >> vOffset = -0.011V # Test item 3- 2- 9- 1

T AMCA: MESE 2253: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 23.9% >> vOffset = 0.001V # Test item 3- 2- 9- 2

T AMCA: MESE 2254: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.500 V, 0.2% >> vOffset = -0.010V # Test item 4- 2- 9- 1

T AMCA: MESE 2254: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 22.7% >> vOffset = 0.004V # Test item 4- 2- 9- 2

T AMCA: MESE 2255: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 0.6% >> vOffset = -0.008V # Test item 5- 2- 9- 1

T AMCA: MESE 2255: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 18.9% >> vOffset = 0.006V # Test item 5- 2- 9- 2

T AMCA: MESE 2256: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 0.4% >> vOffset = -0.008V # Test item 6- 2- 9- 1

T AMCA: MESE 2256: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 23.9% >> vOffset = 0.007V # Test item 6- 2- 9- 2

T AMCA: MESE 2257: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 0.6% >> vOffset = -0.009V # Test item 7- 2- 9- 1

T AMCA: MESE 2257: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 18.9% >> vOffset = 0.006V # Test item 7- 2- 9- 2

T AMCA: MESE 2258: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 0.6% >> vOffset = -0.009V # Test item 8- 2- 9- 1

T AMCA: MESE 2258: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 21.4% >> vOffset = 0.006V # Test item 8- 2- 9- 2

T AMCA: MESE 2251: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 1- 7- 1- 1

T AMCA: MESE 2252: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 2- 7- 1- 1

T AMCA: MESE 2253: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 3- 7- 1- 1

T AMCA: MESE 2254: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 4- 7- 1- 1

T AMCA: MESE 2255: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 5- 7- 1- 1

T AMCA: MESE 2256: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 6- 7- 1- 1

T AMCA: MESE 2257: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 7- 7- 1- 1

T AMCA: MESE 2258: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 8- 7- 1- 1

T AMCA: MESE 2251: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 1- 7- 1- 2

T AMCA: MESE 2252: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 2- 7- 1- 2

T AMCA: MESE 2253: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 3- 7- 1- 2

T AMCA: MESE 2254: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 4- 7- 1- 2

T AMCA: MESE 2255: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 5- 7- 1- 2

T AMCA: MESE 2256: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 6- 7- 1- 2

T AMCA: MESE 2257: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 7- 7- 1- 2

T AMCA: MESE 2258: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 8- 7- 1- 2

T AMCA: MESE 2251: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 1- 7- 2- 1

T AMCA: MESE 2252: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 2- 7- 2- 1

T AMCA: MESE 2253: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 3- 7- 2- 1

T AMCA: MESE 2254: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 4- 7- 2- 1

T AMCA: MESE 2255: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 5- 7- 2- 1

T AMCA: MESE 2256: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 6- 7- 2- 1

T AMCA: MESE 2257: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 7- 7- 2- 1

T AMCA: MESE 2258: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 8- 7- 2- 1

T AMCA: MESE 2251: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 7- 3- 1

T AMCA: MESE 2251: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 7- 3- 2

T AMCA: MESE 2252: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 7- 3- 1

T AMCA: MESE 2252: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 7- 3- 2

T AMCA: MESE 2253: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 7- 3- 1

T AMCA: MESE 2253: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 7- 3- 2

T AMCA: MESE 2254: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 7- 3- 1

T AMCA: MESE 2254: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 7- 3- 2

T AMCA: MESE 2255: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 7- 3- 1

T AMCA: MESE 2255: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 7- 3- 2

T AMCA: MESE 2256: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 7- 3- 1

T AMCA: MESE 2256: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 7- 3- 2

T AMCA: MESE 2257: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 7- 3- 1

T AMCA: MESE 2257: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 7- 3- 2

T AMCA: MESE 2258: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 7- 3- 1

T AMCA: MESE 2258: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 7- 3- 2

T AMCA: MESE 2251: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 1- 8- 1- 1

T AMCA: MESE 2251: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 1- 8- 1- 2

T AMCA: MESE 2251: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 1- 8- 1- 3

T AMCA: MESE 2251: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 4

T AMCA: MESE 2251: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 1- 8- 1- 5

T AMCA: MESE 2251: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 6

T AMCA: MESE 2251: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 1- 8- 1- 7

T AMCA: MESE 2251: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 8

T AMCA: MESE 2251: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 9

T AMCA: MESE 2252: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 2- 8- 1- 1

T AMCA: MESE 2252: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 2- 8- 1- 2

T AMCA: MESE 2252: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 2- 8- 1- 3

T AMCA: MESE 2252: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 4

T AMCA: MESE 2252: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 2- 8- 1- 5

T AMCA: MESE 2252: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 6

T AMCA: MESE 2252: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 2- 8- 1- 7

T AMCA: MESE 2252: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 8

T AMCA: MESE 2252: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 9

T AMCA: MESE 2253: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 3- 8- 1- 1

T AMCA: MESE 2253: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 3- 8- 1- 2

T AMCA: MESE 2253: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 3- 8- 1- 3

T AMCA: MESE 2253: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 4

T AMCA: MESE 2253: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 3- 8- 1- 5

T AMCA: MESE 2253: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 6

T AMCA: MESE 2253: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 3- 8- 1- 7

T AMCA: MESE 2253: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 8

T AMCA: MESE 2253: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 9

T AMCA: MESE 2254: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 4- 8- 1- 1

T AMCA: MESE 2254: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 4- 8- 1- 2

T AMCA: MESE 2254: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 4- 8- 1- 3

T AMCA: MESE 2254: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 4

T AMCA: MESE 2254: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 4- 8- 1- 5

T AMCA: MESE 2254: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 6

T AMCA: MESE 2254: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 4- 8- 1- 7

T AMCA: MESE 2254: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 8

T AMCA: MESE 2254: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 9

T AMCA: MESE 2255: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 5- 8- 1- 1

T AMCA: MESE 2255: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 5- 8- 1- 2

T AMCA: MESE 2255: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 5- 8- 1- 3

T AMCA: MESE 2255: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 4

T AMCA: MESE 2255: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 5- 8- 1- 5

T AMCA: MESE 2255: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 6

T AMCA: MESE 2255: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 5- 8- 1- 7

T AMCA: MESE 2255: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 8

T AMCA: MESE 2255: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 9

T AMCA: MESE 2256: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 6- 8- 1- 1

T AMCA: MESE 2256: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 6- 8- 1- 2

T AMCA: MESE 2256: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 6- 8- 1- 3

T AMCA: MESE 2256: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 4

T AMCA: MESE 2256: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 6- 8- 1- 5

T AMCA: MESE 2256: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 6

T AMCA: MESE 2256: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 6- 8- 1- 7

T AMCA: MESE 2256: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 8

T AMCA: MESE 2256: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 9

T AMCA: MESE 2257: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 7- 8- 1- 1

T AMCA: MESE 2257: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 7- 8- 1- 2

T AMCA: MESE 2257: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 7- 8- 1- 3

T AMCA: MESE 2257: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 4

T AMCA: MESE 2257: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 7- 8- 1- 5

T AMCA: MESE 2257: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 6

T AMCA: MESE 2257: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 7- 8- 1- 7

T AMCA: MESE 2257: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 8

T AMCA: MESE 2257: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 9

T AMCA: MESE 2258: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 8- 8- 1- 1

T AMCA: MESE 2258: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 8- 8- 1- 2

T AMCA: MESE 2258: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 8- 8- 1- 3

T AMCA: MESE 2258: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 4

T AMCA: MESE 2258: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 8- 8- 1- 5

T AMCA: MESE 2258: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 6

T AMCA: MESE 2258: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 8- 8- 1- 7

T AMCA: MESE 2258: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 8

T AMCA: MESE 2258: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 9

T AMCA: MESE 2251: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 1, masterModule = 1, slaveUnit = 3, slaveModule = 1 # Test item 1- 8- 3- 1

T AMCA: MESE 2251: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 1, masterModule = 1, slaveUnit = 3, slaveModule = 1 # Test item 1- 8- 3- 2

T AMCA: MESE 2253: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 3, masterModule = 1, slaveUnit = 2, slaveModule = 2 # Test item 3- 8- 3- 3

T AMCA: MESE 2253: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 3, masterModule = 1, slaveUnit = 2, slaveModule = 2 # Test item 3- 8- 3- 4

T AMCA: MESE 2252: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 2, masterModule = 2, slaveUnit = 4, slaveModule = 2 # Test item 2- 8- 3- 5

T AMCA: MESE 2252: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 2, masterModule = 2, slaveUnit = 4, slaveModule = 2 # Test item 2- 8- 3- 6

T AMCA: MESE 2254: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 4, masterModule = 2, slaveUnit = 5, slaveModule = 3 # Test item 4- 8- 3- 7

T AMCA: MESE 2254: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 4, masterModule = 2, slaveUnit = 5, slaveModule = 3 # Test item 4- 8- 3- 8

T AMCA: MESE 2255: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 5, masterModule = 3, slaveUnit = 7, slaveModule = 3 # Test item 5- 8- 3- 9

T AMCA: MESE 2255: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 5, masterModule = 3, slaveUnit = 7, slaveModule = 3 # Test item 5- 8- 3-10

T AMCA: MESE 2257: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 7, masterModule = 3, slaveUnit = 8, slaveModule = 4 # Test item 7- 8- 3-11

T AMCA: MESE 2257: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 7, masterModule = 3, slaveUnit = 8, slaveModule = 4 # Test item 7- 8- 3-12

T AMCA: MESE 2258: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 8, masterModule = 4, slaveUnit = 6, slaveModule = 4 # Test item 8- 8- 3-13

T AMCA: MESE 2258: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 8, masterModule = 4, slaveUnit = 6, slaveModule = 4 # Test item 8- 8- 3-14

T AMCA: MESE 2256: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 6, masterModule = 4, slaveUnit = 1, slaveModule = 1 # Test item 6- 8- 3-15

T AMCA: MESE 2256: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 6, masterModule = 4, slaveUnit = 1, slaveModule = 1 # Test item 6- 8- 3-16

T AMCA: MESE 2251: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.630 ns, 18.5% >> short = 59350, long = 30396 # Test item 1- 8- 6- 1

T AMCA: MESE 2252: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.501 ns, 25.0% >> short = 59505, long = 30637 # Test item 2- 8- 6- 1

T AMCA: MESE 2253: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.665 ns, 16.8% >> short = 59787, long = 30456 # Test item 3- 8- 6- 1

T AMCA: MESE 2254: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.566 ns, 21.7% >> short = 60122, long = 30697 # Test item 4- 8- 6- 1

T AMCA: MESE 2255: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.538 ns, 23.1% >> short = 59898, long = 30682 # Test item 5- 8- 6- 1

T AMCA: MESE 2256: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.541 ns, 22.9% >> short = 60454, long = 30823 # Test item 6- 8- 6- 1

T AMCA: MESE 2257: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.663 ns, 16.9% >> short = 60005, long = 30516 # Test item 7- 8- 6- 1

T AMCA: MESE 2258: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.907 ns, 4.7% >> short = 59630, long = 30047 # Test item 8- 8- 6- 1

T AMCA: MESE 2251: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 1- 8- 7- 1

T AMCA: MESE 2251: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17696, 57.6% # Test item 1- 8- 7- 2

T AMCA: MESE 2251: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004520: Reg\_meas = 0x00004520 # Test item 1- 8- 7- 3

T AMCA: MESE 2251: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 8- 7- 4

T AMCA: MESE 2252: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 2- 8- 7- 1

T AMCA: MESE 2252: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17865, 53.4% # Test item 2- 8- 7- 2

T AMCA: MESE 2252: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x000045C9: Reg\_meas = 0x000045C9 # Test item 2- 8- 7- 3

T AMCA: MESE 2252: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 8- 7- 4

T AMCA: MESE 2253: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 3- 8- 7- 1

T AMCA: MESE 2253: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17203, 69.9% # Test item 3- 8- 7- 2

T AMCA: MESE 2253: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004333: Reg\_meas = 0x00004333 # Test item 3- 8- 7- 3

T AMCA: MESE 2253: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 8- 7- 4

T AMCA: MESE 2254: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 4- 8- 7- 1

T AMCA: MESE 2254: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17706, 57.4% # Test item 4- 8- 7- 2

T AMCA: MESE 2254: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000452A: Reg\_meas = 0x0000452A # Test item 4- 8- 7- 3

T AMCA: MESE 2254: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 8- 7- 4

T AMCA: MESE 2255: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 5- 8- 7- 1

T AMCA: MESE 2255: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18344, 41.4% # Test item 5- 8- 7- 2

T AMCA: MESE 2255: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x000047A8: Reg\_meas = 0x000047A8 # Test item 5- 8- 7- 3

T AMCA: MESE 2255: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 8- 7- 4

T AMCA: MESE 2256: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 6- 8- 7- 1

T AMCA: MESE 2256: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17483, 62.9% # Test item 6- 8- 7- 2

T AMCA: MESE 2256: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000444B: Reg\_meas = 0x0000444B # Test item 6- 8- 7- 3

T AMCA: MESE 2256: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 8- 7- 4

T AMCA: MESE 2257: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 7- 8- 7- 1

T AMCA: MESE 2257: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17272, 68.2% # Test item 7- 8- 7- 2

T AMCA: MESE 2257: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004378: Reg\_meas = 0x00004378 # Test item 7- 8- 7- 3

T AMCA: MESE 2257: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 8- 7- 4

T AMCA: MESE 2258: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 8- 8- 7- 1

T AMCA: MESE 2258: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17884, 52.9% # Test item 8- 8- 7- 2

T AMCA: MESE 2258: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x000045DC: Reg\_meas = 0x000045DC # Test item 8- 8- 7- 3

T AMCA: MESE 2258: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 8- 7- 4

T AMCA: MESE 2251: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 9- 1- 1

T AMCA: MESE 2251: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 9- 1- 2

T AMCA: MESE 2251: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 3

T AMCA: MESE 2251: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 4

T AMCA: MESE 2251: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 5

T AMCA: MESE 2251: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 6

T AMCA: MESE 2251: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 7

T AMCA: MESE 2251: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 1- 9- 1- 8

T AMCA: MESE 2251: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 9

T AMCA: MESE 2251: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1-10

T AMCA: MESE 2251: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 1- 9- 1-11

T AMCA: MESE 2251: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 1- 9- 1-12

T AMCA: MESE 2251: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 1- 9- 1-13

T AMCA: MESE 2251: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 9- 1-14

T AMCA: MESE 2252: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 9- 1- 1

T AMCA: MESE 2252: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 9- 1- 2

T AMCA: MESE 2252: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 3

T AMCA: MESE 2252: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 4

T AMCA: MESE 2252: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 5

T AMCA: MESE 2252: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 6

T AMCA: MESE 2252: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 7

T AMCA: MESE 2252: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 2- 9- 1- 8

T AMCA: MESE 2252: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 9

T AMCA: MESE 2252: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1-10

T AMCA: MESE 2252: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 2- 9- 1-11

T AMCA: MESE 2252: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 2- 9- 1-12

T AMCA: MESE 2252: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 2- 9- 1-13

T AMCA: MESE 2252: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 9- 1-14

T AMCA: MESE 2253: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 9- 1- 1

T AMCA: MESE 2253: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 9- 1- 2

T AMCA: MESE 2253: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 3

T AMCA: MESE 2253: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 4

T AMCA: MESE 2253: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 5

T AMCA: MESE 2253: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 6

T AMCA: MESE 2253: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 7

T AMCA: MESE 2253: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 3- 9- 1- 8

T AMCA: MESE 2253: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 9

T AMCA: MESE 2253: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1-10

T AMCA: MESE 2253: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 3- 9- 1-11

T AMCA: MESE 2253: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 3- 9- 1-12

T AMCA: MESE 2253: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 3- 9- 1-13

T AMCA: MESE 2253: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 9- 1-14

T AMCA: MESE 2254: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 9- 1- 1

T AMCA: MESE 2254: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 9- 1- 2

T AMCA: MESE 2254: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 3

T AMCA: MESE 2254: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 4

T AMCA: MESE 2254: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 5

T AMCA: MESE 2254: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 6

T AMCA: MESE 2254: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 7

T AMCA: MESE 2254: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 4- 9- 1- 8

T AMCA: MESE 2254: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 9

T AMCA: MESE 2254: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1-10

T AMCA: MESE 2254: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 4- 9- 1-11

T AMCA: MESE 2254: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 4- 9- 1-12

T AMCA: MESE 2254: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 4- 9- 1-13

T AMCA: MESE 2254: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 9- 1-14

T AMCA: MESE 2255: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 9- 1- 1

T AMCA: MESE 2255: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 9- 1- 2

T AMCA: MESE 2255: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 3

T AMCA: MESE 2255: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 4

T AMCA: MESE 2255: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 5

T AMCA: MESE 2255: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 6

T AMCA: MESE 2255: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 7

T AMCA: MESE 2255: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 5- 9- 1- 8

T AMCA: MESE 2255: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 9

T AMCA: MESE 2255: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1-10

T AMCA: MESE 2255: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 5- 9- 1-11

T AMCA: MESE 2255: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 5- 9- 1-12

T AMCA: MESE 2255: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 5- 9- 1-13

T AMCA: MESE 2255: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 9- 1-14

T AMCA: MESE 2256: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 9- 1- 1

T AMCA: MESE 2256: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 9- 1- 2

T AMCA: MESE 2256: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 3

T AMCA: MESE 2256: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 4

T AMCA: MESE 2256: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 5

T AMCA: MESE 2256: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 6

T AMCA: MESE 2256: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 7

T AMCA: MESE 2256: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 6- 9- 1- 8

T AMCA: MESE 2256: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 9

T AMCA: MESE 2256: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1-10

T AMCA: MESE 2256: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 6- 9- 1-11

T AMCA: MESE 2256: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 6- 9- 1-12

T AMCA: MESE 2256: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 6- 9- 1-13

T AMCA: MESE 2256: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 9- 1-14

T AMCA: MESE 2257: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 9- 1- 1

T AMCA: MESE 2257: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 9- 1- 2

T AMCA: MESE 2257: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 3

T AMCA: MESE 2257: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 4

T AMCA: MESE 2257: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 5

T AMCA: MESE 2257: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 6

T AMCA: MESE 2257: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 7

T AMCA: MESE 2257: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 7- 9- 1- 8

T AMCA: MESE 2257: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 9

T AMCA: MESE 2257: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1-10

T AMCA: MESE 2257: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 7- 9- 1-11

T AMCA: MESE 2257: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 7- 9- 1-12

T AMCA: MESE 2257: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 7- 9- 1-13

T AMCA: MESE 2257: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 9- 1-14

T AMCA: MESE 2258: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 9- 1- 1

T AMCA: MESE 2258: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 9- 1- 2

T AMCA: MESE 2258: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 3

T AMCA: MESE 2258: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 4

T AMCA: MESE 2258: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 5

T AMCA: MESE 2258: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 6

T AMCA: MESE 2258: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 7

T AMCA: MESE 2258: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 8- 9- 1- 8

T AMCA: MESE 2258: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 9

T AMCA: MESE 2258: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1-10

T AMCA: MESE 2258: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 8- 9- 1-11

T AMCA: MESE 2258: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 8- 9- 1-12

T AMCA: MESE 2258: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 8- 9- 1-13

T AMCA: MESE 2258: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 9- 1-14

T AMCA: MESE 2251: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.957 V, 0.9% # Test item 1-17- 1- 1

T AMCA: MESE 2251: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.951 V, 6.4% # Test item 1-17- 1- 2

T AMCA: MESE 2251: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.966 V, 7.3% # Test item 1-17- 1- 3

T AMCA: MESE 2251: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.958 V, 0.0% # Test item 1-17- 1- 4

T AMCA: MESE 2251: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.067 V, 1.1% # Test item 1-17- 1- 5

T AMCA: MESE 2251: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.982 V, 2.7% # Test item 1-17- 1- 6

T AMCA: MESE 2251: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.970 V, 2.7% # Test item 1-17- 1- 7

T AMCA: MESE 2251: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.921 V, 10.9% # Test item 1-17- 1- 8

T AMCA: MESE 2251: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.976 V, 2.7% # Test item 1-17- 1- 9

T AMCA: MESE 2251: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.991 V, 1.8% # Test item 1-17- 1-10

T AMCA: MESE 2251: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.978 V, 1.8% # Test item 1-17- 1-11

T AMCA: MESE 2252: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.961 V, 2.7% # Test item 2-17- 1- 1

T AMCA: MESE 2252: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.960 V, 1.8% # Test item 2-17- 1- 2

T AMCA: MESE 2252: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.956 V, 1.8% # Test item 2-17- 1- 3

T AMCA: MESE 2252: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.954 V, 3.6% # Test item 2-17- 1- 4

T AMCA: MESE 2252: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.064 V, 4.4% # Test item 2-17- 1- 5

T AMCA: MESE 2252: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.979 V, 0.0% # Test item 2-17- 1- 6

T AMCA: MESE 2252: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.974 V, 0.9% # Test item 2-17- 1- 7

T AMCA: MESE 2252: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.919 V, 10.0% # Test item 2-17- 1- 8

T AMCA: MESE 2252: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.971 V, 7.3% # Test item 2-17- 1- 9

T AMCA: MESE 2252: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.987 V, 5.5% # Test item 2-17- 1-10

T AMCA: MESE 2252: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.968 V, 7.3% # Test item 2-17- 1-11

T AMCA: MESE 2253: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.954 V, 3.6% # Test item 3-17- 1- 1

T AMCA: MESE 2253: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.959 V, 0.9% # Test item 3-17- 1- 2

T AMCA: MESE 2253: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.955 V, 2.7% # Test item 3-17- 1- 3

T AMCA: MESE 2253: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.960 V, 1.8% # Test item 3-17- 1- 4

T AMCA: MESE 2253: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.064 V, 3.6% # Test item 3-17- 1- 5

T AMCA: MESE 2253: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.984 V, 4.5% # Test item 3-17- 1- 6

T AMCA: MESE 2253: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.979 V, 5.5% # Test item 3-17- 1- 7

T AMCA: MESE 2253: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.649 V, 14.5% # Test item 3-17- 1- 8

T AMCA: MESE 2253: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.976 V, 2.7% # Test item 3-17- 1- 9

T AMCA: MESE 2253: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.717 V, 10.0% # Test item 3-17- 1-10

T AMCA: MESE 2254: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.952 V, 5.5% # Test item 4-17- 1- 1

T AMCA: MESE 2254: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.958 V, 0.0% # Test item 4-17- 1- 2

T AMCA: MESE 2254: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.961 V, 2.7% # Test item 4-17- 1- 3

T AMCA: MESE 2254: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.963 V, 4.5% # Test item 4-17- 1- 4

T AMCA: MESE 2254: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.064 V, 3.6% # Test item 4-17- 1- 5

T AMCA: MESE 2254: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.982 V, 2.7% # Test item 4-17- 1- 6

T AMCA: MESE 2254: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.984 V, 10.0% # Test item 4-17- 1- 7

T AMCA: MESE 2254: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.651 V, 16.3% # Test item 4-17- 1- 8

T AMCA: MESE 2254: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.978 V, 0.9% # Test item 4-17- 1- 9

T AMCA: MESE 2254: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.710 V, 16.4% # Test item 4-17- 1-10

T AMCA: MESE 2255: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.958 V, 0.0% # Test item 5-17- 1- 1

T AMCA: MESE 2255: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.952 V, 5.5% # Test item 5-17- 1- 2

T AMCA: MESE 2255: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.962 V, 3.6% # Test item 5-17- 1- 3

T AMCA: MESE 2255: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.956 V, 1.8% # Test item 5-17- 1- 4

T AMCA: MESE 2255: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.065 V, 3.3% # Test item 5-17- 1- 5

T AMCA: MESE 2255: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.986 V, 6.4% # Test item 5-17- 1- 6

T AMCA: MESE 2255: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.973 V, 0.0% # Test item 5-17- 1- 7

T AMCA: MESE 2255: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.922 V, 11.4% # Test item 5-17- 1- 8

T AMCA: MESE 2255: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.979 V, 0.0% # Test item 5-17- 1- 9

T AMCA: MESE 2255: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.985 V, 7.3% # Test item 5-17- 1-10

T AMCA: MESE 2255: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.973 V, 2.7% # Test item 5-17- 1-11

T AMCA: MESE 2256: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.960 V, 1.8% # Test item 6-17- 1- 1

T AMCA: MESE 2256: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.967 V, 8.2% # Test item 6-17- 1- 2

T AMCA: MESE 2256: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.958 V, 0.0% # Test item 6-17- 1- 3

T AMCA: MESE 2256: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.960 V, 1.8% # Test item 6-17- 1- 4

T AMCA: MESE 2256: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.069 V, 0.9% # Test item 6-17- 1- 5

T AMCA: MESE 2256: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.983 V, 3.6% # Test item 6-17- 1- 6

T AMCA: MESE 2256: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.967 V, 5.5% # Test item 6-17- 1- 7

T AMCA: MESE 2256: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.642 V, 8.1% # Test item 6-17- 1- 8

T AMCA: MESE 2256: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.980 V, 0.9% # Test item 6-17- 1- 9

T AMCA: MESE 2256: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.710 V, 16.4% # Test item 6-17- 1-10

T AMCA: MESE 2257: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.955 V, 2.7% # Test item 7-17- 1- 1

T AMCA: MESE 2257: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.954 V, 3.6% # Test item 7-17- 1- 2

T AMCA: MESE 2257: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.956 V, 1.8% # Test item 7-17- 1- 3

T AMCA: MESE 2257: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.954 V, 3.6% # Test item 7-17- 1- 4

T AMCA: MESE 2257: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.071 V, 2.7% # Test item 7-17- 1- 5

T AMCA: MESE 2257: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.982 V, 2.7% # Test item 7-17- 1- 6

T AMCA: MESE 2257: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.979 V, 5.5% # Test item 7-17- 1- 7

T AMCA: MESE 2257: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.648 V, 13.6% # Test item 7-17- 1- 8

T AMCA: MESE 2257: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.979 V, 0.0% # Test item 7-17- 1- 9

T AMCA: MESE 2257: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.715 V, 11.8% # Test item 7-17- 1-10

T AMCA: MESE 2258: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.955 V, 2.7% # Test item 8-17- 1- 1

T AMCA: MESE 2258: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.960 V, 1.8% # Test item 8-17- 1- 2

T AMCA: MESE 2258: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.950 V, 7.3% # Test item 8-17- 1- 3

T AMCA: MESE 2258: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.951 V, 6.4% # Test item 8-17- 1- 4

T AMCA: MESE 2258: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.068 V, 0.0% # Test item 8-17- 1- 5

T AMCA: MESE 2258: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.980 V, 0.9% # Test item 8-17- 1- 6

T AMCA: MESE 2258: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.978 V, 4.5% # Test item 8-17- 1- 7

T AMCA: MESE 2258: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.949 V, 24.3% # Test item 8-17- 1- 8

T AMCA: MESE 2258: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.973 V, 5.5% # Test item 8-17- 1- 9

T AMCA: MESE 2258: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.986 V, 6.4% # Test item 8-17- 1-10

T AMCA: MESE 2258: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.969 V, 6.4% # Test item 8-17- 1-11

T AMCA: MESE 2251: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.7% # Test item 1-15- 1- 1

T AMCA: MESE 2251: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% # Test item 1-15- 1- 2

T AMCA: MESE 2251: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.012 V, 4.1% # Test item 1-15- 1- 3

T AMCA: MESE 2251: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.7% # Test item 1-15- 1- 4

T AMCA: MESE 2251: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% # Test item 1-15- 1- 5

T AMCA: MESE 2251: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.012 V, 4.1% # Test item 1-15- 1- 6

T AMCA: MESE 2251: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.1% # Test item 1-15- 1- 7

T AMCA: MESE 2251: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.5% # Test item 1-15- 1- 8

T AMCA: MESE 2251: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.3% # Test item 1-15- 1- 9

T AMCA: MESE 2251: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.1% # Test item 1-15- 1-10

T AMCA: MESE 2251: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.5% # Test item 1-15- 1-11

T AMCA: MESE 2251: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.3% # Test item 1-15- 1-12

T AMCA: MESE 2252: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.1% # Test item 2-15- 1- 1

T AMCA: MESE 2252: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% # Test item 2-15- 1- 2

T AMCA: MESE 2252: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.014 V, 4.8% # Test item 2-15- 1- 3

T AMCA: MESE 2252: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.0% # Test item 2-15- 1- 4

T AMCA: MESE 2252: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% # Test item 2-15- 1- 5

T AMCA: MESE 2252: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.014 V, 4.8% # Test item 2-15- 1- 6

T AMCA: MESE 2252: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.7% # Test item 2-15- 1- 7

T AMCA: MESE 2252: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% # Test item 2-15- 1- 8

T AMCA: MESE 2252: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.015 V, 4.9% # Test item 2-15- 1- 9

T AMCA: MESE 2252: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.6% # Test item 2-15- 1-10

T AMCA: MESE 2252: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.9% # Test item 2-15- 1-11

T AMCA: MESE 2252: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.015 V, 4.9% # Test item 2-15- 1-12

T AMCA: MESE 2253: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.0% # Test item 3-15- 1- 1

T AMCA: MESE 2253: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.8% # Test item 3-15- 1- 2

T AMCA: MESE 2253: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.016 V, 5.4% # Test item 3-15- 1- 3

T AMCA: MESE 2253: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.0% # Test item 3-15- 1- 4

T AMCA: MESE 2253: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.9% # Test item 3-15- 1- 5

T AMCA: MESE 2253: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.017 V, 5.5% # Test item 3-15- 1- 6

T AMCA: MESE 2253: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.011 V, 3.6% # Test item 3-15- 1- 7

T AMCA: MESE 2253: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% # Test item 3-15- 1- 8

T AMCA: MESE 2253: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.015 V, 5.0% # Test item 3-15- 1- 9

T AMCA: MESE 2253: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.011 V, 3.6% # Test item 3-15- 1-10

T AMCA: MESE 2253: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% # Test item 3-15- 1-11

T AMCA: MESE 2253: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.015 V, 5.0% # Test item 3-15- 1-12

T AMCA: MESE 2254: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.4% # Test item 4-15- 1- 1

T AMCA: MESE 2254: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.0% # Test item 4-15- 1- 2

T AMCA: MESE 2254: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.8% # Test item 4-15- 1- 3

T AMCA: MESE 2254: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.3% # Test item 4-15- 1- 4

T AMCA: MESE 2254: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.9% # Test item 4-15- 1- 5

T AMCA: MESE 2254: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.8% # Test item 4-15- 1- 6

T AMCA: MESE 2254: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 4.9% # Test item 4-15- 1- 7

T AMCA: MESE 2254: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.2% # Test item 4-15- 1- 8

T AMCA: MESE 2254: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.009 V, 2.9% # Test item 4-15- 1- 9

T AMCA: MESE 2254: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 4.9% # Test item 4-15- 1-10

T AMCA: MESE 2254: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.2% # Test item 4-15- 1-11

T AMCA: MESE 2254: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.009 V, 3.0% # Test item 4-15- 1-12

T AMCA: MESE 2255: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.6% # Test item 5-15- 1- 1

T AMCA: MESE 2255: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.5% # Test item 5-15- 1- 2

T AMCA: MESE 2255: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.6% # Test item 5-15- 1- 3

T AMCA: MESE 2255: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.5% # Test item 5-15- 1- 4

T AMCA: MESE 2255: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.5% # Test item 5-15- 1- 5

T AMCA: MESE 2255: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.6% # Test item 5-15- 1- 6

T AMCA: MESE 2255: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.7% # Test item 5-15- 1- 7

T AMCA: MESE 2255: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.4% # Test item 5-15- 1- 8

T AMCA: MESE 2255: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.3% # Test item 5-15- 1- 9

T AMCA: MESE 2255: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.6% # Test item 5-15- 1-10

T AMCA: MESE 2255: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.4% # Test item 5-15- 1-11

T AMCA: MESE 2255: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.3% # Test item 5-15- 1-12

T AMCA: MESE 2256: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.2% # Test item 6-15- 1- 1

T AMCA: MESE 2256: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 7.8% # Test item 6-15- 1- 2

T AMCA: MESE 2256: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.004 V, 1.3% # Test item 6-15- 1- 3

T AMCA: MESE 2256: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.2% # Test item 6-15- 1- 4

T AMCA: MESE 2256: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 8.1% # Test item 6-15- 1- 5

T AMCA: MESE 2256: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.004 V, 1.4% # Test item 6-15- 1- 6

T AMCA: MESE 2256: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.3% # Test item 6-15- 1- 7

T AMCA: MESE 2256: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 7.8% # Test item 6-15- 1- 8

T AMCA: MESE 2256: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.005 V, 1.6% # Test item 6-15- 1- 9

T AMCA: MESE 2256: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.2% # Test item 6-15- 1-10

T AMCA: MESE 2256: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 7.8% # Test item 6-15- 1-11

T AMCA: MESE 2256: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.004 V, 1.5% # Test item 6-15- 1-12

T AMCA: MESE 2257: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 7-15- 1- 1

T AMCA: MESE 2257: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.4% # Test item 7-15- 1- 2

T AMCA: MESE 2257: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.009 V, 3.1% # Test item 7-15- 1- 3

T AMCA: MESE 2257: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.5% # Test item 7-15- 1- 4

T AMCA: MESE 2257: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.5% # Test item 7-15- 1- 5

T AMCA: MESE 2257: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.009 V, 3.1% # Test item 7-15- 1- 6

T AMCA: MESE 2257: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 4.9% # Test item 7-15- 1- 7

T AMCA: MESE 2257: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.3% # Test item 7-15- 1- 8

T AMCA: MESE 2257: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.3% # Test item 7-15- 1- 9

T AMCA: MESE 2257: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 4.9% # Test item 7-15- 1-10

T AMCA: MESE 2257: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.2% # Test item 7-15- 1-11

T AMCA: MESE 2257: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.3% # Test item 7-15- 1-12

T AMCA: MESE 2258: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.4% # Test item 8-15- 1- 1

T AMCA: MESE 2258: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 8.1% # Test item 8-15- 1- 2

T AMCA: MESE 2258: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.004 V, 1.4% # Test item 8-15- 1- 3

T AMCA: MESE 2258: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.4% # Test item 8-15- 1- 4

T AMCA: MESE 2258: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 8.2% # Test item 8-15- 1- 5

T AMCA: MESE 2258: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.004 V, 1.5% # Test item 8-15- 1- 6

T AMCA: MESE 2258: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.1% # Test item 8-15- 1- 7

T AMCA: MESE 2258: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 8.1% # Test item 8-15- 1- 8

T AMCA: MESE 2258: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.004 V, 1.2% # Test item 8-15- 1- 9

T AMCA: MESE 2258: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.0% # Test item 8-15- 1-10

T AMCA: MESE 2258: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 7.9% # Test item 8-15- 1-11

T AMCA: MESE 2258: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.004 V, 1.3% # Test item 8-15- 1-12

T AMCA: MESE 2251: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.875 MOhm, 8.3% # Test item 1-15- 2- 1

T AMCA: MESE 2251: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.051 MOhm, 3.4% # Test item 1-15- 2- 2

T AMCA: MESE 2251: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.750 MOhm, 16.7% # Test item 1-15- 2- 3

T AMCA: MESE 2251: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.262 MOhm, 17.4% # Test item 1-15- 2- 4

T AMCA: MESE 2252: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.864 MOhm, 9.1% # Test item 2-15- 2- 1

T AMCA: MESE 2252: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.158 MOhm, 10.5% # Test item 2-15- 2- 2

T AMCA: MESE 2252: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.027 MOhm, 1.8% # Test item 2-15- 2- 3

T AMCA: MESE 2252: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.005 MOhm, 0.3% # Test item 2-15- 2- 4

T AMCA: MESE 2253: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.008 MOhm, 0.6% # Test item 3-15- 2- 1

T AMCA: MESE 2253: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.164 MOhm, 10.9% # Test item 3-15- 2- 2

T AMCA: MESE 2253: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.106 MOhm, 7.1% # Test item 3-15- 2- 3

T AMCA: MESE 2253: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.963 MOhm, 2.5% # Test item 3-15- 2- 4

T AMCA: MESE 2254: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.038 MOhm, 2.6% # Test item 4-15- 2- 1

T AMCA: MESE 2254: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.987 MOhm, 0.9% # Test item 4-15- 2- 2

T AMCA: MESE 2254: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.109 MOhm, 7.3% # Test item 4-15- 2- 3

T AMCA: MESE 2254: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.115 MOhm, 7.7% # Test item 4-15- 2- 4

T AMCA: MESE 2255: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.851 MOhm, 9.9% # Test item 5-15- 2- 1

T AMCA: MESE 2255: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.876 MOhm, 8.3% # Test item 5-15- 2- 2

T AMCA: MESE 2255: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.872 MOhm, 8.6% # Test item 5-15- 2- 3

T AMCA: MESE 2255: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.212 MOhm, 14.1% # Test item 5-15- 2- 4

T AMCA: MESE 2256: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.942 MOhm, 3.8% # Test item 6-15- 2- 1

T AMCA: MESE 2256: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.918 MOhm, 5.4% # Test item 6-15- 2- 2

T AMCA: MESE 2256: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.058 MOhm, 3.9% # Test item 6-15- 2- 3

T AMCA: MESE 2256: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.014 MOhm, 0.9% # Test item 6-15- 2- 4

T AMCA: MESE 2257: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.077 MOhm, 5.1% # Test item 7-15- 2- 1

T AMCA: MESE 2257: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.117 MOhm, 7.8% # Test item 7-15- 2- 2

T AMCA: MESE 2257: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.979 MOhm, 1.4% # Test item 7-15- 2- 3

T AMCA: MESE 2257: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.158 MOhm, 10.5% # Test item 7-15- 2- 4

T AMCA: MESE 2258: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.145 MOhm, 9.7% # Test item 8-15- 2- 1

T AMCA: MESE 2258: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.019 MOhm, 1.3% # Test item 8-15- 2- 2

T AMCA: MESE 2258: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 10.087 MOhm, 5.8% # Test item 8-15- 2- 3

T AMCA: MESE 2258: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.857 MOhm, 9.6% # Test item 8-15- 2- 4

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.016V, neg = 0.016V # Test item 1-15- 3- 1

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.704 V, 20.0% >> pos = 2.368V, neg = -2.336V # Test item 1-15- 3- 2

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.016V, neg = 0.016V # Test item 1-15- 3- 3

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.6% >> pos = 0.799V, neg = -0.766V # Test item 1-15- 3- 4

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.016V, neg = 0.016V # Test item 1-15- 3- 5

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.5% >> pos = 0.114V, neg = -0.082V # Test item 1-15- 3- 6

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.016V, neg = -0.016V # Test item 1-15- 3- 7

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.704 V, 20.1% >> pos = 2.335V, neg = -2.368V # Test item 1-15- 3- 8

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.016V, neg = -0.016V # Test item 1-15- 3- 9

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.7% >> pos = 0.766V, neg = -0.799V # Test item 1-15- 3-10

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.016V, neg = -0.016V # Test item 1-15- 3-11

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.6% >> pos = 0.082V, neg = -0.114V # Test item 1-15- 3-12

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.013V, neg = 0.013V # Test item 1-15- 3-13

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.2% >> pos = 2.365V, neg = -2.338V # Test item 1-15- 3-14

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.013V, neg = 0.013V # Test item 1-15- 3-15

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.8% >> pos = 0.796V, neg = -0.769V # Test item 1-15- 3-16

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.013V, neg = 0.013V # Test item 1-15- 3-17

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.6% >> pos = 0.111V, neg = -0.085V # Test item 1-15- 3-18

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.013V, neg = -0.013V # Test item 1-15- 3-19

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.1% >> pos = 2.338V, neg = -2.365V # Test item 1-15- 3-20

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.013V, neg = -0.013V # Test item 1-15- 3-21

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.8% >> pos = 0.769V, neg = -0.796V # Test item 1-15- 3-22

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.013V, neg = -0.013V # Test item 1-15- 3-23

T AMCA: MESE 2251: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.5% >> pos = 0.085V, neg = -0.111V # Test item 1-15- 3-24

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.016V, neg = 0.016V # Test item 2-15- 3- 1

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.2% >> pos = 2.367V, neg = -2.336V # Test item 2-15- 3- 2

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.015V, neg = 0.015V # Test item 2-15- 3- 3

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.7% >> pos = 0.798V, neg = -0.767V # Test item 2-15- 3- 4

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.016V, neg = 0.016V # Test item 2-15- 3- 5

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.6% >> pos = 0.114V, neg = -0.082V # Test item 2-15- 3- 6

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.016V, neg = -0.016V # Test item 2-15- 3- 7

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.3% >> pos = 2.336V, neg = -2.367V # Test item 2-15- 3- 8

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.016V, neg = -0.016V # Test item 2-15- 3- 9

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.7% >> pos = 0.767V, neg = -0.798V # Test item 2-15- 3-10

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.016V, neg = -0.016V # Test item 2-15- 3-11

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.5% >> pos = 0.082V, neg = -0.114V # Test item 2-15- 3-12

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.016V, neg = 0.016V # Test item 2-15- 3-13

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.3% >> pos = 2.368V, neg = -2.335V # Test item 2-15- 3-14

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.016V, neg = 0.016V # Test item 2-15- 3-15

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.1% >> pos = 0.799V, neg = -0.766V # Test item 2-15- 3-16

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 2-15- 3-17

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.6% >> pos = 0.115V, neg = -0.081V # Test item 2-15- 3-18

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.017V, neg = -0.016V # Test item 2-15- 3-19

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.3% >> pos = 2.335V, neg = -2.368V # Test item 2-15- 3-20

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.016V, neg = -0.016V # Test item 2-15- 3-21

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.1% >> pos = 0.766V, neg = -0.799V # Test item 2-15- 3-22

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 2-15- 3-23

T AMCA: MESE 2252: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.6% >> pos = 0.081V, neg = -0.115V # Test item 2-15- 3-24

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.018V, neg = 0.018V # Test item 3-15- 3- 1

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.698 V, 21.3% >> pos = 2.367V, neg = -2.330V # Test item 3-15- 3- 2

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.018V, neg = 0.018V # Test item 3-15- 3- 3

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.563 V, 22.9% >> pos = 0.800V, neg = -0.763V # Test item 3-15- 3- 4

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.018V, neg = 0.019V # Test item 3-15- 3- 5

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.6% >> pos = 0.116V, neg = -0.079V # Test item 3-15- 3- 6

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.019V, neg = -0.019V # Test item 3-15- 3- 7

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.698 V, 21.3% >> pos = 2.330V, neg = -2.367V # Test item 3-15- 3- 8

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.018V, neg = -0.018V # Test item 3-15- 3- 9

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.563 V, 22.9% >> pos = 0.763V, neg = -0.800V # Test item 3-15- 3-10

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.018V, neg = -0.018V # Test item 3-15- 3-11

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.6% >> pos = 0.079V, neg = -0.116V # Test item 3-15- 3-12

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 3-15- 3-13

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.698 V, 21.3% >> pos = 2.366V, neg = -2.332V # Test item 3-15- 3-14

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 3-15- 3-15

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.7% >> pos = 0.799V, neg = -0.765V # Test item 3-15- 3-16

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 3-15- 3-17

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.7% >> pos = 0.115V, neg = -0.081V # Test item 3-15- 3-18

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 3-15- 3-19

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.698 V, 21.3% >> pos = 2.332V, neg = -2.366V # Test item 3-15- 3-20

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 3-15- 3-21

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.7% >> pos = 0.765V, neg = -0.799V # Test item 3-15- 3-22

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 3-15- 3-23

T AMCA: MESE 2253: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.7% >> pos = 0.081V, neg = -0.115V # Test item 3-15- 3-24

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.015V, neg = 0.015V # Test item 4-15- 3- 1

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.705 V, 19.8% >> pos = 2.367V, neg = -2.338V # Test item 4-15- 3- 2

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.015V, neg = 0.015V # Test item 4-15- 3- 3

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.4% >> pos = 0.798V, neg = -0.768V # Test item 4-15- 3- 4

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.015V, neg = 0.015V # Test item 4-15- 3- 5

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.3% >> pos = 0.113V, neg = -0.083V # Test item 4-15- 3- 6

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.015V, neg = -0.015V # Test item 4-15- 3- 7

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.705 V, 19.9% >> pos = 2.337V, neg = -2.367V # Test item 4-15- 3- 8

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.015V, neg = -0.015V # Test item 4-15- 3- 9

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.5% >> pos = 0.768V, neg = -0.798V # Test item 4-15- 3-10

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.015V, neg = -0.015V # Test item 4-15- 3-11

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.4% >> pos = 0.083V, neg = -0.113V # Test item 4-15- 3-12

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.016V, neg = 0.016V # Test item 4-15- 3-13

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.704 V, 20.0% >> pos = 2.368V, neg = -2.336V # Test item 4-15- 3-14

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.016V, neg = 0.016V # Test item 4-15- 3-15

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.8% >> pos = 0.798V, neg = -0.767V # Test item 4-15- 3-16

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.016V, neg = 0.016V # Test item 4-15- 3-17

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.5% >> pos = 0.114V, neg = -0.082V # Test item 4-15- 3-18

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.016V, neg = -0.016V # Test item 4-15- 3-19

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.704 V, 20.0% >> pos = 2.336V, neg = -2.368V # Test item 4-15- 3-20

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.016V, neg = -0.016V # Test item 4-15- 3-21

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.8% >> pos = 0.767V, neg = -0.798V # Test item 4-15- 3-22

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.016V, neg = -0.016V # Test item 4-15- 3-23

T AMCA: MESE 2254: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.4% >> pos = 0.082V, neg = -0.114V # Test item 4-15- 3-24

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.010V, neg = 0.010V # Test item 5-15- 3- 1

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.685 V, 23.9% >> pos = 2.353V, neg = -2.333V # Test item 5-15- 3- 2

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.010V, neg = 0.010V # Test item 5-15- 3- 3

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.560 V, 25.3% >> pos = 0.790V, neg = -0.770V # Test item 5-15- 3- 4

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.010V, neg = 0.010V # Test item 5-15- 3- 5

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 24.0% >> pos = 0.108V, neg = -0.087V # Test item 5-15- 3- 6

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.010V, neg = -0.010V # Test item 5-15- 3- 7

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.687 V, 23.6% >> pos = 2.333V, neg = -2.353V # Test item 5-15- 3- 8

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.010V, neg = -0.010V # Test item 5-15- 3- 9

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.560 V, 25.2% >> pos = 0.770V, neg = -0.790V # Test item 5-15- 3-10

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.010V, neg = -0.010V # Test item 5-15- 3-11

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 24.0% >> pos = 0.087V, neg = -0.108V # Test item 5-15- 3-12

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.009V, neg = 0.009V # Test item 5-15- 3-13

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.687 V, 23.5% >> pos = 2.353V, neg = -2.334V # Test item 5-15- 3-14

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.010V, neg = 0.009V # Test item 5-15- 3-15

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.560 V, 25.1% >> pos = 0.790V, neg = -0.770V # Test item 5-15- 3-16

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.010V, neg = 0.010V # Test item 5-15- 3-17

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.8% >> pos = 0.107V, neg = -0.088V # Test item 5-15- 3-18

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.009V, neg = -0.010V # Test item 5-15- 3-19

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.686 V, 23.7% >> pos = 2.334V, neg = -2.353V # Test item 5-15- 3-20

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.009V, neg = -0.009V # Test item 5-15- 3-21

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.560 V, 25.2% >> pos = 0.770V, neg = -0.789V # Test item 5-15- 3-22

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.009V, neg = -0.009V # Test item 5-15- 3-23

T AMCA: MESE 2255: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.9% >> pos = 0.088V, neg = -0.107V # Test item 5-15- 3-24

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.011V, neg = 0.011V # Test item 6-15- 3- 1

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.702 V, 20.3% >> pos = 2.363V, neg = -2.340V # Test item 6-15- 3- 2

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.011V, neg = 0.011V # Test item 6-15- 3- 3

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.9% >> pos = 0.794V, neg = -0.771V # Test item 6-15- 3- 4

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.011V, neg = 0.011V # Test item 6-15- 3- 5

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.8% >> pos = 0.109V, neg = -0.086V # Test item 6-15- 3- 6

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.011V, neg = -0.011V # Test item 6-15- 3- 7

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.702 V, 20.4% >> pos = 2.340V, neg = -2.363V # Test item 6-15- 3- 8

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.011V, neg = -0.011V # Test item 6-15- 3- 9

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.9% >> pos = 0.771V, neg = -0.794V # Test item 6-15- 3-10

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.011V, neg = -0.011V # Test item 6-15- 3-11

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.0% >> pos = 0.086V, neg = -0.109V # Test item 6-15- 3-12

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.012V, neg = 0.012V # Test item 6-15- 3-13

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.702 V, 20.4% >> pos = 2.363V, neg = -2.339V # Test item 6-15- 3-14

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.012V, neg = 0.012V # Test item 6-15- 3-15

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.1% >> pos = 0.794V, neg = -0.770V # Test item 6-15- 3-16

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.012V, neg = 0.012V # Test item 6-15- 3-17

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.1% >> pos = 0.110V, neg = -0.086V # Test item 6-15- 3-18

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.012V, neg = -0.012V # Test item 6-15- 3-19

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.3% >> pos = 2.339V, neg = -2.363V # Test item 6-15- 3-20

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.012V, neg = -0.012V # Test item 6-15- 3-21

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.0% >> pos = 0.770V, neg = -0.794V # Test item 6-15- 3-22

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.012V, neg = -0.012V # Test item 6-15- 3-23

T AMCA: MESE 2256: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.9% >> pos = 0.086V, neg = -0.110V # Test item 6-15- 3-24

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.014V, neg = 0.014V # Test item 7-15- 3- 1

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.715 V, 17.7% >> pos = 2.372V, neg = -2.344V # Test item 7-15- 3- 2

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.014V, neg = 0.014V # Test item 7-15- 3- 3

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.569 V, 19.2% >> pos = 0.799V, neg = -0.771V # Test item 7-15- 3- 4

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.014V, neg = 0.014V # Test item 7-15- 3- 5

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.0% >> pos = 0.112V, neg = -0.084V # Test item 7-15- 3- 6

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.014V, neg = -0.014V # Test item 7-15- 3- 7

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.716 V, 17.6% >> pos = 2.344V, neg = -2.372V # Test item 7-15- 3- 8

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.014V, neg = -0.014V # Test item 7-15- 3- 9

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.569 V, 19.2% >> pos = 0.771V, neg = -0.799V # Test item 7-15- 3-10

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.014V, neg = -0.014V # Test item 7-15- 3-11

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.2% >> pos = 0.084V, neg = -0.112V # Test item 7-15- 3-12

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.012V, neg = 0.012V # Test item 7-15- 3-13

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.716 V, 17.6% >> pos = 2.370V, neg = -2.345V # Test item 7-15- 3-14

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.012V, neg = 0.012V # Test item 7-15- 3-15

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.569 V, 19.3% >> pos = 0.797V, neg = -0.772V # Test item 7-15- 3-16

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.013V, neg = 0.013V # Test item 7-15- 3-17

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.0% >> pos = 0.111V, neg = -0.086V # Test item 7-15- 3-18

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.013V, neg = -0.013V # Test item 7-15- 3-19

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.715 V, 17.7% >> pos = 2.345V, neg = -2.370V # Test item 7-15- 3-20

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.013V, neg = -0.013V # Test item 7-15- 3-21

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.569 V, 19.3% >> pos = 0.772V, neg = -0.797V # Test item 7-15- 3-22

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.013V, neg = -0.013V # Test item 7-15- 3-23

T AMCA: MESE 2257: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.0% >> pos = 0.086V, neg = -0.111V # Test item 7-15- 3-24

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.012V, neg = 0.012V # Test item 8-15- 3- 1

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.711 V, 18.6% >> pos = 2.368V, neg = -2.343V # Test item 8-15- 3- 2

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.012V, neg = 0.012V # Test item 8-15- 3- 3

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.567 V, 20.3% >> pos = 0.796V, neg = -0.771V # Test item 8-15- 3- 4

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.012V, neg = 0.012V # Test item 8-15- 3- 5

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.7% >> pos = 0.111V, neg = -0.086V # Test item 8-15- 3- 6

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.012V, neg = -0.012V # Test item 8-15- 3- 7

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.711 V, 18.5% >> pos = 2.343V, neg = -2.368V # Test item 8-15- 3- 8

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.012V, neg = -0.012V # Test item 8-15- 3- 9

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 20.3% >> pos = 0.771V, neg = -0.796V # Test item 8-15- 3-10

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.013V, neg = -0.013V # Test item 8-15- 3-11

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.8% >> pos = 0.086V, neg = -0.111V # Test item 8-15- 3-12

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.013V, neg = 0.013V # Test item 8-15- 3-13

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.710 V, 18.7% >> pos = 2.368V, neg = -2.342V # Test item 8-15- 3-14

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.013V, neg = 0.013V # Test item 8-15- 3-15

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 20.2% >> pos = 0.797V, neg = -0.771V # Test item 8-15- 3-16

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.013V, neg = 0.013V # Test item 8-15- 3-17

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.0% >> pos = 0.111V, neg = -0.085V # Test item 8-15- 3-18

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.013V, neg = -0.013V # Test item 8-15- 3-19

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.711 V, 18.6% >> pos = 2.342V, neg = -2.368V # Test item 8-15- 3-20

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.013V, neg = -0.013V # Test item 8-15- 3-21

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 20.2% >> pos = 0.771V, neg = -0.797V # Test item 8-15- 3-22

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.013V, neg = -0.013V # Test item 8-15- 3-23

T AMCA: MESE 2258: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.8% >> pos = 0.085V, neg = -0.111V # Test item 8-15- 3-24

T AMCA: MESE 2251: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.7% # Test item 1-16- 1- 1

T AMCA: MESE 2251: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.1% # Test item 1-16- 1- 2

T AMCA: MESE 2251: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.003 V, 0.9% # Test item 1-16- 1- 3

T AMCA: MESE 2251: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.6% # Test item 1-16- 1- 4

T AMCA: MESE 2251: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.3% # Test item 1-16- 1- 5

T AMCA: MESE 2251: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.002 V, 0.8% # Test item 1-16- 1- 6

T AMCA: MESE 2251: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.8% # Test item 1-16- 1- 7

T AMCA: MESE 2251: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.1% # Test item 1-16- 1- 8

T AMCA: MESE 2251: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.002 V, 0.8% # Test item 1-16- 1- 9

T AMCA: MESE 2251: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.5% # Test item 1-16- 1-10

T AMCA: MESE 2251: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.4% # Test item 1-16- 1-11

T AMCA: MESE 2251: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.002 V, 0.8% # Test item 1-16- 1-12

T AMCA: MESE 2252: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.3% # Test item 2-16- 1- 1

T AMCA: MESE 2252: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.6% # Test item 2-16- 1- 2

T AMCA: MESE 2252: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.006 V, 1.9% # Test item 2-16- 1- 3

T AMCA: MESE 2252: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.3% # Test item 2-16- 1- 4

T AMCA: MESE 2252: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.5% # Test item 2-16- 1- 5

T AMCA: MESE 2252: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.006 V, 1.9% # Test item 2-16- 1- 6

T AMCA: MESE 2252: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.2% # Test item 2-16- 1- 7

T AMCA: MESE 2252: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.4% # Test item 2-16- 1- 8

T AMCA: MESE 2252: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.006 V, 1.8% # Test item 2-16- 1- 9

T AMCA: MESE 2252: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.3% # Test item 2-16- 1-10

T AMCA: MESE 2252: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.4% # Test item 2-16- 1-11

T AMCA: MESE 2252: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.006 V, 1.9% # Test item 2-16- 1-12

T AMCA: MESE 2253: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.8% # Test item 3-16- 1- 1

T AMCA: MESE 2253: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.0% # Test item 3-16- 1- 2

T AMCA: MESE 2253: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.006 V, 1.8% # Test item 3-16- 1- 3

T AMCA: MESE 2253: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.8% # Test item 3-16- 1- 4

T AMCA: MESE 2253: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.1% # Test item 3-16- 1- 5

T AMCA: MESE 2253: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.005 V, 1.8% # Test item 3-16- 1- 6

T AMCA: MESE 2253: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.8% # Test item 3-16- 1- 7

T AMCA: MESE 2253: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% # Test item 3-16- 1- 8

T AMCA: MESE 2253: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.006 V, 1.8% # Test item 3-16- 1- 9

T AMCA: MESE 2253: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.8% # Test item 3-16- 1-10

T AMCA: MESE 2253: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.0% # Test item 3-16- 1-11

T AMCA: MESE 2253: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.006 V, 1.8% # Test item 3-16- 1-12

T AMCA: MESE 2254: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.1% # Test item 4-16- 1- 1

T AMCA: MESE 2254: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.7% # Test item 4-16- 1- 2

T AMCA: MESE 2254: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.999 V, 0.4% # Test item 4-16- 1- 3

T AMCA: MESE 2254: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.1% # Test item 4-16- 1- 4

T AMCA: MESE 2254: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.8% # Test item 4-16- 1- 5

T AMCA: MESE 2254: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.999 V, 0.5% # Test item 4-16- 1- 6

T AMCA: MESE 2254: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.1% # Test item 4-16- 1- 7

T AMCA: MESE 2254: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 8.4% # Test item 4-16- 1- 8

T AMCA: MESE 2254: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.999 V, 0.4% # Test item 4-16- 1- 9

T AMCA: MESE 2254: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.2% # Test item 4-16- 1-10

T AMCA: MESE 2254: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.6% # Test item 4-16- 1-11

T AMCA: MESE 2254: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.999 V, 0.5% # Test item 4-16- 1-12

T AMCA: MESE 2255: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 5-16- 1- 1

T AMCA: MESE 2255: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 7.9% # Test item 5-16- 1- 2

T AMCA: MESE 2255: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.999 V, 0.4% # Test item 5-16- 1- 3

T AMCA: MESE 2255: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 5-16- 1- 4

T AMCA: MESE 2255: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 7.9% # Test item 5-16- 1- 5

T AMCA: MESE 2255: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.6% # Test item 5-16- 1- 6

T AMCA: MESE 2255: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 5-16- 1- 7

T AMCA: MESE 2255: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 8.1% # Test item 5-16- 1- 8

T AMCA: MESE 2255: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.5% # Test item 5-16- 1- 9

T AMCA: MESE 2255: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 5-16- 1-10

T AMCA: MESE 2255: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 8.1% # Test item 5-16- 1-11

T AMCA: MESE 2255: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.5% # Test item 5-16- 1-12

T AMCA: MESE 2256: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.5% # Test item 6-16- 1- 1

T AMCA: MESE 2256: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 8.2% # Test item 6-16- 1- 2

T AMCA: MESE 2256: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.7% # Test item 6-16- 1- 3

T AMCA: MESE 2256: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.5% # Test item 6-16- 1- 4

T AMCA: MESE 2256: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 8.3% # Test item 6-16- 1- 5

T AMCA: MESE 2256: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.8% # Test item 6-16- 1- 6

T AMCA: MESE 2256: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 6-16- 1- 7

T AMCA: MESE 2256: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 8.2% # Test item 6-16- 1- 8

T AMCA: MESE 2256: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.7% # Test item 6-16- 1- 9

T AMCA: MESE 2256: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 6-16- 1-10

T AMCA: MESE 2256: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 8.3% # Test item 6-16- 1-11

T AMCA: MESE 2256: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.7% # Test item 6-16- 1-12

T AMCA: MESE 2257: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.011 V, 3.6% # Test item 7-16- 1- 1

T AMCA: MESE 2257: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.4% # Test item 7-16- 1- 2

T AMCA: MESE 2257: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.999 V, 0.3% # Test item 7-16- 1- 3

T AMCA: MESE 2257: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.011 V, 3.6% # Test item 7-16- 1- 4

T AMCA: MESE 2257: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.3% # Test item 7-16- 1- 5

T AMCA: MESE 2257: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.999 V, 0.3% # Test item 7-16- 1- 6

T AMCA: MESE 2257: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.011 V, 3.6% # Test item 7-16- 1- 7

T AMCA: MESE 2257: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.3% # Test item 7-16- 1- 8

T AMCA: MESE 2257: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.999 V, 0.3% # Test item 7-16- 1- 9

T AMCA: MESE 2257: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.011 V, 3.6% # Test item 7-16- 1-10

T AMCA: MESE 2257: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.3% # Test item 7-16- 1-11

T AMCA: MESE 2257: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.999 V, 0.3% # Test item 7-16- 1-12

T AMCA: MESE 2258: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.1% # Test item 8-16- 1- 1

T AMCA: MESE 2258: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.4% # Test item 8-16- 1- 2

T AMCA: MESE 2258: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 1.1% # Test item 8-16- 1- 3

T AMCA: MESE 2258: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.2% # Test item 8-16- 1- 4

T AMCA: MESE 2258: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.4% # Test item 8-16- 1- 5

T AMCA: MESE 2258: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 1.0% # Test item 8-16- 1- 6

T AMCA: MESE 2258: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.1% # Test item 8-16- 1- 7

T AMCA: MESE 2258: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.3% # Test item 8-16- 1- 8

T AMCA: MESE 2258: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 1.0% # Test item 8-16- 1- 9

T AMCA: MESE 2258: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.1% # Test item 8-16- 1-10

T AMCA: MESE 2258: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.6% # Test item 8-16- 1-11

T AMCA: MESE 2258: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 1.1% # Test item 8-16- 1-12

T AMCA: MESE 2251: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9896.450 Ohm, 10.4% >> MV = 1.910V, offset = -0.069V # Test item 1-16- 2- 1

T AMCA: MESE 2251: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 59.082 Ohm, 18.7% >> MV = 0.180V, offset = 0.003V # Test item 1-16- 2- 2

T AMCA: MESE 2251: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 46.242 Ohm, 27.6% >> MV = 0.141V, offset = 0.003V # Test item 1-16- 2- 3

T AMCA: MESE 2251: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9896.450 Ohm, 10.4% >> MV = 1.908V, offset = -0.071V # Test item 1-16- 2- 4

T AMCA: MESE 2251: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 58.956 Ohm, 16.5% >> MV = 0.180V, offset = 0.003V # Test item 1-16- 2- 5

T AMCA: MESE 2251: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 46.242 Ohm, 27.6% >> MV = 0.141V, offset = 0.003V # Test item 1-16- 2- 6

T AMCA: MESE 2252: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9877.567 Ohm, 12.2% >> MV = 1.899V, offset = -0.076V # Test item 2-16- 2- 1

T AMCA: MESE 2252: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 59.166 Ohm, 20.1% >> MV = 0.177V, offset = -0.001V # Test item 2-16- 2- 2

T AMCA: MESE 2252: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 46.284 Ohm, 28.5% >> MV = 0.139V, offset = 0.000V # Test item 2-16- 2- 3

T AMCA: MESE 2252: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9880.085 Ohm, 12.0% >> MV = 1.895V, offset = -0.081V # Test item 2-16- 2- 4

T AMCA: MESE 2252: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 58.662 Ohm, 11.4% >> MV = 0.176V, offset = -0.000V # Test item 2-16- 2- 5

T AMCA: MESE 2252: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 45.822 Ohm, 18.3% >> MV = 0.138V, offset = 0.000V # Test item 2-16- 2- 6

T AMCA: MESE 2253: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9907.780 Ohm, 9.2% >> MV = 1.893V, offset = -0.088V # Test item 3-16- 2- 1

T AMCA: MESE 2253: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 58.914 Ohm, 15.8% >> MV = 0.177V, offset = 0.000V # Test item 3-16- 2- 2

T AMCA: MESE 2253: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 46.032 Ohm, 22.9% >> MV = 0.139V, offset = 0.001V # Test item 3-16- 2- 3

T AMCA: MESE 2253: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9904.003 Ohm, 9.6% >> MV = 1.891V, offset = -0.089V # Test item 3-16- 2- 4

T AMCA: MESE 2253: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 59.334 Ohm, 23.0% >> MV = 0.178V, offset = 0.000V # Test item 3-16- 2- 5

T AMCA: MESE 2253: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 46.494 Ohm, 33.2% >> MV = 0.140V, offset = 0.000V # Test item 3-16- 2- 6

T AMCA: MESE 2254: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9878.826 Ohm, 12.1% >> MV = 1.884V, offset = -0.092V # Test item 4-16- 2- 1

T AMCA: MESE 2254: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 59.208 Ohm, 20.8% >> MV = 0.181V, offset = 0.004V # Test item 4-16- 2- 2

T AMCA: MESE 2254: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 46.116 Ohm, 24.8% >> MV = 0.143V, offset = 0.005V # Test item 4-16- 2- 3

T AMCA: MESE 2254: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9874.421 Ohm, 12.6% >> MV = 1.883V, offset = -0.091V # Test item 4-16- 2- 4

T AMCA: MESE 2254: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 58.537 Ohm, 9.3% >> MV = 0.179V, offset = 0.004V # Test item 4-16- 2- 5

T AMCA: MESE 2254: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 45.696 Ohm, 15.5% >> MV = 0.142V, offset = 0.005V # Test item 4-16- 2- 6

T AMCA: MESE 2255: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9880.085 Ohm, 12.0% >> MV = 1.913V, offset = -0.063V # Test item 5-16- 2- 1

T AMCA: MESE 2255: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 59.166 Ohm, 20.1% >> MV = 0.183V, offset = 0.005V # Test item 5-16- 2- 2

T AMCA: MESE 2255: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 46.242 Ohm, 27.6% >> MV = 0.144V, offset = 0.005V # Test item 5-16- 2- 3

T AMCA: MESE 2255: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9882.603 Ohm, 11.7% >> MV = 1.910V, offset = -0.066V # Test item 5-16- 2- 4

T AMCA: MESE 2255: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 59.418 Ohm, 24.4% >> MV = 0.183V, offset = 0.005V # Test item 5-16- 2- 5

T AMCA: MESE 2255: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 46.494 Ohm, 33.2% >> MV = 0.145V, offset = 0.006V # Test item 5-16- 2- 6

T AMCA: MESE 2256: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9864.350 Ohm, 13.6% >> MV = 1.898V, offset = -0.075V # Test item 6-16- 2- 1

T AMCA: MESE 2256: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 58.914 Ohm, 15.8% >> MV = 0.182V, offset = 0.006V # Test item 6-16- 2- 2

T AMCA: MESE 2256: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 46.116 Ohm, 24.8% >> MV = 0.144V, offset = 0.006V # Test item 6-16- 2- 3

T AMCA: MESE 2256: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9860.573 Ohm, 13.9% >> MV = 1.897V, offset = -0.075V # Test item 6-16- 2- 4

T AMCA: MESE 2256: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 58.159 Ohm, 2.7% >> MV = 0.181V, offset = 0.006V # Test item 6-16- 2- 5

T AMCA: MESE 2256: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 45.528 Ohm, 11.7% >> MV = 0.143V, offset = 0.006V # Test item 6-16- 2- 6

T AMCA: MESE 2257: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9873.161 Ohm, 12.7% >> MV = 1.898V, offset = -0.076V # Test item 7-16- 2- 1

T AMCA: MESE 2257: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 58.872 Ohm, 15.0% >> MV = 0.181V, offset = 0.005V # Test item 7-16- 2- 2

T AMCA: MESE 2257: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 46.116 Ohm, 24.8% >> MV = 0.143V, offset = 0.005V # Test item 7-16- 2- 3

T AMCA: MESE 2257: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9866.238 Ohm, 13.4% >> MV = 1.897V, offset = -0.076V # Test item 7-16- 2- 4

T AMCA: MESE 2257: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 59.040 Ohm, 17.9% >> MV = 0.182V, offset = 0.005V # Test item 7-16- 2- 5

T AMCA: MESE 2257: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 46.158 Ohm, 25.7% >> MV = 0.144V, offset = 0.005V # Test item 7-16- 2- 6

T AMCA: MESE 2258: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9887.639 Ohm, 11.2% >> MV = 1.898V, offset = -0.079V # Test item 8-16- 2- 1

T AMCA: MESE 2258: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 58.788 Ohm, 13.6% >> MV = 0.182V, offset = 0.006V # Test item 8-16- 2- 2

T AMCA: MESE 2258: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 45.948 Ohm, 21.1% >> MV = 0.144V, offset = 0.006V # Test item 8-16- 2- 3

T AMCA: MESE 2258: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9892.044 Ohm, 10.8% >> MV = 1.895V, offset = -0.084V # Test item 8-16- 2- 4

T AMCA: MESE 2258: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 59.250 Ohm, 21.5% >> MV = 0.183V, offset = 0.006V # Test item 8-16- 2- 5

T AMCA: MESE 2258: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 46.284 Ohm, 28.5% >> MV = 0.145V, offset = 0.006V # Test item 8-16- 2- 6

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.046V, neg = 0.046V # Test item 1-16- 3- 1

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.187 V, 4.1% >> pos = 1.642V, neg = -1.545V # Test item 1-16- 3- 2

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.5% >> pos = 0.030V, neg = 0.029V # Test item 1-16- 3- 3

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.582 V, 11.4% >> pos = 0.820V, neg = -0.762V # Test item 1-16- 3- 4

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.9% >> pos = 0.021V, neg = 0.021V # Test item 1-16- 3- 5

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.795 V, 5.9% >> pos = 0.418V, neg = -0.377V # Test item 1-16- 3- 6

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = 0.017V, neg = 0.016V # Test item 1-16- 3- 7

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 10.3% >> pos = 0.140V, neg = -0.107V # Test item 1-16- 3- 8

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.5% >> pos = 0.015V, neg = 0.015V # Test item 1-16- 3- 9

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 6.0% >> pos = 0.064V, neg = -0.035V # Test item 1-16- 3-10

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.6% >> pos = 0.023V, neg = 0.018V # Test item 1-16- 3-11

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.177 V, 7.3% >> pos = 1.610V, neg = -1.567V # Test item 1-16- 3-12

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.6% >> pos = 0.012V, neg = 0.010V # Test item 1-16- 3-13

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.575 V, 15.6% >> pos = 0.791V, neg = -0.784V # Test item 1-16- 3-14

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = -0.003V, neg = -0.003V # Test item 1-16- 3-15

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.791 V, 11.2% >> pos = 0.392V, neg = -0.399V # Test item 1-16- 3-16

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = -0.009V, neg = -0.008V # Test item 1-16- 3-17

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.246 V, 14.8% >> pos = 0.114V, neg = -0.132V # Test item 1-16- 3-18

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = -0.010V, neg = -0.011V # Test item 1-16- 3-19

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.0% >> pos = 0.040V, neg = -0.060V # Test item 1-16- 3-20

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.7% >> pos = 0.047V, neg = 0.038V # Test item 1-16- 3-21

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.184 V, 5.0% >> pos = 1.640V, neg = -1.544V # Test item 1-16- 3-22

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.029V, neg = 0.030V # Test item 1-16- 3-23

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.581 V, 12.1% >> pos = 0.820V, neg = -0.761V # Test item 1-16- 3-24

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.024V, neg = 0.024V # Test item 1-16- 3-25

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.792 V, 9.6% >> pos = 0.417V, neg = -0.375V # Test item 1-16- 3-26

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = 0.016V, neg = 0.017V # Test item 1-16- 3-27

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 11.6% >> pos = 0.140V, neg = -0.107V # Test item 1-16- 3-28

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.014V, neg = 0.014V # Test item 1-16- 3-29

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.103 V, 14.4% >> pos = 0.068V, neg = -0.035V # Test item 1-16- 3-30

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = 0.020V, neg = 0.021V # Test item 1-16- 3-31

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.181 V, 6.0% >> pos = 1.611V, neg = -1.570V # Test item 1-16- 3-32

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = 0.004V, neg = 0.003V # Test item 1-16- 3-33

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.576 V, 15.2% >> pos = 0.791V, neg = -0.785V # Test item 1-16- 3-34

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = -0.003V, neg = -0.003V # Test item 1-16- 3-35

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.790 V, 12.7% >> pos = 0.392V, neg = -0.398V # Test item 1-16- 3-36

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = -0.009V, neg = -0.008V # Test item 1-16- 3-37

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 13.6% >> pos = 0.115V, neg = -0.132V # Test item 1-16- 3-38

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = -0.010V, neg = -0.010V # Test item 1-16- 3-39

T AMCA: MESE 2251: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 2.9% >> pos = 0.040V, neg = -0.059V # Test item 1-16- 3-40

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = 0.072V, neg = 0.073V # Test item 2-16- 3- 1

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.196 V, 1.2% >> pos = 1.669V, neg = -1.528V # Test item 2-16- 3- 2

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.0% >> pos = 0.043V, neg = 0.045V # Test item 2-16- 3- 3

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.583 V, 10.4% >> pos = 0.835V, neg = -0.748V # Test item 2-16- 3- 4

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.2% >> pos = 0.028V, neg = 0.031V # Test item 2-16- 3- 5

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.788 V, 15.0% >> pos = 0.424V, neg = -0.364V # Test item 2-16- 3- 6

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = 0.022V, neg = 0.022V # Test item 2-16- 3- 7

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 1.5% >> pos = 0.146V, neg = -0.104V # Test item 2-16- 3- 8

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.018V, neg = 0.018V # Test item 2-16- 3- 9

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.095 V, 25.2% >> pos = 0.063V, neg = -0.032V # Test item 2-16- 3-10

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.1% >> pos = 0.042V, neg = 0.045V # Test item 2-16- 3-11

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.177 V, 7.3% >> pos = 1.630V, neg = -1.547V # Test item 2-16- 3-12

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.6% >> pos = 0.023V, neg = 0.020V # Test item 2-16- 3-13

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.573 V, 16.6% >> pos = 0.807V, neg = -0.767V # Test item 2-16- 3-14

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.1% >> pos = 0.003V, neg = -0.000V # Test item 2-16- 3-15

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.789 V, 13.9% >> pos = 0.395V, neg = -0.394V # Test item 2-16- 3-16

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.7% >> pos = -0.008V, neg = -0.006V # Test item 2-16- 3-17

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 12.2% >> pos = 0.115V, neg = -0.131V # Test item 2-16- 3-18

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = -0.012V, neg = -0.013V # Test item 2-16- 3-19

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 9.6% >> pos = 0.038V, neg = -0.061V # Test item 2-16- 3-20

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.008 V, 8.4% >> pos = 0.071V, neg = 0.079V # Test item 2-16- 3-21

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.196 V, 1.1% >> pos = 1.669V, neg = -1.527V # Test item 2-16- 3-22

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = 0.044V, neg = 0.042V # Test item 2-16- 3-23

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.583 V, 10.9% >> pos = 0.834V, neg = -0.749V # Test item 2-16- 3-24

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.030V, neg = 0.030V # Test item 2-16- 3-25

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.794 V, 7.8% >> pos = 0.426V, neg = -0.368V # Test item 2-16- 3-26

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = 0.021V, neg = 0.022V # Test item 2-16- 3-27

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.244 V, 22.3% >> pos = 0.145V, neg = -0.099V # Test item 2-16- 3-28

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = 0.017V, neg = 0.017V # Test item 2-16- 3-29

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.095 V, 25.5% >> pos = 0.063V, neg = -0.032V # Test item 2-16- 3-30

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.9% >> pos = 0.040V, neg = 0.042V # Test item 2-16- 3-31

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.172 V, 8.7% >> pos = 1.630V, neg = -1.542V # Test item 2-16- 3-32

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.6% >> pos = 0.015V, neg = 0.016V # Test item 2-16- 3-33

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.578 V, 13.8% >> pos = 0.803V, neg = -0.775V # Test item 2-16- 3-34

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.8% >> pos = -0.002V, neg = -0.000V # Test item 2-16- 3-35

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.791 V, 11.4% >> pos = 0.395V, neg = -0.396V # Test item 2-16- 3-36

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = -0.007V, neg = -0.007V # Test item 2-16- 3-37

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 10.2% >> pos = 0.116V, neg = -0.131V # Test item 2-16- 3-38

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = -0.012V, neg = -0.011V # Test item 2-16- 3-39

T AMCA: MESE 2252: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.096 V, 20.1% >> pos = 0.036V, neg = -0.060V # Test item 2-16- 3-40

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.005 V, 4.7% >> pos = 0.057V, neg = 0.062V # Test item 3-16- 3- 1

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.194 V, 2.0% >> pos = 1.658V, neg = -1.536V # Test item 3-16- 3- 2

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = 0.036V, neg = 0.037V # Test item 3-16- 3- 3

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.587 V, 8.3% >> pos = 0.831V, neg = -0.756V # Test item 3-16- 3- 4

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.5% >> pos = 0.026V, neg = 0.028V # Test item 3-16- 3- 5

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.793 V, 9.2% >> pos = 0.422V, neg = -0.370V # Test item 3-16- 3- 6

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = 0.020V, neg = 0.021V # Test item 3-16- 3- 7

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 12.9% >> pos = 0.146V, neg = -0.101V # Test item 3-16- 3- 8

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = 0.018V, neg = 0.018V # Test item 3-16- 3- 9

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 9.5% >> pos = 0.066V, neg = -0.032V # Test item 3-16- 3-10

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = 0.029V, neg = 0.030V # Test item 3-16- 3-11

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.163 V, 11.5% >> pos = 1.608V, neg = -1.555V # Test item 3-16- 3-12

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.017V, neg = 0.016V # Test item 3-16- 3-13

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.575 V, 15.4% >> pos = 0.793V, neg = -0.782V # Test item 3-16- 3-14

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.1% >> pos = 0.003V, neg = -0.004V # Test item 3-16- 3-15

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.793 V, 8.8% >> pos = 0.391V, neg = -0.402V # Test item 3-16- 3-16

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = -0.011V, neg = -0.010V # Test item 3-16- 3-17

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 1.2% >> pos = 0.115V, neg = -0.135V # Test item 3-16- 3-18

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.0% >> pos = -0.014V, neg = -0.016V # Test item 3-16- 3-19

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 5.4% >> pos = 0.036V, neg = -0.063V # Test item 3-16- 3-20

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.007 V, 6.6% >> pos = 0.056V, neg = 0.063V # Test item 3-16- 3-21

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.189 V, 3.4% >> pos = 1.655V, neg = -1.534V # Test item 3-16- 3-22

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.037V, neg = 0.036V # Test item 3-16- 3-23

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.593 V, 4.6% >> pos = 0.840V, neg = -0.753V # Test item 3-16- 3-24

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.6% >> pos = 0.027V, neg = 0.021V # Test item 3-16- 3-25

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.794 V, 7.8% >> pos = 0.425V, neg = -0.369V # Test item 3-16- 3-26

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.3% >> pos = 0.021V, neg = 0.021V # Test item 3-16- 3-27

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.246 V, 15.2% >> pos = 0.142V, neg = -0.104V # Test item 3-16- 3-28

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.3% >> pos = 0.018V, neg = 0.017V # Test item 3-16- 3-29

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 4.4% >> pos = 0.068V, neg = -0.031V # Test item 3-16- 3-30

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.1% >> pos = 0.033V, neg = 0.026V # Test item 3-16- 3-31

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.163 V, 11.7% >> pos = 1.606V, neg = -1.556V # Test item 3-16- 3-32

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = 0.006V, neg = 0.006V # Test item 3-16- 3-33

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.574 V, 16.2% >> pos = 0.794V, neg = -0.780V # Test item 3-16- 3-34

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = -0.005V, neg = -0.005V # Test item 3-16- 3-35

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.786 V, 17.7% >> pos = 0.387V, neg = -0.399V # Test item 3-16- 3-36

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = -0.011V, neg = -0.011V # Test item 3-16- 3-37

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 10.0% >> pos = 0.114V, neg = -0.134V # Test item 3-16- 3-38

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.2% >> pos = -0.015V, neg = -0.014V # Test item 3-16- 3-39

T AMCA: MESE 2253: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 8.4% >> pos = 0.036V, neg = -0.063V # Test item 3-16- 3-40

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = 0.059V, neg = 0.058V # Test item 4-16- 3- 1

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.189 V, 3.6% >> pos = 1.649V, neg = -1.540V # Test item 4-16- 3- 2

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.3% >> pos = 0.040V, neg = 0.037V # Test item 4-16- 3- 3

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.581 V, 11.9% >> pos = 0.826V, neg = -0.755V # Test item 4-16- 3- 4

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.025V, neg = 0.026V # Test item 4-16- 3- 5

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.789 V, 14.3% >> pos = 0.419V, neg = -0.369V # Test item 4-16- 3- 6

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.3% >> pos = 0.020V, neg = 0.020V # Test item 4-16- 3- 7

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 8.6% >> pos = 0.144V, neg = -0.104V # Test item 4-16- 3- 8

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.017V, neg = 0.018V # Test item 4-16- 3- 9

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 3.6% >> pos = 0.067V, neg = -0.033V # Test item 4-16- 3-10

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% >> pos = 0.028V, neg = 0.026V # Test item 4-16- 3-11

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.176 V, 7.4% >> pos = 1.613V, neg = -1.563V # Test item 4-16- 3-12

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.013V, neg = 0.013V # Test item 4-16- 3-13

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.575 V, 15.6% >> pos = 0.793V, neg = -0.782V # Test item 4-16- 3-14

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = -0.005V, neg = -0.004V # Test item 4-16- 3-15

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.795 V, 6.4% >> pos = 0.390V, neg = -0.404V # Test item 4-16- 3-16

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = -0.011V, neg = -0.010V # Test item 4-16- 3-17

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 13.0% >> pos = 0.113V, neg = -0.134V # Test item 4-16- 3-18

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.3% >> pos = -0.013V, neg = -0.013V # Test item 4-16- 3-19

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 7.1% >> pos = 0.037V, neg = -0.062V # Test item 4-16- 3-20

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.5% >> pos = 0.059V, neg = 0.061V # Test item 4-16- 3-21

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.189 V, 3.5% >> pos = 1.649V, neg = -1.540V # Test item 4-16- 3-22

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.035V, neg = 0.035V # Test item 4-16- 3-23

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.581 V, 12.1% >> pos = 0.829V, neg = -0.751V # Test item 4-16- 3-24

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.5% >> pos = 0.026V, neg = 0.024V # Test item 4-16- 3-25

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.790 V, 12.8% >> pos = 0.421V, neg = -0.369V # Test item 4-16- 3-26

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.2% >> pos = 0.020V, neg = 0.021V # Test item 4-16- 3-27

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 12.8% >> pos = 0.144V, neg = -0.103V # Test item 4-16- 3-28

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = 0.019V, neg = 0.017V # Test item 4-16- 3-29

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 6.2% >> pos = 0.067V, neg = -0.032V # Test item 4-16- 3-30

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.4% >> pos = 0.028V, neg = 0.023V # Test item 4-16- 3-31

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.182 V, 5.6% >> pos = 1.618V, neg = -1.564V # Test item 4-16- 3-32

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.0% >> pos = 0.004V, neg = 0.002V # Test item 4-16- 3-33

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.581 V, 11.7% >> pos = 0.796V, neg = -0.785V # Test item 4-16- 3-34

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.6% >> pos = -0.009V, neg = -0.005V # Test item 4-16- 3-35

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.791 V, 11.0% >> pos = 0.388V, neg = -0.403V # Test item 4-16- 3-36

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = -0.011V, neg = -0.011V # Test item 4-16- 3-37

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 12.5% >> pos = 0.113V, neg = -0.134V # Test item 4-16- 3-38

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = -0.012V, neg = -0.013V # Test item 4-16- 3-39

T AMCA: MESE 2254: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 4.6% >> pos = 0.037V, neg = -0.063V # Test item 4-16- 3-40

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.006 V, 6.5% >> pos = 0.046V, neg = 0.052V # Test item 5-16- 3- 1

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.169 V, 9.6% >> pos = 1.635V, neg = -1.534V # Test item 5-16- 3- 2

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = 0.030V, neg = 0.032V # Test item 5-16- 3- 3

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.577 V, 14.5% >> pos = 0.820V, neg = -0.757V # Test item 5-16- 3- 4

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 4.2% >> pos = 0.020V, neg = 0.024V # Test item 5-16- 3- 5

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.791 V, 11.4% >> pos = 0.416V, neg = -0.375V # Test item 5-16- 3- 6

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.5% >> pos = 0.016V, neg = 0.015V # Test item 5-16- 3- 7

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 8.8% >> pos = 0.141V, neg = -0.107V # Test item 5-16- 3- 8

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = 0.014V, neg = 0.013V # Test item 5-16- 3- 9

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 8.4% >> pos = 0.061V, neg = -0.037V # Test item 5-16- 3-10

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.2% >> pos = 0.030V, neg = 0.031V # Test item 5-16- 3-11

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.188 V, 3.7% >> pos = 1.624V, neg = -1.564V # Test item 5-16- 3-12

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.5% >> pos = 0.016V, neg = 0.008V # Test item 5-16- 3-13

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.573 V, 16.6% >> pos = 0.797V, neg = -0.777V # Test item 5-16- 3-14

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.4% >> pos = -0.001V, neg = 0.001V # Test item 5-16- 3-15

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.789 V, 13.5% >> pos = 0.396V, neg = -0.394V # Test item 5-16- 3-16

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = -0.006V, neg = -0.006V # Test item 5-16- 3-17

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 7.9% >> pos = 0.119V, neg = -0.129V # Test item 5-16- 3-18

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = -0.008V, neg = -0.008V # Test item 5-16- 3-19

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.4% >> pos = 0.043V, neg = -0.057V # Test item 5-16- 3-20

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.9% >> pos = 0.052V, neg = 0.056V # Test item 5-16- 3-21

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.174 V, 8.1% >> pos = 1.637V, neg = -1.537V # Test item 5-16- 3-22

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.017 V, 17.4% >> pos = 0.045V, neg = 0.027V # Test item 5-16- 3-23

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.573 V, 16.8% >> pos = 0.816V, neg = -0.757V # Test item 5-16- 3-24

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.6% >> pos = 0.023V, neg = 0.020V # Test item 5-16- 3-25

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.791 V, 11.8% >> pos = 0.415V, neg = -0.376V # Test item 5-16- 3-26

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.014V, neg = 0.014V # Test item 5-16- 3-27

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 9.3% >> pos = 0.139V, neg = -0.109V # Test item 5-16- 3-28

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.012V, neg = 0.012V # Test item 5-16- 3-29

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 7.9% >> pos = 0.061V, neg = -0.037V # Test item 5-16- 3-30

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.027V, neg = 0.027V # Test item 5-16- 3-31

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.185 V, 4.8% >> pos = 1.618V, neg = -1.566V # Test item 5-16- 3-32

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.8% >> pos = 0.011V, neg = 0.007V # Test item 5-16- 3-33

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.579 V, 13.3% >> pos = 0.798V, neg = -0.781V # Test item 5-16- 3-34

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 4.4% >> pos = -0.001V, neg = 0.003V # Test item 5-16- 3-35

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.789 V, 13.4% >> pos = 0.394V, neg = -0.395V # Test item 5-16- 3-36

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.5% >> pos = -0.007V, neg = -0.005V # Test item 5-16- 3-37

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 12.3% >> pos = 0.117V, neg = -0.130V # Test item 5-16- 3-38

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.3% >> pos = -0.007V, neg = -0.007V # Test item 5-16- 3-39

T AMCA: MESE 2255: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.097 V, 16.7% >> pos = 0.040V, neg = -0.056V # Test item 5-16- 3-40

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.6% >> pos = 0.047V, neg = 0.045V # Test item 6-16- 3- 1

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.174 V, 8.0% >> pos = 1.638V, neg = -1.536V # Test item 6-16- 3- 2

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.030V, neg = 0.030V # Test item 6-16- 3- 3

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.580 V, 12.3% >> pos = 0.821V, neg = -0.760V # Test item 6-16- 3- 4

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.5% >> pos = 0.023V, neg = 0.021V # Test item 6-16- 3- 5

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.793 V, 8.8% >> pos = 0.421V, neg = -0.372V # Test item 6-16- 3- 6

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = 0.016V, neg = 0.017V # Test item 6-16- 3- 7

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 9.8% >> pos = 0.140V, neg = -0.108V # Test item 6-16- 3- 8

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = 0.014V, neg = 0.015V # Test item 6-16- 3- 9

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.101 V, 3.9% >> pos = 0.065V, neg = -0.035V # Test item 6-16- 3-10

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.9% >> pos = 0.024V, neg = 0.027V # Test item 6-16- 3-11

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.194 V, 2.0% >> pos = 1.622V, neg = -1.571V # Test item 6-16- 3-12

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.7% >> pos = 0.015V, neg = 0.012V # Test item 6-16- 3-13

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.586 V, 8.8% >> pos = 0.800V, neg = -0.786V # Test item 6-16- 3-14

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.6% >> pos = 0.004V, neg = 0.000V # Test item 6-16- 3-15

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.796 V, 5.6% >> pos = 0.395V, neg = -0.400V # Test item 6-16- 3-16

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = -0.007V, neg = -0.008V # Test item 6-16- 3-17

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.246 V, 16.7% >> pos = 0.115V, neg = -0.131V # Test item 6-16- 3-18

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = -0.009V, neg = -0.010V # Test item 6-16- 3-19

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 5.5% >> pos = 0.039V, neg = -0.060V # Test item 6-16- 3-20

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.1% >> pos = 0.049V, neg = 0.051V # Test item 6-16- 3-21

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.185 V, 4.6% >> pos = 1.645V, neg = -1.541V # Test item 6-16- 3-22

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.4% >> pos = 0.031V, neg = 0.032V # Test item 6-16- 3-23

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.582 V, 11.2% >> pos = 0.820V, neg = -0.763V # Test item 6-16- 3-24

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.9% >> pos = 0.021V, neg = 0.023V # Test item 6-16- 3-25

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.795 V, 6.1% >> pos = 0.421V, neg = -0.374V # Test item 6-16- 3-26

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.012 V, 11.6% >> pos = 0.004V, neg = 0.015V # Test item 6-16- 3-27

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.252 V, 8.2% >> pos = 0.142V, neg = -0.110V # Test item 6-16- 3-28

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = 0.013V, neg = 0.014V # Test item 6-16- 3-29

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 4.3% >> pos = 0.064V, neg = -0.035V # Test item 6-16- 3-30

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.6% >> pos = 0.022V, neg = 0.024V # Test item 6-16- 3-31

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.205 V, 1.7% >> pos = 1.628V, neg = -1.577V # Test item 6-16- 3-32

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.0% >> pos = 0.009V, neg = 0.010V # Test item 6-16- 3-33

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.586 V, 9.0% >> pos = 0.799V, neg = -0.787V # Test item 6-16- 3-34

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.2% >> pos = -0.005V, neg = -0.003V # Test item 6-16- 3-35

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.796 V, 5.4% >> pos = 0.397V, neg = -0.399V # Test item 6-16- 3-36

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.014 V, 14.2% >> pos = -0.008V, neg = 0.006V # Test item 6-16- 3-37

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.245 V, 20.2% >> pos = 0.115V, neg = -0.130V # Test item 6-16- 3-38

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = -0.009V, neg = -0.009V # Test item 6-16- 3-39

T AMCA: MESE 2256: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.101 V, 3.4% >> pos = 0.040V, neg = -0.060V # Test item 6-16- 3-40

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.007 V, 6.8% >> pos = 0.073V, neg = 0.080V # Test item 7-16- 3- 1

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.187 V, 4.1% >> pos = 1.665V, neg = -1.522V # Test item 7-16- 3- 2

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 4.5% >> pos = 0.041V, neg = 0.045V # Test item 7-16- 3- 3

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.585 V, 9.2% >> pos = 0.836V, neg = -0.749V # Test item 7-16- 3- 4

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.4% >> pos = 0.032V, neg = 0.034V # Test item 7-16- 3- 5

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.794 V, 7.5% >> pos = 0.428V, neg = -0.366V # Test item 7-16- 3- 6

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.3% >> pos = 0.021V, neg = 0.021V # Test item 7-16- 3- 7

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 6.7% >> pos = 0.144V, neg = -0.104V # Test item 7-16- 3- 8

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.6% >> pos = 0.015V, neg = 0.016V # Test item 7-16- 3- 9

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.095 V, 24.7% >> pos = 0.063V, neg = -0.032V # Test item 7-16- 3-10

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.6% >> pos = 0.054V, neg = 0.047V # Test item 7-16- 3-11

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.202 V, 0.5% >> pos = 1.650V, neg = -1.551V # Test item 7-16- 3-12

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = 0.025V, neg = 0.024V # Test item 7-16- 3-13

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.585 V, 9.4% >> pos = 0.811V, neg = -0.774V # Test item 7-16- 3-14

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.007 V, 7.4% >> pos = 0.000V, neg = 0.008V # Test item 7-16- 3-15

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.797 V, 3.6% >> pos = 0.402V, neg = -0.395V # Test item 7-16- 3-16

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.0% >> pos = -0.005V, neg = -0.002V # Test item 7-16- 3-17

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 6.3% >> pos = 0.121V, neg = -0.128V # Test item 7-16- 3-18

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = -0.008V, neg = -0.009V # Test item 7-16- 3-19

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 5.7% >> pos = 0.042V, neg = -0.057V # Test item 7-16- 3-20

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.005 V, 4.8% >> pos = 0.072V, neg = 0.077V # Test item 7-16- 3-21

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.182 V, 5.8% >> pos = 1.669V, neg = -1.512V # Test item 7-16- 3-22

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.4% >> pos = 0.047V, neg = 0.045V # Test item 7-16- 3-23

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.578 V, 14.0% >> pos = 0.833V, neg = -0.744V # Test item 7-16- 3-24

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.0% >> pos = 0.032V, neg = 0.029V # Test item 7-16- 3-25

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.793 V, 9.2% >> pos = 0.427V, neg = -0.366V # Test item 7-16- 3-26

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.005 V, 4.7% >> pos = 0.017V, neg = 0.021V # Test item 7-16- 3-27

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.252 V, 9.8% >> pos = 0.144V, neg = -0.108V # Test item 7-16- 3-28

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.4% >> pos = 0.015V, neg = 0.017V # Test item 7-16- 3-29

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.102 V, 7.8% >> pos = 0.067V, neg = -0.034V # Test item 7-16- 3-30

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.4% >> pos = 0.049V, neg = 0.046V # Test item 7-16- 3-31

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.197 V, 1.0% >> pos = 1.652V, neg = -1.545V # Test item 7-16- 3-32

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.6% >> pos = 0.018V, neg = 0.017V # Test item 7-16- 3-33

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.589 V, 7.2% >> pos = 0.814V, neg = -0.775V # Test item 7-16- 3-34

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.6% >> pos = 0.005V, neg = 0.002V # Test item 7-16- 3-35

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.795 V, 6.4% >> pos = 0.404V, neg = -0.390V # Test item 7-16- 3-36

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = -0.006V, neg = -0.005V # Test item 7-16- 3-37

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 3.5% >> pos = 0.121V, neg = -0.130V # Test item 7-16- 3-38

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = -0.010V, neg = -0.009V # Test item 7-16- 3-39

T AMCA: MESE 2257: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 8.1% >> pos = 0.042V, neg = -0.056V # Test item 7-16- 3-40

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.057V, neg = 0.057V # Test item 8-16- 3- 1

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.195 V, 1.7% >> pos = 1.649V, neg = -1.545V # Test item 8-16- 3- 2

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% >> pos = 0.034V, neg = 0.033V # Test item 8-16- 3- 3

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.591 V, 5.8% >> pos = 0.833V, neg = -0.758V # Test item 8-16- 3- 4

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = 0.026V, neg = 0.025V # Test item 8-16- 3- 5

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.791 V, 11.3% >> pos = 0.420V, neg = -0.371V # Test item 8-16- 3- 6

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.020V, neg = 0.020V # Test item 8-16- 3- 7

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 5.7% >> pos = 0.142V, neg = -0.106V # Test item 8-16- 3- 8

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = 0.017V, neg = 0.016V # Test item 8-16- 3- 9

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.7% >> pos = 0.068V, neg = -0.032V # Test item 8-16- 3-10

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.9% >> pos = 0.024V, neg = 0.025V # Test item 8-16- 3-11

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.201 V, 0.3% >> pos = 1.627V, neg = -1.574V # Test item 8-16- 3-12

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.1% >> pos = 0.012V, neg = 0.014V # Test item 8-16- 3-13

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.583 V, 10.4% >> pos = 0.800V, neg = -0.783V # Test item 8-16- 3-14

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = -0.002V, neg = -0.002V # Test item 8-16- 3-15

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.797 V, 3.7% >> pos = 0.396V, neg = -0.401V # Test item 8-16- 3-16

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = -0.009V, neg = -0.009V # Test item 8-16- 3-17

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 2.4% >> pos = 0.116V, neg = -0.133V # Test item 8-16- 3-18

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = -0.010V, neg = -0.010V # Test item 8-16- 3-19

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 7.3% >> pos = 0.039V, neg = -0.060V # Test item 8-16- 3-20

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.6% >> pos = 0.051V, neg = 0.055V # Test item 8-16- 3-21

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.205 V, 1.4% >> pos = 1.649V, neg = -1.556V # Test item 8-16- 3-22

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = 0.034V, neg = 0.035V # Test item 8-16- 3-23

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.589 V, 7.1% >> pos = 0.829V, neg = -0.760V # Test item 8-16- 3-24

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.5% >> pos = 0.029V, neg = 0.025V # Test item 8-16- 3-25

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.802 V, 2.0% >> pos = 0.424V, neg = -0.377V # Test item 8-16- 3-26

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = 0.020V, neg = 0.019V # Test item 8-16- 3-27

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 0.6% >> pos = 0.145V, neg = -0.105V # Test item 8-16- 3-28

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.017V, neg = 0.017V # Test item 8-16- 3-29

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 8.3% >> pos = 0.065V, neg = -0.033V # Test item 8-16- 3-30

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.4% >> pos = 0.024V, neg = 0.025V # Test item 8-16- 3-31

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.195 V, 1.5% >> pos = 1.626V, neg = -1.569V # Test item 8-16- 3-32

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.6% >> pos = 0.007V, neg = 0.010V # Test item 8-16- 3-33

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.588 V, 7.7% >> pos = 0.803V, neg = -0.785V # Test item 8-16- 3-34

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = -0.004V, neg = -0.003V # Test item 8-16- 3-35

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.799 V, 1.4% >> pos = 0.396V, neg = -0.402V # Test item 8-16- 3-36

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = -0.007V, neg = -0.009V # Test item 8-16- 3-37

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 12.8% >> pos = 0.116V, neg = -0.130V # Test item 8-16- 3-38

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = -0.011V, neg = -0.010V # Test item 8-16- 3-39

T AMCA: MESE 2258: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.9% >> pos = 0.040V, neg = -0.060V # Test item 8-16- 3-40

T AMCA: MESE 2251: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.775 V, 24.5% >> POS = 0.801V, NEG = 0.025V # Test item 1-16- 4- 1

T AMCA: MESE 2251: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.760 V, 40.1% >> POS = 0.784V, NEG = 0.024V # Test item 1-16- 4- 2

T AMCA: MESE 2251: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.741 V, 58.9% >> POS = 0.756V, NEG = 0.015V # Test item 1-16- 4- 3

T AMCA: MESE 2251: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.789 V, 11.3% >> POS = 0.820V, NEG = 0.031V # Test item 1-16- 4- 4

T AMCA: MESE 2252: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.775 V, 25.4% >> POS = 0.815V, NEG = 0.040V # Test item 2-16- 4- 1

T AMCA: MESE 2252: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.760 V, 40.5% >> POS = 0.797V, NEG = 0.038V # Test item 2-16- 4- 2

T AMCA: MESE 2252: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.744 V, 55.8% >> POS = 0.773V, NEG = 0.029V # Test item 2-16- 4- 3

T AMCA: MESE 2252: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.787 V, 13.0% >> POS = 0.834V, NEG = 0.047V # Test item 2-16- 4- 4

T AMCA: MESE 2253: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.776 V, 24.1% >> POS = 0.810V, NEG = 0.034V # Test item 3-16- 4- 1

T AMCA: MESE 2253: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.761 V, 38.7% >> POS = 0.792V, NEG = 0.030V # Test item 3-16- 4- 2

T AMCA: MESE 2253: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.741 V, 58.9% >> POS = 0.764V, NEG = 0.023V # Test item 3-16- 4- 3

T AMCA: MESE 2253: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.796 V, 3.9% >> POS = 0.832V, NEG = 0.035V # Test item 3-16- 4- 4

T AMCA: MESE 2254: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.776 V, 23.8% >> POS = 0.810V, NEG = 0.034V # Test item 4-16- 4- 1

T AMCA: MESE 2254: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.760 V, 39.6% >> POS = 0.792V, NEG = 0.031V # Test item 4-16- 4- 2

T AMCA: MESE 2254: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.743 V, 57.1% >> POS = 0.764V, NEG = 0.021V # Test item 4-16- 4- 3

T AMCA: MESE 2254: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.791 V, 9.3% >> POS = 0.827V, NEG = 0.036V # Test item 4-16- 4- 4

T AMCA: MESE 2255: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.775 V, 24.8% >> POS = 0.800V, NEG = 0.025V # Test item 5-16- 4- 1

T AMCA: MESE 2255: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.760 V, 40.5% >> POS = 0.783V, NEG = 0.024V # Test item 5-16- 4- 2

T AMCA: MESE 2255: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.740 V, 59.8% >> POS = 0.755V, NEG = 0.015V # Test item 5-16- 4- 3

T AMCA: MESE 2255: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.790 V, 9.6% >> POS = 0.820V, NEG = 0.029V # Test item 5-16- 4- 4

T AMCA: MESE 2256: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.783 V, 17.4% >> POS = 0.807V, NEG = 0.025V # Test item 6-16- 4- 1

T AMCA: MESE 2256: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.758 V, 42.0% >> POS = 0.782V, NEG = 0.024V # Test item 6-16- 4- 2

T AMCA: MESE 2256: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.742 V, 58.3% >> POS = 0.758V, NEG = 0.016V # Test item 6-16- 4- 3

T AMCA: MESE 2256: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.791 V, 9.0% >> POS = 0.826V, NEG = 0.035V # Test item 6-16- 4- 4

T AMCA: MESE 2257: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.774 V, 26.3% >> POS = 0.813V, NEG = 0.039V # Test item 7-16- 4- 1

T AMCA: MESE 2257: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.762 V, 38.2% >> POS = 0.802V, NEG = 0.040V # Test item 7-16- 4- 2

T AMCA: MESE 2257: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.742 V, 58.3% >> POS = 0.770V, NEG = 0.028V # Test item 7-16- 4- 3

T AMCA: MESE 2257: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.792 V, 8.3% >> POS = 0.834V, NEG = 0.042V # Test item 7-16- 4- 4

T AMCA: MESE 2258: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.780 V, 20.2% >> POS = 0.807V, NEG = 0.028V # Test item 8-16- 4- 1

T AMCA: MESE 2258: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.762 V, 38.4% >> POS = 0.791V, NEG = 0.030V # Test item 8-16- 4- 2

T AMCA: MESE 2258: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.743 V, 56.9% >> POS = 0.763V, NEG = 0.020V # Test item 8-16- 4- 3

T AMCA: MESE 2258: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.791 V, 9.0% >> POS = 0.828V, NEG = 0.037V # Test item 8-16- 4- 4

T AMCA: MESE 2251: Signal\_Input/Output\_relay\_test\_Resistance\_A+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5030.869 Ohm, 1.4% >> vOffset = -0.048V, vMeas = 2.467V, iForce = 0.500mA # Test item 1-20- 1- 1

T AMCA: MESE 2251: Signal\_Input/Output\_relay\_test\_Resistance\_A-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5032.127 Ohm, 1.6% >> vOffset = -0.049V, vMeas = 2.467V, iForce = 0.500mA # Test item 1-20- 1- 2

T AMCA: MESE 2251: Signal\_Input/Output\_relay\_test\_Resistance\_AA+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1006.828 Ohm, 6.8% >> vOffset = -0.005V, vMeas = 0.498V, iForce = 0.500mA # Test item 1-20- 1- 3

T AMCA: MESE 2251: Signal\_Input/Output\_relay\_test\_Resistance\_AA-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1008.591 Ohm, 8.6% >> vOffset = -0.005V, vMeas = 0.499V, iForce = 0.500mA # Test item 1-20- 1- 4

T AMCA: MESE 2252: Signal\_Input/Output\_relay\_test\_Resistance\_B+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5028.351 Ohm, 0.9% >> vOffset = -0.055V, vMeas = 2.459V, iForce = 0.500mA # Test item 2-20- 1- 1

T AMCA: MESE 2252: Signal\_Input/Output\_relay\_test\_Resistance\_B-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5027.596 Ohm, 0.7% >> vOffset = -0.056V, vMeas = 2.458V, iForce = 0.500mA # Test item 2-20- 1- 2

T AMCA: MESE 2252: Signal\_Input/Output\_relay\_test\_Resistance\_BB+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1006.828 Ohm, 6.8% >> vOffset = -0.010V, vMeas = 0.494V, iForce = 0.500mA # Test item 2-20- 1- 3

T AMCA: MESE 2252: Signal\_Input/Output\_relay\_test\_Resistance\_BB-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1008.591 Ohm, 8.6% >> vOffset = -0.010V, vMeas = 0.495V, iForce = 0.500mA # Test item 2-20- 1- 4

T AMCA: MESE 2253: Signal\_Input/Output\_relay\_test\_Resistance\_C+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5036.156 Ohm, 2.4% >> vOffset = -0.059V, vMeas = 2.459V, iForce = 0.500mA # Test item 3-20- 1- 1

T AMCA: MESE 2253: Signal\_Input/Output\_relay\_test\_Resistance\_C-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5036.911 Ohm, 2.6% >> vOffset = -0.059V, vMeas = 2.459V, iForce = 0.500mA # Test item 3-20- 1- 2

T AMCA: MESE 2253: Signal\_Input/Output\_relay\_test\_Resistance\_CC+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1007.332 Ohm, 7.3% >> vOffset = -0.010V, vMeas = 0.494V, iForce = 0.500mA # Test item 3-20- 1- 3

T AMCA: MESE 2253: Signal\_Input/Output\_relay\_test\_Resistance\_CC-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1008.087 Ohm, 8.1% >> vOffset = -0.010V, vMeas = 0.494V, iForce = 0.500mA # Test item 3-20- 1- 4

T AMCA: MESE 2254: Signal\_Input/Output\_relay\_test\_Resistance\_D+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5025.582 Ohm, 0.3% >> vOffset = -0.059V, vMeas = 2.453V, iForce = 0.500mA # Test item 4-20- 1- 1

T AMCA: MESE 2254: Signal\_Input/Output\_relay\_test\_Resistance\_D-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5024.826 Ohm, 0.2% >> vOffset = -0.060V, vMeas = 2.453V, iForce = 0.500mA # Test item 4-20- 1- 2

T AMCA: MESE 2254: Signal\_Input/Output\_relay\_test\_Resistance\_DD+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1007.584 Ohm, 7.6% >> vOffset = -0.007V, vMeas = 0.497V, iForce = 0.500mA # Test item 4-20- 1- 3

T AMCA: MESE 2254: Signal\_Input/Output\_relay\_test\_Resistance\_DD-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1005.066 Ohm, 5.1% >> vOffset = -0.007V, vMeas = 0.496V, iForce = 0.500mA # Test item 4-20- 1- 4

T AMCA: MESE 2255: Signal\_Input/Output\_relay\_test\_Resistance\_E+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5029.358 Ohm, 1.1% >> vOffset = -0.049V, vMeas = 2.465V, iForce = 0.500mA # Test item 5-20- 1- 1

T AMCA: MESE 2255: Signal\_Input/Output\_relay\_test\_Resistance\_E-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5026.840 Ohm, 0.6% >> vOffset = -0.049V, vMeas = 2.464V, iForce = 0.500mA # Test item 5-20- 1- 2

T AMCA: MESE 2255: Signal\_Input/Output\_relay\_test\_Resistance\_EE+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5027.344 Ohm, 0.7% >> vOffset = -0.049V, vMeas = 2.464V, iForce = 0.500mA # Test item 5-20- 1- 3

T AMCA: MESE 2255: Signal\_Input/Output\_relay\_test\_Resistance\_EE-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5029.861 Ohm, 1.2% >> vOffset = -0.049V, vMeas = 2.466V, iForce = 0.500mA # Test item 5-20- 1- 4

T AMCA: MESE 2256: Signal\_Input/Output\_relay\_test\_Resistance\_F+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5021.049 Ohm, 0.6% >> vOffset = -0.055V, vMeas = 2.455V, iForce = 0.500mA # Test item 6-20- 1- 1

T AMCA: MESE 2256: Signal\_Input/Output\_relay\_test\_Resistance\_F-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5021.301 Ohm, 0.5% >> vOffset = -0.055V, vMeas = 2.455V, iForce = 0.500mA # Test item 6-20- 1- 2

T AMCA: MESE 2256: Signal\_Input/Output\_relay\_test\_Resistance\_FF+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5019.791 Ohm, 0.8% >> vOffset = -0.055V, vMeas = 2.455V, iForce = 0.500mA # Test item 6-20- 1- 3

T AMCA: MESE 2256: Signal\_Input/Output\_relay\_test\_Resistance\_FF-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5019.791 Ohm, 0.8% >> vOffset = -0.054V, vMeas = 2.456V, iForce = 0.500mA # Test item 6-20- 1- 4

T AMCA: MESE 2257: Signal\_Input/Output\_relay\_test\_Resistance\_G+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5022.812 Ohm, 0.2% >> vOffset = -0.055V, vMeas = 2.457V, iForce = 0.500mA # Test item 7-20- 1- 1

T AMCA: MESE 2257: Signal\_Input/Output\_relay\_test\_Resistance\_G-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5023.567 Ohm, 0.1% >> vOffset = -0.056V, vMeas = 2.456V, iForce = 0.500mA # Test item 7-20- 1- 2

T AMCA: MESE 2257: Signal\_Input/Output\_relay\_test\_Resistance\_GG+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5023.063 Ohm, 0.2% >> vOffset = -0.055V, vMeas = 2.457V, iForce = 0.500mA # Test item 7-20- 1- 3

T AMCA: MESE 2257: Signal\_Input/Output\_relay\_test\_Resistance\_GG-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5023.819 Ohm, 0.0% >> vOffset = -0.055V, vMeas = 2.457V, iForce = 0.500mA # Test item 7-20- 1- 4

T AMCA: MESE 2258: Signal\_Input/Output\_relay\_test\_Resistance\_H+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5027.847 Ohm, 0.8% >> vOffset = -0.059V, vMeas = 2.455V, iForce = 0.500mA # Test item 8-20- 1- 1

T AMCA: MESE 2258: Signal\_Input/Output\_relay\_test\_Resistance\_H-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5028.603 Ohm, 0.9% >> vOffset = -0.059V, vMeas = 2.456V, iForce = 0.500mA # Test item 8-20- 1- 2

T AMCA: MESE 2258: Signal\_Input/Output\_relay\_test\_Resistance\_HH+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5026.085 Ohm, 0.4% >> vOffset = -0.058V, vMeas = 2.455V, iForce = 0.500mA # Test item 8-20- 1- 3

T AMCA: MESE 2258: Signal\_Input/Output\_relay\_test\_Resistance\_HH-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5026.840 Ohm, 0.6% >> vOffset = -0.058V, vMeas = 2.455V, iForce = 0.500mA # Test item 8-20- 1- 4

T AMCA: MCE 2250: Common\_trigger\_input\_relay\_test\_Common\_Trigger\_1: R\_exp = 470.000 Ohm, [369.000 ... 571.000 Ohm] : R\_meas = 471.439 Ohm, 1.4% >> vMeas = 1.414V, vOffset = -0.000V, usedUnit = 1 # Test item 0-21- 1- 1

T AMCA: MCE 2250: Common\_trigger\_input\_relay\_test\_Common\_Trigger\_2: R\_exp = 470.000 Ohm, [369.000 ... 571.000 Ohm] : R\_meas = 472.530 Ohm, 2.5% >> vMeas = 1.418V, vOffset = 0.000V, usedUnit = 1 # Test item 0-21- 1- 2

T AMCA: MCE 2260: Unit\_FPGA\_configuration\_test\_Unit\_1: exp = passed: meas = passed # Test item 1- 1- 1- 1

T AMCA: MCE 2260: Unit\_FPGA\_configuration\_test\_Unit\_2: exp = passed: meas = passed # Test item 2- 1- 1- 1

T AMCA: MCE 2260: Unit\_FPGA\_configuration\_test\_Unit\_3: exp = passed: meas = passed # Test item 3- 1- 1- 1

T AMCA: MCE 2260: Unit\_FPGA\_configuration\_test\_Unit\_4: exp = passed: meas = passed # Test item 4- 1- 1- 1

T AMCA: MCE 2260: Unit\_FPGA\_configuration\_test\_Unit\_5: exp = passed: meas = passed # Test item 5- 1- 1- 1

T AMCA: MCE 2260: Unit\_FPGA\_configuration\_test\_Unit\_6: exp = passed: meas = passed # Test item 6- 1- 1- 1

T AMCA: MCE 2260: Unit\_FPGA\_configuration\_test\_Unit\_7: exp = passed: meas = passed # Test item 7- 1- 1- 1

T AMCA: MCE 2260: Unit\_FPGA\_configuration\_test\_Unit\_8: exp = passed: meas = passed # Test item 8- 1- 1- 1

T AMCA: MCE 2260: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Data\_Bus\_Test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 0- 1- 2- 1

T AMCA: MCE 2260: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Data\_Bus\_Test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 1- 2- 2

T AMCA: MCE 2260: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 1- 2- 3

T AMCA: MESE 2261: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 1- 8- 1

T AMCA: MESE 2262: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 1- 8- 1

T AMCA: MESE 2263: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 1- 8- 1

T AMCA: MESE 2264: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 1- 8- 1

T AMCA: MESE 2265: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 1- 8- 1

T AMCA: MESE 2266: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 1- 8- 1

T AMCA: MESE 2267: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 1- 8- 1

T AMCA: MESE 2268: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 1- 8- 1

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_DGND\_B\_(U10144): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.004 V, 0.8% # Test item 0- 1- 3- 1

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_(U10145): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.007 V, 1.5% # Test item 1- 1- 3- 2

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_IF\_(U10145): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.024 V, 4.8% # Test item 2- 1- 3- 3

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_(U811): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.002 V, 0.4% # Test item 3- 1- 3- 4

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.963 V, 17.3% # Test item 0- 1- 3- 5

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 0.989 V, 20.8% # Test item 1- 1- 3- 6

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_B: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.983 V, 3.1% # Test item 2- 1- 3- 7

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_B: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.015 V, 5.0% # Test item 3- 1- 3- 8

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5VD\_B: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.978 V, 18.8% # Test item 4- 1- 3- 9

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_B: V\_exp = 1.020 V, [0.918 ... 1.122 V] : V\_meas = 1.030 V, 9.8% # Test item 5- 1- 3-10

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_TEMP\_BUSIF: V\_exp = 0.750 V, [0.500 ... 1.000 V] : V\_meas = 0.625 V, 50.0% # Test item 6- 1- 3-11

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5.5VA: V\_exp = 1.050 V, [0.945 ... 1.155 V] : V\_meas = 1.048 V, 2.4% # Test item 7- 1- 3-12

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5.5VA: V\_exp = -1.140 V, [-1.254 ... -1.026 V] : V\_meas = -1.132 V, 7.5% # Test item 8- 1- 3-13

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+10VA: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 1.003 V, 2.5% # Test item 9- 1- 3-14

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.2VA\_IF: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.988 V, 12.5% # Test item 10- 1- 3-15

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U13: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.967 V, 23.2% # Test item 11- 1- 3-16

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U24: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.968 V, 22.2% # Test item 12- 1- 3-17

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U57: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.969 V, 21.2% # Test item 13- 1- 3-18

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U68: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.971 V, 19.2% # Test item 14- 1- 3-19

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U13: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.997 V, 7.1% # Test item 15- 1- 3-20

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U24: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.988 V, 2.0% # Test item 16- 1- 3-21

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U57: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.000 V, 10.1% # Test item 17- 1- 3-22

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U68: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.988 V, 2.0% # Test item 18- 1- 3-23

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_F\_TG: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.011 V, 27.9% # Test item 19- 1- 3-24

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.5VD\_F\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.977 V, 23.0% # Test item 20- 1- 3-25

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_F: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.005 V, 33.7% # Test item 21- 1- 3-26

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.5VD\_F: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.984 V, 16.0% # Test item 22- 1- 3-27

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5VD\_F: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.954 V, 6.2% # Test item 23- 1- 3-28

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5VA\_F: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.962 V, 2.1% # Test item 24- 1- 3-29

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5VA\_F: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.966 V, 6.3% # Test item 25- 1- 3-30

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VA\_F: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.006 V, 32.7% # Test item 26- 1- 3-31

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.970 V, 10.2% # Test item 27- 1- 3-32

T AMCA: MCE 2260: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_IF\_IF2: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.978 V, 22.0% # Test item 28- 1- 3-33

T AMCA: MCE 2260: Unit\_bus\_data\_line\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 0- 1- 4- 1

T AMCA: MCE 2260: Unit\_bus\_data\_line\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 1- 4- 2

T AMCA: MCE 2260: Unit\_bus\_address/control\_line\_test\_WE\_line\_and\_bit\_0\_of\_addr.\_bus: Reg\_exp = 0x00030002: Reg\_meas = 0x00030002 # Test item 0- 1- 5- 1

T AMCA: MCE 2260: Unit\_bus\_address/control\_line\_test\_OE\_line\_\_unit\_select\_line\_for\_unit\_1--4\_and\_bit\_1\_of\_addr.\_bus: Reg\_exp = 0x0002F004: Reg\_meas = 0x0002F004 # Test item 1- 1- 5- 2

T AMCA: MCE 2260: Unit\_bus\_address/control\_line\_test\_Unit\_select\_line\_for\_unit\_5--8\_and\_bit\_2\_of\_addr.\_bus: Reg\_exp = 0x00020F08: Reg\_meas = 0x00020F08 # Test item 2- 1- 5- 3

T AMCA: MCE 2260: Unit\_bus\_address/control\_line\_test\_Reset: Reg\_exp = 0x00060000: Reg\_meas = 0x00060000 # Test item 3- 1- 5- 4

T AMCA: MCE 2260: Analog\_interface\_module\_EEPROM\_access\_test\_Analog\_Interface\_Module: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 0- 1- 6- 1

T AMCA: MCE 2260: Front\_module\_serial\_bus\_test\_1k\_Ohm\_Rref\_connect\_relay\_on\_test: Reg\_exp = 0x00000003: Reg\_meas = 0x00000003 # Test item 0- 1- 7- 1

T AMCA: MCE 2260: Temperature\_sensor\_test\_START: V\_exp = 0.912 V, [0.450 ... 1.375 V] : V\_meas = 0.807 V, 22.8% >> degree = 34.280degree # Test item 0- 2- 3- 1

T AMCA: MCE 2260: MCLK\_test\_START: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.019 V, 19.0% >> D\_MCLK\_DC = 0.935V, D\_MCLK\_DC\* = 0.954V # Test item 0- 2- 4- 1

T AMCA: MCE 2260: MCLK\_test\_STOP: V\_exp = 0.300 V, [0.200 ... 19.980 V] : V\_meas = 0.336 V, 0.2% >> D\_MCLK\_DC = 0.756V, D\_MCLK\_DC\* = 1.092V # Test item 0- 2- 4- 2

T AMCA: MCE 2260: DC\_rail\_path\_test: R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1000.833 Ohm, 0.8% # Test item 0- 2- 8- 1

T AMCA: MCE 2260: DC\_rail\_path\_test\_GND\_connection\_switch\_On: R\_exp = 5.000 Ohm, [0.000 ... 10.000 Ohm] : R\_meas = 0.917 Ohm, 81.7% # Test item 0- 2- 8- 2

T AMCA: MESE 2261: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 3- 1- 1

T AMCA: MESE 2262: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 3- 1- 1

T AMCA: MESE 2263: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 3- 1- 1

T AMCA: MESE 2264: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 3- 1- 1

T AMCA: MESE 2265: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 3- 1- 1

T AMCA: MESE 2266: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 3- 1- 1

T AMCA: MESE 2267: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 3- 1- 1

T AMCA: MESE 2268: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 3- 1- 1

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.984 V, 4.6% # Test item 1- 3- 2- 1

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.980 V, 0.5% # Test item 1- 3- 2- 2

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.030 V, 20.3% # Test item 1- 3- 2- 3

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.013 V, 3.0% # Test item 1- 3- 2- 4

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.979 V, 1.0% # Test item 1- 3- 2- 5

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.978 V, 22.0% # Test item 1- 3- 2- 6

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.953 V, 7.0% # Test item 1- 3- 2- 7

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.964 V, 3.9% # Test item 1- 3- 2- 8

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.016 V, 36.2% # Test item 2- 3- 2- 1

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.013 V, 34.2% # Test item 2- 3- 2- 2

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.053 V, 42.1% # Test item 2- 3- 2- 3

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.003 V, 6.9% # Test item 2- 3- 2- 4

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.975 V, 5.1% # Test item 2- 3- 2- 5

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.981 V, 19.0% # Test item 2- 3- 2- 6

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.965 V, 5.5% # Test item 2- 3- 2- 7

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.963 V, 2.9% # Test item 2- 3- 2- 8

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.011 V, 32.1% # Test item 3- 3- 2- 1

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.008 V, 29.1% # Test item 3- 3- 2- 2

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.049 V, 38.1% # Test item 3- 3- 2- 3

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.007 V, 3.0% # Test item 3- 3- 2- 4

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.983 V, 3.1% # Test item 3- 3- 2- 5

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.975 V, 25.0% # Test item 3- 3- 2- 6

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.959 V, 0.8% # Test item 3- 3- 2- 7

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.971 V, 11.2% # Test item 3- 3- 2- 8

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.988 V, 8.7% # Test item 4- 3- 2- 1

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.983 V, 2.6% # Test item 4- 3- 2- 2

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.031 V, 21.3% # Test item 4- 3- 2- 3

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.009 V, 1.0% # Test item 4- 3- 2- 4

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.973 V, 7.1% # Test item 4- 3- 2- 5

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.979 V, 21.0% # Test item 4- 3- 2- 6

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.951 V, 9.1% # Test item 4- 3- 2- 7

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.974 V, 14.3% # Test item 4- 3- 2- 8

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.013 V, 33.2% # Test item 5- 3- 2- 1

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.023 V, 43.4% # Test item 5- 3- 2- 2

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.046 V, 36.1% # Test item 5- 3- 2- 3

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.005 V, 5.0% # Test item 5- 3- 2- 4

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.979 V, 1.0% # Test item 5- 3- 2- 5

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.977 V, 23.0% # Test item 5- 3- 2- 6

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.972 V, 12.8% # Test item 5- 3- 2- 7

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.960 V, 0.3% # Test item 5- 3- 2- 8

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.003 V, 24.0% # Test item 6- 3- 2- 1

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.021 V, 42.3% # Test item 6- 3- 2- 2

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.052 V, 41.1% # Test item 6- 3- 2- 3

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.010 V, 0.0% # Test item 6- 3- 2- 4

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.975 V, 5.1% # Test item 6- 3- 2- 5

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.980 V, 20.0% # Test item 6- 3- 2- 6

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.963 V, 3.4% # Test item 6- 3- 2- 7

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.997 V, 38.3% # Test item 6- 3- 2- 8

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.014 V, 35.2% # Test item 7- 3- 2- 1

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.013 V, 33.2% # Test item 7- 3- 2- 2

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.062 V, 52.0% # Test item 7- 3- 2- 3

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.007 V, 3.0% # Test item 7- 3- 2- 4

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.985 V, 5.1% # Test item 7- 3- 2- 5

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.977 V, 23.0% # Test item 7- 3- 2- 6

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.960 V, 0.3% # Test item 7- 3- 2- 7

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.949 V, 11.7% # Test item 7- 3- 2- 8

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.016 V, 36.2% # Test item 8- 3- 2- 1

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.024 V, 45.4% # Test item 8- 3- 2- 2

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.059 V, 49.0% # Test item 8- 3- 2- 3

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.006 V, 4.0% # Test item 8- 3- 2- 4

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.979 V, 1.0% # Test item 8- 3- 2- 5

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.985 V, 15.0% # Test item 8- 3- 2- 6

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.964 V, 4.4% # Test item 8- 3- 2- 7

T AMCA: MCE 2260: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.962 V, 1.8% # Test item 8- 3- 2- 8

T AMCA: MESE 2261: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 3- 3- 1

T AMCA: MESE 2262: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 3- 3- 1

T AMCA: MESE 2263: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 3- 3- 1

T AMCA: MESE 2264: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 3- 3- 1

T AMCA: MESE 2265: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 3- 3- 1

T AMCA: MESE 2266: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 3- 3- 1

T AMCA: MESE 2267: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 3- 3- 1

T AMCA: MESE 2268: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 3- 3- 1

T AMCA: MESE 2261: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 1- 3- 4- 1

T AMCA: MESE 2261: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 3- 4- 2

T AMCA: MESE 2262: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 2- 3- 4- 1

T AMCA: MESE 2262: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 2- 3- 4- 2

T AMCA: MESE 2263: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 3- 3- 4- 1

T AMCA: MESE 2263: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 3- 3- 4- 2

T AMCA: MESE 2264: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 4- 3- 4- 1

T AMCA: MESE 2264: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 4- 3- 4- 2

T AMCA: MESE 2265: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 5- 3- 4- 1

T AMCA: MESE 2265: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 5- 3- 4- 2

T AMCA: MESE 2266: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 6- 3- 4- 1

T AMCA: MESE 2266: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 6- 3- 4- 2

T AMCA: MESE 2267: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 7- 3- 4- 1

T AMCA: MESE 2267: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 7- 3- 4- 2

T AMCA: MESE 2268: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 8- 3- 4- 1

T AMCA: MESE 2268: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 8- 3- 4- 2

T AMCA: MESE 2261: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 3- 5- 1

T AMCA: MESE 2262: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 3- 5- 1

T AMCA: MESE 2263: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 3- 5- 1

T AMCA: MESE 2264: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 3- 5- 1

T AMCA: MESE 2265: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 3- 5- 1

T AMCA: MESE 2266: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 3- 5- 1

T AMCA: MESE 2267: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 3- 5- 1

T AMCA: MESE 2268: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 3- 5- 1

T AMCA: MESE 2261: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 3- 5- 2

T AMCA: MESE 2262: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 3- 5- 2

T AMCA: MESE 2263: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 3- 5- 2

T AMCA: MESE 2264: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 3- 5- 2

T AMCA: MESE 2265: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 3- 5- 2

T AMCA: MESE 2266: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 3- 5- 2

T AMCA: MESE 2267: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 3- 5- 2

T AMCA: MESE 2268: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 3- 5- 2

T AMCA: MESE 2261: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 3- 5- 3

T AMCA: MESE 2262: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 3- 5- 3

T AMCA: MESE 2263: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 3- 5- 3

T AMCA: MESE 2264: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 3- 5- 3

T AMCA: MESE 2265: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 3- 5- 3

T AMCA: MESE 2266: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 3- 5- 3

T AMCA: MESE 2267: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 3- 5- 3

T AMCA: MESE 2268: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 3- 5- 3

T AMCA: MESE 2261: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 3- 5- 4

T AMCA: MESE 2262: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 3- 5- 4

T AMCA: MESE 2263: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 3- 5- 4

T AMCA: MESE 2264: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 3- 5- 4

T AMCA: MESE 2265: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 3- 5- 4

T AMCA: MESE 2266: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 3- 5- 4

T AMCA: MESE 2267: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 3- 5- 4

T AMCA: MESE 2268: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 3- 5- 4

T AMCA: MESE 2261: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 1- 3- 6- 1

T AMCA: MESE 2262: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 2- 3- 6- 1

T AMCA: MESE 2263: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 3- 3- 6- 1

T AMCA: MESE 2264: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 4- 3- 6- 1

T AMCA: MESE 2265: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 5- 3- 6- 1

T AMCA: MESE 2266: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 6- 3- 6- 1

T AMCA: MESE 2267: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 7- 3- 6- 1

T AMCA: MESE 2268: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 8- 3- 6- 1

T AMCA: MESE 2261: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 1- 3- 6- 2

T AMCA: MESE 2262: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 2- 3- 6- 2

T AMCA: MESE 2263: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 3- 3- 6- 2

T AMCA: MESE 2264: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 4- 3- 6- 2

T AMCA: MESE 2265: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 5- 3- 6- 2

T AMCA: MESE 2266: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 6- 3- 6- 2

T AMCA: MESE 2267: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 7- 3- 6- 2

T AMCA: MESE 2268: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 8- 3- 6- 2

T AMCA: MESE 2261: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 3- 7- 1

T AMCA: MESE 2262: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 3- 7- 1

T AMCA: MESE 2263: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 3- 7- 1

T AMCA: MESE 2264: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 3- 7- 1

T AMCA: MESE 2265: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 3- 7- 1

T AMCA: MESE 2266: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 3- 7- 1

T AMCA: MESE 2267: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 3- 7- 1

T AMCA: MESE 2268: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 3- 7- 1

T AMCA: MESE 2261: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 1-22- 1- 1

T AMCA: MESE 2261: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 1-22- 1- 2

T AMCA: MESE 2262: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 2-22- 1- 1

T AMCA: MESE 2262: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 2-22- 1- 2

T AMCA: MESE 2263: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 3-22- 1- 1

T AMCA: MESE 2263: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 3-22- 1- 2

T AMCA: MESE 2264: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 4-22- 1- 1

T AMCA: MESE 2264: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 4-22- 1- 2

T AMCA: MESE 2265: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 5-22- 1- 1

T AMCA: MESE 2265: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 5-22- 1- 2

T AMCA: MESE 2266: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 6-22- 1- 1

T AMCA: MESE 2266: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 6-22- 1- 2

T AMCA: MESE 2267: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 7-22- 1- 1

T AMCA: MESE 2267: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 7-22- 1- 2

T AMCA: MESE 2268: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 8-22- 1- 1

T AMCA: MESE 2268: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 8-22- 1- 2

T AMCA: MESE 2261: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.899 V, 10.2% # Test item 1- 4- 1- 1

T AMCA: MESE 2261: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.842 V, 9.9% # Test item 1- 4- 1- 2

T AMCA: MESE 2262: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.897 V, 9.5% # Test item 2- 4- 1- 1

T AMCA: MESE 2262: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.838 V, 11.1% # Test item 2- 4- 1- 2

T AMCA: MESE 2263: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.899 V, 10.2% # Test item 3- 4- 1- 1

T AMCA: MESE 2263: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.834 V, 12.5% # Test item 3- 4- 1- 2

T AMCA: MESE 2264: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.901 V, 10.7% # Test item 4- 4- 1- 1

T AMCA: MESE 2264: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.841 V, 10.3% # Test item 4- 4- 1- 2

T AMCA: MESE 2265: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.903 V, 11.6% # Test item 5- 4- 1- 1

T AMCA: MESE 2265: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.839 V, 10.7% # Test item 5- 4- 1- 2

T AMCA: MESE 2266: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.902 V, 11.1% # Test item 6- 4- 1- 1

T AMCA: MESE 2266: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.839 V, 10.6% # Test item 6- 4- 1- 2

T AMCA: MESE 2267: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.864 V, 2.1% # Test item 7- 4- 1- 1

T AMCA: MESE 2267: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.866 V, 1.2% # Test item 7- 4- 1- 2

T AMCA: MESE 2268: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.901 V, 10.8% # Test item 8- 4- 1- 1

T AMCA: MESE 2268: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.841 V, 10.0% # Test item 8- 4- 1- 2

T AMCA: MESE 2261: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.027 V, 9.0% # Test item 1- 4- 2- 1

T AMCA: MESE 2261: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.033 V, 32.7% # Test item 1- 4- 2- 2

T AMCA: MESE 2261: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.962 V, 12.8% # Test item 1- 4- 2- 3

T AMCA: MESE 2262: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.026 V, 8.6% # Test item 2- 4- 2- 1

T AMCA: MESE 2262: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.032 V, 32.1% # Test item 2- 4- 2- 2

T AMCA: MESE 2262: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.962 V, 12.6% # Test item 2- 4- 2- 3

T AMCA: MESE 2263: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.028 V, 9.2% # Test item 3- 4- 2- 1

T AMCA: MESE 2263: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.034 V, 34.1% # Test item 3- 4- 2- 2

T AMCA: MESE 2263: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.960 V, 13.5% # Test item 3- 4- 2- 3

T AMCA: MESE 2264: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.028 V, 9.4% # Test item 4- 4- 2- 1

T AMCA: MESE 2264: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.036 V, 36.1% # Test item 4- 4- 2- 2

T AMCA: MESE 2264: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.956 V, 14.7% # Test item 4- 4- 2- 3

T AMCA: MESE 2265: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.029 V, 9.8% # Test item 5- 4- 2- 1

T AMCA: MESE 2265: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.037 V, 36.6% # Test item 5- 4- 2- 2

T AMCA: MESE 2265: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.956 V, 14.7% # Test item 5- 4- 2- 3

T AMCA: MESE 2266: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.026 V, 8.6% # Test item 6- 4- 2- 1

T AMCA: MESE 2266: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.030 V, 30.0% # Test item 6- 4- 2- 2

T AMCA: MESE 2266: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.965 V, 11.5% # Test item 6- 4- 2- 3

T AMCA: MESE 2267: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.995 V, 1.6% # Test item 7- 4- 2- 1

T AMCA: MESE 2267: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% # Test item 7- 4- 2- 2

T AMCA: MESE 2267: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.994 V, 2.1% # Test item 7- 4- 2- 3

T AMCA: MESE 2261: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.887 mA, 37.6% # Test item 1- 4- 3- 1

T AMCA: MESE 2261: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.008 mA, 5.3% # Test item 1- 4- 3- 2

T AMCA: MESE 2261: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.835 mA, 36.6% # Test item 1- 4- 3- 3

T AMCA: MESE 2261: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.011 mA, 7.0% # Test item 1- 4- 3- 4

T AMCA: MESE 2262: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.891 mA, 36.3% # Test item 2- 4- 3- 1

T AMCA: MESE 2262: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.011 mA, 7.4% # Test item 2- 4- 3- 2

T AMCA: MESE 2262: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.831 mA, 37.7% # Test item 2- 4- 3- 3

T AMCA: MESE 2262: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.007 mA, 4.7% # Test item 2- 4- 3- 4

T AMCA: MESE 2263: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.891 mA, 36.4% # Test item 3- 4- 3- 1

T AMCA: MESE 2263: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.009 mA, 6.2% # Test item 3- 4- 3- 2

T AMCA: MESE 2263: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.829 mA, 38.0% # Test item 3- 4- 3- 3

T AMCA: MESE 2263: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.010 mA, 6.6% # Test item 3- 4- 3- 4

T AMCA: MESE 2264: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.892 mA, 35.9% # Test item 4- 4- 3- 1

T AMCA: MESE 2264: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.014 mA, 9.1% # Test item 4- 4- 3- 2

T AMCA: MESE 2264: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.826 mA, 38.7% # Test item 4- 4- 3- 3

T AMCA: MESE 2264: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.005 mA, 3.0% # Test item 4- 4- 3- 4

T AMCA: MESE 2265: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.892 mA, 36.1% # Test item 5- 4- 3- 1

T AMCA: MESE 2265: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.013 mA, 8.5% # Test item 5- 4- 3- 2

T AMCA: MESE 2265: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.826 mA, 38.8% # Test item 5- 4- 3- 3

T AMCA: MESE 2265: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.006 mA, 4.3% # Test item 5- 4- 3- 4

T AMCA: MESE 2266: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.890 mA, 36.5% # Test item 6- 4- 3- 1

T AMCA: MESE 2266: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.009 mA, 5.7% # Test item 6- 4- 3- 2

T AMCA: MESE 2266: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.832 mA, 37.4% # Test item 6- 4- 3- 3

T AMCA: MESE 2266: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.008 mA, 5.3% # Test item 6- 4- 3- 4

T AMCA: MESE 2267: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.861 mA, 46.2% # Test item 7- 4- 3- 1

T AMCA: MESE 2267: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.013 mA, 8.8% # Test item 7- 4- 3- 2

T AMCA: MESE 2267: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.856 mA, 32.0% # Test item 7- 4- 3- 3

T AMCA: MESE 2267: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.005 mA, 3.3% # Test item 7- 4- 3- 4

T AMCA: MESE 2268: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.890 mA, 36.5% # Test item 8- 4- 3- 1

T AMCA: MESE 2268: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.009 mA, 6.1% # Test item 8- 4- 3- 2

T AMCA: MESE 2268: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.835 mA, 36.7% # Test item 8- 4- 3- 3

T AMCA: MESE 2268: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.009 mA, 6.2% # Test item 8- 4- 3- 4

T AMCA: MESE 2261: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.979 V, 7.1% # Test item 1- 4- 4- 1

T AMCA: MESE 2261: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.275 V, 9.4% # Test item 1- 4- 4- 2

T AMCA: MESE 2261: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.019 V, 4.3% # Test item 1- 4- 4- 3

T AMCA: MESE 2261: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.274 V, 6.2% # Test item 1- 4- 4- 4

T AMCA: MESE 2262: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.980 V, 6.6% # Test item 2- 4- 4- 1

T AMCA: MESE 2262: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.273 V, 2.0% # Test item 2- 4- 4- 2

T AMCA: MESE 2262: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.023 V, 5.1% # Test item 2- 4- 4- 3

T AMCA: MESE 2262: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.273 V, 2.0% # Test item 2- 4- 4- 4

T AMCA: MESE 2263: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.972 V, 9.3% # Test item 3- 4- 4- 1

T AMCA: MESE 2263: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.273 V, 2.0% # Test item 3- 4- 4- 2

T AMCA: MESE 2263: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.021 V, 4.7% # Test item 3- 4- 4- 3

T AMCA: MESE 2263: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.274 V, 7.5% # Test item 3- 4- 4- 4

T AMCA: MESE 2264: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.980 V, 6.7% # Test item 4- 4- 4- 1

T AMCA: MESE 2264: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.277 V, 17.7% # Test item 4- 4- 4- 2

T AMCA: MESE 2264: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.018 V, 4.0% # Test item 4- 4- 4- 3

T AMCA: MESE 2264: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.271 V, 2.2% # Test item 4- 4- 4- 4

T AMCA: MESE 2265: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.976 V, 7.9% # Test item 5- 4- 4- 1

T AMCA: MESE 2265: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.275 V, 10.3% # Test item 5- 4- 4- 2

T AMCA: MESE 2265: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.018 V, 4.0% # Test item 5- 4- 4- 3

T AMCA: MESE 2265: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.270 V, 8.2% # Test item 5- 4- 4- 4

T AMCA: MESE 2266: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.975 V, 8.4% # Test item 6- 4- 4- 1

T AMCA: MESE 2266: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.270 V, 7.7% # Test item 6- 4- 4- 2

T AMCA: MESE 2266: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.027 V, 6.0% # Test item 6- 4- 4- 3

T AMCA: MESE 2266: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.276 V, 15.4% # Test item 6- 4- 4- 4

T AMCA: MESE 2267: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.020 V, 6.6% # Test item 7- 4- 4- 1

T AMCA: MESE 2267: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.280 V, 27.9% # Test item 7- 4- 4- 2

T AMCA: MESE 2267: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -2.978 V, 4.8% # Test item 7- 4- 4- 3

T AMCA: MESE 2267: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.267 V, 17.0% # Test item 7- 4- 4- 4

T AMCA: MESE 2268: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.979 V, 7.1% # Test item 8- 4- 4- 1

T AMCA: MESE 2268: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.271 V, 4.0% # Test item 8- 4- 4- 2

T AMCA: MESE 2268: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.029 V, 6.4% # Test item 8- 4- 4- 3

T AMCA: MESE 2268: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.277 V, 16.8% # Test item 8- 4- 4- 4

T AMCA: MESE 2261: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.992 kOhm, 8.0% # Test item 1- 4- 5- 1

T AMCA: MESE 2262: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.993 kOhm, 7.3% # Test item 2- 4- 5- 1

T AMCA: MESE 2263: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.990 kOhm, 9.9% # Test item 3- 4- 5- 1

T AMCA: MESE 2264: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.993 kOhm, 7.4% # Test item 4- 4- 5- 1

T AMCA: MESE 2265: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.992 kOhm, 8.5% # Test item 5- 4- 5- 1

T AMCA: MESE 2266: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.991 kOhm, 8.9% # Test item 6- 4- 5- 1

T AMCA: MESE 2267: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 1.006 kOhm, 5.9% # Test item 7- 4- 5- 1

T AMCA: MESE 2268: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.992 kOhm, 7.7% # Test item 8- 4- 5- 1

T AMCA: MCE 2260: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_1: R\_exp = 10220.000 Ohm, [9198.000 ... 11242.000 Ohm] : R\_meas = 10008.277 Ohm, 20.7% >> vMeas = 2.763V, vOffset = -0.240V, usedUnit = 1 # Test item 0- 5- 1- 1

T AMCA: MCE 2260: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_1\_(with\_GND): R\_exp = 59.000 Ohm, [49.000 ... 69.000 Ohm] : R\_meas = 58.956 Ohm, 0.4% >> vMeas = 0.181V, vOffset = 0.004V, usedUnit = 1 # Test item 0- 5- 1- 2

T AMCA: MCE 2260: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_2: R\_exp = 10220.000 Ohm, [9198.000 ... 11242.000 Ohm] : R\_meas = 9988.556 Ohm, 22.6% >> vMeas = 2.758V, vOffset = -0.239V, usedUnit = 1 # Test item 0- 5- 1- 3

T AMCA: MCE 2260: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_2\_(with\_GND): R\_exp = 59.000 Ohm, [49.000 ... 69.000 Ohm] : R\_meas = 58.788 Ohm, 2.1% >> vMeas = 0.180V, vOffset = 0.004V, usedUnit = 1 # Test item 0- 5- 1- 4

T AMCA: MCE 2260: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_\_0.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 1

T AMCA: MCE 2260: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_\_1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 2

T AMCA: MCE 2260: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_-1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 3

T AMCA: MCE 2260: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_\_0.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 4

T AMCA: MCE 2260: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_\_1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 5

T AMCA: MCE 2260: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_-1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 6

T AMCA: MESE 2261: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.498 V, 1.3% >> vOffset = -0.020V # Test item 1- 2- 9- 1

T AMCA: MESE 2261: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 22.7% >> vOffset = 0.004V # Test item 1- 2- 9- 2

T AMCA: MESE 2262: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 0.5% >> vOffset = -0.021V # Test item 2- 2- 9- 1

T AMCA: MESE 2262: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 18.9% >> vOffset = 0.003V # Test item 2- 2- 9- 2

T AMCA: MESE 2263: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.497 V, 2.0% >> vOffset = -0.023V # Test item 3- 2- 9- 1

T AMCA: MESE 2263: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 22.7% >> vOffset = 0.002V # Test item 3- 2- 9- 2

T AMCA: MESE 2264: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.498 V, 1.2% >> vOffset = -0.019V # Test item 4- 2- 9- 1

T AMCA: MESE 2264: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 20.1% >> vOffset = 0.006V # Test item 4- 2- 9- 2

T AMCA: MESE 2265: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.497 V, 2.3% >> vOffset = -0.019V # Test item 5- 2- 9- 1

T AMCA: MESE 2265: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 18.9% >> vOffset = 0.008V # Test item 5- 2- 9- 2

T AMCA: MESE 2266: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 0.9% >> vOffset = -0.024V # Test item 6- 2- 9- 1

T AMCA: MESE 2266: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 22.7% >> vOffset = 0.002V # Test item 6- 2- 9- 2

T AMCA: MESE 2267: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.497 V, 2.0% >> vOffset = 0.023V # Test item 7- 2- 9- 1

T AMCA: MESE 2267: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 22.7% >> vOffset = 0.007V # Test item 7- 2- 9- 2

T AMCA: MESE 2268: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.500 V, 0.1% >> vOffset = -0.023V # Test item 8- 2- 9- 1

T AMCA: MESE 2268: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 21.4% >> vOffset = 0.002V # Test item 8- 2- 9- 2

T AMCA: MESE 2261: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 1- 7- 1- 1

T AMCA: MESE 2262: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 2- 7- 1- 1

T AMCA: MESE 2263: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 3- 7- 1- 1

T AMCA: MESE 2264: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 4- 7- 1- 1

T AMCA: MESE 2265: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 5- 7- 1- 1

T AMCA: MESE 2266: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 6- 7- 1- 1

T AMCA: MESE 2267: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 7- 7- 1- 1

T AMCA: MESE 2268: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 8- 7- 1- 1

T AMCA: MESE 2261: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 1- 7- 1- 2

T AMCA: MESE 2262: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 2- 7- 1- 2

T AMCA: MESE 2263: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 3- 7- 1- 2

T AMCA: MESE 2264: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 4- 7- 1- 2

T AMCA: MESE 2265: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 5- 7- 1- 2

T AMCA: MESE 2266: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 6- 7- 1- 2

T AMCA: MESE 2267: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 7- 7- 1- 2

T AMCA: MESE 2268: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 8- 7- 1- 2

T AMCA: MESE 2261: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 1- 7- 2- 1

T AMCA: MESE 2262: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 2- 7- 2- 1

T AMCA: MESE 2263: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 3- 7- 2- 1

T AMCA: MESE 2264: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 4- 7- 2- 1

T AMCA: MESE 2265: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 5- 7- 2- 1

T AMCA: MESE 2266: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 6- 7- 2- 1

T AMCA: MESE 2267: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 7- 7- 2- 1

T AMCA: MESE 2268: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 8- 7- 2- 1

T AMCA: MESE 2261: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 7- 3- 1

T AMCA: MESE 2261: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 7- 3- 2

T AMCA: MESE 2262: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 7- 3- 1

T AMCA: MESE 2262: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 7- 3- 2

T AMCA: MESE 2263: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 7- 3- 1

T AMCA: MESE 2263: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 7- 3- 2

T AMCA: MESE 2264: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 7- 3- 1

T AMCA: MESE 2264: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 7- 3- 2

T AMCA: MESE 2265: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 7- 3- 1

T AMCA: MESE 2265: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 7- 3- 2

T AMCA: MESE 2266: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 7- 3- 1

T AMCA: MESE 2266: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 7- 3- 2

T AMCA: MESE 2267: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 7- 3- 1

T AMCA: MESE 2267: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 7- 3- 2

T AMCA: MESE 2268: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 7- 3- 1

T AMCA: MESE 2268: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 7- 3- 2

T AMCA: MESE 2261: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 1- 8- 1- 1

T AMCA: MESE 2261: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 1- 8- 1- 2

T AMCA: MESE 2261: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 1- 8- 1- 3

T AMCA: MESE 2261: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 4

T AMCA: MESE 2261: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 1- 8- 1- 5

T AMCA: MESE 2261: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 6

T AMCA: MESE 2261: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 1- 8- 1- 7

T AMCA: MESE 2261: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 8

T AMCA: MESE 2261: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 9

T AMCA: MESE 2262: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 2- 8- 1- 1

T AMCA: MESE 2262: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 2- 8- 1- 2

T AMCA: MESE 2262: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 2- 8- 1- 3

T AMCA: MESE 2262: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 4

T AMCA: MESE 2262: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 2- 8- 1- 5

T AMCA: MESE 2262: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 6

T AMCA: MESE 2262: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 2- 8- 1- 7

T AMCA: MESE 2262: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 8

T AMCA: MESE 2262: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 9

T AMCA: MESE 2263: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 3- 8- 1- 1

T AMCA: MESE 2263: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 3- 8- 1- 2

T AMCA: MESE 2263: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 3- 8- 1- 3

T AMCA: MESE 2263: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 4

T AMCA: MESE 2263: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 3- 8- 1- 5

T AMCA: MESE 2263: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 6

T AMCA: MESE 2263: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 3- 8- 1- 7

T AMCA: MESE 2263: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 8

T AMCA: MESE 2263: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 9

T AMCA: MESE 2264: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 4- 8- 1- 1

T AMCA: MESE 2264: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 4- 8- 1- 2

T AMCA: MESE 2264: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 4- 8- 1- 3

T AMCA: MESE 2264: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 4

T AMCA: MESE 2264: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 4- 8- 1- 5

T AMCA: MESE 2264: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 6

T AMCA: MESE 2264: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 4- 8- 1- 7

T AMCA: MESE 2264: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 8

T AMCA: MESE 2264: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 9

T AMCA: MESE 2265: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 5- 8- 1- 1

T AMCA: MESE 2265: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 5- 8- 1- 2

T AMCA: MESE 2265: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 5- 8- 1- 3

T AMCA: MESE 2265: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 4

T AMCA: MESE 2265: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 5- 8- 1- 5

T AMCA: MESE 2265: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 6

T AMCA: MESE 2265: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 5- 8- 1- 7

T AMCA: MESE 2265: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 8

T AMCA: MESE 2265: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 9

T AMCA: MESE 2266: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 6- 8- 1- 1

T AMCA: MESE 2266: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 6- 8- 1- 2

T AMCA: MESE 2266: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 6- 8- 1- 3

T AMCA: MESE 2266: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 4

T AMCA: MESE 2266: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 6- 8- 1- 5

T AMCA: MESE 2266: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 6

T AMCA: MESE 2266: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 6- 8- 1- 7

T AMCA: MESE 2266: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 8

T AMCA: MESE 2266: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 9

T AMCA: MESE 2267: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 7- 8- 1- 1

T AMCA: MESE 2267: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 7- 8- 1- 2

T AMCA: MESE 2267: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 7- 8- 1- 3

T AMCA: MESE 2267: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 4

T AMCA: MESE 2267: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 7- 8- 1- 5

T AMCA: MESE 2267: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 6

T AMCA: MESE 2267: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 7- 8- 1- 7

T AMCA: MESE 2267: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 8

T AMCA: MESE 2267: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 9

T AMCA: MESE 2268: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 8- 8- 1- 1

T AMCA: MESE 2268: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 8- 8- 1- 2

T AMCA: MESE 2268: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 8- 8- 1- 3

T AMCA: MESE 2268: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 4

T AMCA: MESE 2268: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 8- 8- 1- 5

T AMCA: MESE 2268: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 6

T AMCA: MESE 2268: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 8- 8- 1- 7

T AMCA: MESE 2268: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 8

T AMCA: MESE 2268: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 9

T AMCA: MESE 2261: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 1, masterModule = 1, slaveUnit = 3, slaveModule = 1 # Test item 1- 8- 3- 1

T AMCA: MESE 2261: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 1, masterModule = 1, slaveUnit = 3, slaveModule = 1 # Test item 1- 8- 3- 2

T AMCA: MESE 2263: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 3, masterModule = 1, slaveUnit = 2, slaveModule = 2 # Test item 3- 8- 3- 3

T AMCA: MESE 2263: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 3, masterModule = 1, slaveUnit = 2, slaveModule = 2 # Test item 3- 8- 3- 4

T AMCA: MESE 2262: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 2, masterModule = 2, slaveUnit = 4, slaveModule = 2 # Test item 2- 8- 3- 5

T AMCA: MESE 2262: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 2, masterModule = 2, slaveUnit = 4, slaveModule = 2 # Test item 2- 8- 3- 6

T AMCA: MESE 2264: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 4, masterModule = 2, slaveUnit = 5, slaveModule = 3 # Test item 4- 8- 3- 7

T AMCA: MESE 2264: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 4, masterModule = 2, slaveUnit = 5, slaveModule = 3 # Test item 4- 8- 3- 8

T AMCA: MESE 2265: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 5, masterModule = 3, slaveUnit = 7, slaveModule = 3 # Test item 5- 8- 3- 9

T AMCA: MESE 2265: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 5, masterModule = 3, slaveUnit = 7, slaveModule = 3 # Test item 5- 8- 3-10

T AMCA: MESE 2267: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 7, masterModule = 3, slaveUnit = 8, slaveModule = 4 # Test item 7- 8- 3-11

T AMCA: MESE 2267: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 7, masterModule = 3, slaveUnit = 8, slaveModule = 4 # Test item 7- 8- 3-12

T AMCA: MESE 2268: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 8, masterModule = 4, slaveUnit = 6, slaveModule = 4 # Test item 8- 8- 3-13

T AMCA: MESE 2268: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 8, masterModule = 4, slaveUnit = 6, slaveModule = 4 # Test item 8- 8- 3-14

T AMCA: MESE 2266: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 6, masterModule = 4, slaveUnit = 1, slaveModule = 1 # Test item 6- 8- 3-15

T AMCA: MESE 2266: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 6, masterModule = 4, slaveUnit = 1, slaveModule = 1 # Test item 6- 8- 3-16

T AMCA: MESE 2261: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 10.102 ns, 3.4% >> short = 60275, long = 29915 # Test item 1- 8- 6- 1

T AMCA: MESE 2262: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 10.079 ns, 2.6% >> short = 61019, long = 30132 # Test item 2- 8- 6- 1

T AMCA: MESE 2263: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.821 ns, 9.0% >> short = 61170, long = 30566 # Test item 3- 8- 6- 1

T AMCA: MESE 2264: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.961 ns, 2.0% >> short = 61374, long = 30400 # Test item 4- 8- 6- 1

T AMCA: MESE 2265: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 10.126 ns, 4.2% >> short = 60842, long = 30019 # Test item 5- 8- 6- 1

T AMCA: MESE 2266: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 10.009 ns, 0.3% >> short = 60923, long = 30215 # Test item 6- 8- 6- 1

T AMCA: MESE 2267: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 10.127 ns, 4.2% >> short = 61499, long = 30176 # Test item 7- 8- 6- 1

T AMCA: MESE 2268: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 10.119 ns, 4.0% >> short = 60085, long = 29843 # Test item 8- 8- 6- 1

T AMCA: MESE 2261: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 1- 8- 7- 1

T AMCA: MESE 2261: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18428, 39.3% # Test item 1- 8- 7- 2

T AMCA: MESE 2261: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x000047FC: Reg\_meas = 0x000047FC # Test item 1- 8- 7- 3

T AMCA: MESE 2261: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 8- 7- 4

T AMCA: MESE 2262: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 2- 8- 7- 1

T AMCA: MESE 2262: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18784, 30.4% # Test item 2- 8- 7- 2

T AMCA: MESE 2262: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004960: Reg\_meas = 0x00004960 # Test item 2- 8- 7- 3

T AMCA: MESE 2262: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 8- 7- 4

T AMCA: MESE 2263: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 3- 8- 7- 1

T AMCA: MESE 2263: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18007, 49.8% # Test item 3- 8- 7- 2

T AMCA: MESE 2263: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004657: Reg\_meas = 0x00004657 # Test item 3- 8- 7- 3

T AMCA: MESE 2263: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 8- 7- 4

T AMCA: MESE 2264: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 4- 8- 7- 1

T AMCA: MESE 2264: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18025, 49.4% # Test item 4- 8- 7- 2

T AMCA: MESE 2264: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004669: Reg\_meas = 0x00004669 # Test item 4- 8- 7- 3

T AMCA: MESE 2264: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 8- 7- 4

T AMCA: MESE 2265: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 5- 8- 7- 1

T AMCA: MESE 2265: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18317, 42.1% # Test item 5- 8- 7- 2

T AMCA: MESE 2265: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000478D: Reg\_meas = 0x0000478D # Test item 5- 8- 7- 3

T AMCA: MESE 2265: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 8- 7- 4

T AMCA: MESE 2266: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 6- 8- 7- 1

T AMCA: MESE 2266: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18060, 48.5% # Test item 6- 8- 7- 2

T AMCA: MESE 2266: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000468C: Reg\_meas = 0x0000468C # Test item 6- 8- 7- 3

T AMCA: MESE 2266: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 8- 7- 4

T AMCA: MESE 2267: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 7- 8- 7- 1

T AMCA: MESE 2267: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17970, 50.7% # Test item 7- 8- 7- 2

T AMCA: MESE 2267: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004632: Reg\_meas = 0x00004632 # Test item 7- 8- 7- 3

T AMCA: MESE 2267: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 8- 7- 4

T AMCA: MESE 2268: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 8- 8- 7- 1

T AMCA: MESE 2268: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18575, 35.6% # Test item 8- 8- 7- 2

T AMCA: MESE 2268: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000488F: Reg\_meas = 0x0000488F # Test item 8- 8- 7- 3

T AMCA: MESE 2268: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 8- 7- 4

T AMCA: MESE 2261: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 9- 1- 1

T AMCA: MESE 2261: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 9- 1- 2

T AMCA: MESE 2261: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 3

T AMCA: MESE 2261: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 4

T AMCA: MESE 2261: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 5

T AMCA: MESE 2261: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 6

T AMCA: MESE 2261: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 7

T AMCA: MESE 2261: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 1- 9- 1- 8

T AMCA: MESE 2261: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 9

T AMCA: MESE 2261: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1-10

T AMCA: MESE 2261: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 1- 9- 1-11

T AMCA: MESE 2261: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 1- 9- 1-12

T AMCA: MESE 2261: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 1- 9- 1-13

T AMCA: MESE 2261: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 9- 1-14

T AMCA: MESE 2262: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 9- 1- 1

T AMCA: MESE 2262: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 9- 1- 2

T AMCA: MESE 2262: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 3

T AMCA: MESE 2262: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 4

T AMCA: MESE 2262: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 5

T AMCA: MESE 2262: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 6

T AMCA: MESE 2262: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 7

T AMCA: MESE 2262: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 2- 9- 1- 8

T AMCA: MESE 2262: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 9

T AMCA: MESE 2262: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1-10

T AMCA: MESE 2262: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 2- 9- 1-11

T AMCA: MESE 2262: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 2- 9- 1-12

T AMCA: MESE 2262: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 2- 9- 1-13

T AMCA: MESE 2262: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 9- 1-14

T AMCA: MESE 2263: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 9- 1- 1

T AMCA: MESE 2263: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 9- 1- 2

T AMCA: MESE 2263: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 3

T AMCA: MESE 2263: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 4

T AMCA: MESE 2263: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 5

T AMCA: MESE 2263: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 6

T AMCA: MESE 2263: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 7

T AMCA: MESE 2263: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 3- 9- 1- 8

T AMCA: MESE 2263: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 9

T AMCA: MESE 2263: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1-10

T AMCA: MESE 2263: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 3- 9- 1-11

T AMCA: MESE 2263: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 3- 9- 1-12

T AMCA: MESE 2263: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 3- 9- 1-13

T AMCA: MESE 2263: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 9- 1-14

T AMCA: MESE 2264: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 9- 1- 1

T AMCA: MESE 2264: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 9- 1- 2

T AMCA: MESE 2264: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 3

T AMCA: MESE 2264: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 4

T AMCA: MESE 2264: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 5

T AMCA: MESE 2264: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 6

T AMCA: MESE 2264: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 7

T AMCA: MESE 2264: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 4- 9- 1- 8

T AMCA: MESE 2264: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 9

T AMCA: MESE 2264: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1-10

T AMCA: MESE 2264: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 4- 9- 1-11

T AMCA: MESE 2264: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 4- 9- 1-12

T AMCA: MESE 2264: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 4- 9- 1-13

T AMCA: MESE 2264: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 9- 1-14

T AMCA: MESE 2265: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 9- 1- 1

T AMCA: MESE 2265: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 9- 1- 2

T AMCA: MESE 2265: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 3

T AMCA: MESE 2265: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 4

T AMCA: MESE 2265: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 5

T AMCA: MESE 2265: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 6

T AMCA: MESE 2265: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 7

T AMCA: MESE 2265: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 5- 9- 1- 8

T AMCA: MESE 2265: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 9

T AMCA: MESE 2265: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1-10

T AMCA: MESE 2265: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 5- 9- 1-11

T AMCA: MESE 2265: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 5- 9- 1-12

T AMCA: MESE 2265: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 5- 9- 1-13

T AMCA: MESE 2265: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 9- 1-14

T AMCA: MESE 2266: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 9- 1- 1

T AMCA: MESE 2266: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 9- 1- 2

T AMCA: MESE 2266: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 3

T AMCA: MESE 2266: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 4

T AMCA: MESE 2266: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 5

T AMCA: MESE 2266: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 6

T AMCA: MESE 2266: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 7

T AMCA: MESE 2266: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 6- 9- 1- 8

T AMCA: MESE 2266: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 9

T AMCA: MESE 2266: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1-10

T AMCA: MESE 2266: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 6- 9- 1-11

T AMCA: MESE 2266: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 6- 9- 1-12

T AMCA: MESE 2266: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 6- 9- 1-13

T AMCA: MESE 2266: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 9- 1-14

T AMCA: MESE 2267: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 9- 1- 1

T AMCA: MESE 2267: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 9- 1- 2

T AMCA: MESE 2267: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 3

T AMCA: MESE 2267: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 4

T AMCA: MESE 2267: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 5

T AMCA: MESE 2267: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 6

T AMCA: MESE 2267: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 7

T AMCA: MESE 2267: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 7- 9- 1- 8

T AMCA: MESE 2267: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 9

T AMCA: MESE 2267: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1-10

T AMCA: MESE 2267: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 7- 9- 1-11

T AMCA: MESE 2267: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 7- 9- 1-12

T AMCA: MESE 2267: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 7- 9- 1-13

T AMCA: MESE 2267: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 9- 1-14

T AMCA: MESE 2268: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 9- 1- 1

T AMCA: MESE 2268: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 9- 1- 2

T AMCA: MESE 2268: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 3

T AMCA: MESE 2268: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 4

T AMCA: MESE 2268: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 5

T AMCA: MESE 2268: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 6

T AMCA: MESE 2268: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 7

T AMCA: MESE 2268: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 8- 9- 1- 8

T AMCA: MESE 2268: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 9

T AMCA: MESE 2268: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1-10

T AMCA: MESE 2268: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 8- 9- 1-11

T AMCA: MESE 2268: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 8- 9- 1-12

T AMCA: MESE 2268: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 8- 9- 1-13

T AMCA: MESE 2268: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 9- 1-14

T AMCA: MESE 2261: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.953 V, 4.5% # Test item 1-17- 1- 1

T AMCA: MESE 2261: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.955 V, 2.7% # Test item 1-17- 1- 2

T AMCA: MESE 2261: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.956 V, 1.8% # Test item 1-17- 1- 3

T AMCA: MESE 2261: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.958 V, 0.0% # Test item 1-17- 1- 4

T AMCA: MESE 2261: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.067 V, 1.1% # Test item 1-17- 1- 5

T AMCA: MESE 2261: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.980 V, 0.9% # Test item 1-17- 1- 6

T AMCA: MESE 2261: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.967 V, 5.5% # Test item 1-17- 1- 7

T AMCA: MESE 2261: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.897 V, 0.5% # Test item 1-17- 1- 8

T AMCA: MESE 2261: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.973 V, 5.5% # Test item 1-17- 1- 9

T AMCA: MESE 2261: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.986 V, 6.4% # Test item 1-17- 1-10

T AMCA: MESE 2261: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.981 V, 4.5% # Test item 1-17- 1-11

T AMCA: MESE 2262: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.956 V, 1.8% # Test item 2-17- 1- 1

T AMCA: MESE 2262: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.953 V, 4.5% # Test item 2-17- 1- 2

T AMCA: MESE 2262: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.950 V, 7.3% # Test item 2-17- 1- 3

T AMCA: MESE 2262: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.957 V, 0.9% # Test item 2-17- 1- 4

T AMCA: MESE 2262: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.065 V, 3.3% # Test item 2-17- 1- 5

T AMCA: MESE 2262: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.983 V, 3.6% # Test item 2-17- 1- 6

T AMCA: MESE 2262: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.963 V, 9.1% # Test item 2-17- 1- 7

T AMCA: MESE 2262: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.920 V, 10.7% # Test item 2-17- 1- 8

T AMCA: MESE 2262: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.973 V, 5.5% # Test item 2-17- 1- 9

T AMCA: MESE 2262: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.991 V, 1.8% # Test item 2-17- 1-10

T AMCA: MESE 2262: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.976 V, 0.0% # Test item 2-17- 1-11

T AMCA: MESE 2263: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.964 V, 5.5% # Test item 3-17- 1- 1

T AMCA: MESE 2263: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.956 V, 1.8% # Test item 3-17- 1- 2

T AMCA: MESE 2263: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.956 V, 1.8% # Test item 3-17- 1- 3

T AMCA: MESE 2263: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.955 V, 2.7% # Test item 3-17- 1- 4

T AMCA: MESE 2263: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.056 V, 10.9% # Test item 3-17- 1- 5

T AMCA: MESE 2263: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.988 V, 8.2% # Test item 3-17- 1- 6

T AMCA: MESE 2263: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.971 V, 1.8% # Test item 3-17- 1- 7

T AMCA: MESE 2263: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.641 V, 7.7% # Test item 3-17- 1- 8

T AMCA: MESE 2263: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.977 V, 1.8% # Test item 3-17- 1- 9

T AMCA: MESE 2263: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.713 V, 13.6% # Test item 3-17- 1-10

T AMCA: MESE 2264: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.955 V, 2.7% # Test item 4-17- 1- 1

T AMCA: MESE 2264: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.945 V, 11.8% # Test item 4-17- 1- 2

T AMCA: MESE 2264: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.956 V, 1.8% # Test item 4-17- 1- 3

T AMCA: MESE 2264: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.958 V, 0.0% # Test item 4-17- 1- 4

T AMCA: MESE 2264: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.065 V, 2.7% # Test item 4-17- 1- 5

T AMCA: MESE 2264: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.975 V, 3.6% # Test item 4-17- 1- 6

T AMCA: MESE 2264: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.971 V, 1.8% # Test item 4-17- 1- 7

T AMCA: MESE 2264: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.630 V, 2.3% # Test item 4-17- 1- 8

T AMCA: MESE 2264: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.976 V, 2.7% # Test item 4-17- 1- 9

T AMCA: MESE 2264: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.713 V, 13.6% # Test item 4-17- 1-10

T AMCA: MESE 2265: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.957 V, 0.9% # Test item 5-17- 1- 1

T AMCA: MESE 2265: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.960 V, 1.8% # Test item 5-17- 1- 2

T AMCA: MESE 2265: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.960 V, 1.8% # Test item 5-17- 1- 3

T AMCA: MESE 2265: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.952 V, 5.5% # Test item 5-17- 1- 4

T AMCA: MESE 2265: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.063 V, 5.6% # Test item 5-17- 1- 5

T AMCA: MESE 2265: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.980 V, 0.9% # Test item 5-17- 1- 6

T AMCA: MESE 2265: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.969 V, 3.6% # Test item 5-17- 1- 7

T AMCA: MESE 2265: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.895 V, 2.3% # Test item 5-17- 1- 8

T AMCA: MESE 2265: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.970 V, 8.2% # Test item 5-17- 1- 9

T AMCA: MESE 2265: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.988 V, 4.5% # Test item 5-17- 1-10

T AMCA: MESE 2265: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.970 V, 5.5% # Test item 5-17- 1-11

T AMCA: MESE 2266: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.957 V, 0.9% # Test item 6-17- 1- 1

T AMCA: MESE 2266: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.959 V, 0.9% # Test item 6-17- 1- 2

T AMCA: MESE 2266: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.958 V, 0.0% # Test item 6-17- 1- 3

T AMCA: MESE 2266: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.958 V, 0.0% # Test item 6-17- 1- 4

T AMCA: MESE 2266: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.057 V, 10.0% # Test item 6-17- 1- 5

T AMCA: MESE 2266: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.983 V, 3.6% # Test item 6-17- 1- 6

T AMCA: MESE 2266: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.959 V, 12.7% # Test item 6-17- 1- 7

T AMCA: MESE 2266: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.634 V, 1.3% # Test item 6-17- 1- 8

T AMCA: MESE 2266: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.977 V, 1.8% # Test item 6-17- 1- 9

T AMCA: MESE 2266: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.712 V, 14.5% # Test item 6-17- 1-10

T AMCA: MESE 2267: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.952 V, 5.5% # Test item 7-17- 1- 1

T AMCA: MESE 2267: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.959 V, 0.9% # Test item 7-17- 1- 2

T AMCA: MESE 2267: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.950 V, 7.3% # Test item 7-17- 1- 3

T AMCA: MESE 2267: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.956 V, 1.8% # Test item 7-17- 1- 4

T AMCA: MESE 2267: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.065 V, 2.7% # Test item 7-17- 1- 5

T AMCA: MESE 2267: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.984 V, 4.5% # Test item 7-17- 1- 6

T AMCA: MESE 2267: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.966 V, 6.4% # Test item 7-17- 1- 7

T AMCA: MESE 2267: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.634 V, 1.3% # Test item 7-17- 1- 8

T AMCA: MESE 2267: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.969 V, 9.1% # Test item 7-17- 1- 9

T AMCA: MESE 2267: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.701 V, 24.5% # Test item 7-17- 1-10

T AMCA: MESE 2268: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.960 V, 1.8% # Test item 8-17- 1- 1

T AMCA: MESE 2268: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.957 V, 0.9% # Test item 8-17- 1- 2

T AMCA: MESE 2268: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.958 V, 0.0% # Test item 8-17- 1- 3

T AMCA: MESE 2268: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.950 V, 7.3% # Test item 8-17- 1- 4

T AMCA: MESE 2268: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.061 V, 7.8% # Test item 8-17- 1- 5

T AMCA: MESE 2268: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.982 V, 2.7% # Test item 8-17- 1- 6

T AMCA: MESE 2268: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.964 V, 8.2% # Test item 8-17- 1- 7

T AMCA: MESE 2268: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.893 V, 4.1% # Test item 8-17- 1- 8

T AMCA: MESE 2268: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.980 V, 0.9% # Test item 8-17- 1- 9

T AMCA: MESE 2268: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.989 V, 3.6% # Test item 8-17- 1-10

T AMCA: MESE 2268: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.972 V, 3.6% # Test item 8-17- 1-11

T AMCA: MESE 2261: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 1.9% # Test item 1-15- 1- 1

T AMCA: MESE 2261: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.0% # Test item 1-15- 1- 2

T AMCA: MESE 2261: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.014 V, 4.6% # Test item 1-15- 1- 3

T AMCA: MESE 2261: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 1.9% # Test item 1-15- 1- 4

T AMCA: MESE 2261: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.0% # Test item 1-15- 1- 5

T AMCA: MESE 2261: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.014 V, 4.7% # Test item 1-15- 1- 6

T AMCA: MESE 2261: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.3% # Test item 1-15- 1- 7

T AMCA: MESE 2261: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% # Test item 1-15- 1- 8

T AMCA: MESE 2261: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.4% # Test item 1-15- 1- 9

T AMCA: MESE 2261: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 2.1% # Test item 1-15- 1-10

T AMCA: MESE 2261: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% # Test item 1-15- 1-11

T AMCA: MESE 2261: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.014 V, 4.6% # Test item 1-15- 1-12

T AMCA: MESE 2262: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.8% # Test item 2-15- 1- 1

T AMCA: MESE 2262: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.8% # Test item 2-15- 1- 2

T AMCA: MESE 2262: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.009 V, 3.0% # Test item 2-15- 1- 3

T AMCA: MESE 2262: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.7% # Test item 2-15- 1- 4

T AMCA: MESE 2262: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.9% # Test item 2-15- 1- 5

T AMCA: MESE 2262: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.009 V, 3.0% # Test item 2-15- 1- 6

T AMCA: MESE 2262: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 4.9% # Test item 2-15- 1- 7

T AMCA: MESE 2262: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.4% # Test item 2-15- 1- 8

T AMCA: MESE 2262: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.3% # Test item 2-15- 1- 9

T AMCA: MESE 2262: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.8% # Test item 2-15- 1-10

T AMCA: MESE 2262: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.5% # Test item 2-15- 1-11

T AMCA: MESE 2262: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.4% # Test item 2-15- 1-12

T AMCA: MESE 2263: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.5% # Test item 3-15- 1- 1

T AMCA: MESE 2263: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.6% # Test item 3-15- 1- 2

T AMCA: MESE 2263: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.017 V, 5.7% # Test item 3-15- 1- 3

T AMCA: MESE 2263: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.5% # Test item 3-15- 1- 4

T AMCA: MESE 2263: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.4% # Test item 3-15- 1- 5

T AMCA: MESE 2263: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.017 V, 5.7% # Test item 3-15- 1- 6

T AMCA: MESE 2263: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.4% # Test item 3-15- 1- 7

T AMCA: MESE 2263: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.6% # Test item 3-15- 1- 8

T AMCA: MESE 2263: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.017 V, 5.6% # Test item 3-15- 1- 9

T AMCA: MESE 2263: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.3% # Test item 3-15- 1-10

T AMCA: MESE 2263: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.6% # Test item 3-15- 1-11

T AMCA: MESE 2263: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.017 V, 5.6% # Test item 3-15- 1-12

T AMCA: MESE 2264: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.0% # Test item 4-15- 1- 1

T AMCA: MESE 2264: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.8% # Test item 4-15- 1- 2

T AMCA: MESE 2264: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.0% # Test item 4-15- 1- 3

T AMCA: MESE 2264: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 4.9% # Test item 4-15- 1- 4

T AMCA: MESE 2264: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.6% # Test item 4-15- 1- 5

T AMCA: MESE 2264: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.0% # Test item 4-15- 1- 6

T AMCA: MESE 2264: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.4% # Test item 4-15- 1- 7

T AMCA: MESE 2264: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.011 V, 11.0% # Test item 4-15- 1- 8

T AMCA: MESE 2264: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.999 V, 0.3% # Test item 4-15- 1- 9

T AMCA: MESE 2264: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.4% # Test item 4-15- 1-10

T AMCA: MESE 2264: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.011 V, 10.6% # Test item 4-15- 1-11

T AMCA: MESE 2264: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.999 V, 0.3% # Test item 4-15- 1-12

T AMCA: MESE 2265: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.6% # Test item 5-15- 1- 1

T AMCA: MESE 2265: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.011 V, 11.0% # Test item 5-15- 1- 2

T AMCA: MESE 2265: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.999 V, 0.4% # Test item 5-15- 1- 3

T AMCA: MESE 2265: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.5% # Test item 5-15- 1- 4

T AMCA: MESE 2265: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.011 V, 11.2% # Test item 5-15- 1- 5

T AMCA: MESE 2265: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.999 V, 0.4% # Test item 5-15- 1- 6

T AMCA: MESE 2265: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.019 V, 6.2% # Test item 5-15- 1- 7

T AMCA: MESE 2265: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 12.2% # Test item 5-15- 1- 8

T AMCA: MESE 2265: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.6% # Test item 5-15- 1- 9

T AMCA: MESE 2265: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.019 V, 6.2% # Test item 5-15- 1-10

T AMCA: MESE 2265: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 12.2% # Test item 5-15- 1-11

T AMCA: MESE 2265: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.6% # Test item 5-15- 1-12

T AMCA: MESE 2266: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.3% # Test item 6-15- 1- 1

T AMCA: MESE 2266: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.3% # Test item 6-15- 1- 2

T AMCA: MESE 2266: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.006 V, 1.8% # Test item 6-15- 1- 3

T AMCA: MESE 2266: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.3% # Test item 6-15- 1- 4

T AMCA: MESE 2266: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.4% # Test item 6-15- 1- 5

T AMCA: MESE 2266: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.006 V, 1.9% # Test item 6-15- 1- 6

T AMCA: MESE 2266: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.011 V, 3.6% # Test item 6-15- 1- 7

T AMCA: MESE 2266: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.4% # Test item 6-15- 1- 8

T AMCA: MESE 2266: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.005 V, 1.7% # Test item 6-15- 1- 9

T AMCA: MESE 2266: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.011 V, 3.6% # Test item 6-15- 1-10

T AMCA: MESE 2266: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.3% # Test item 6-15- 1-11

T AMCA: MESE 2266: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.005 V, 1.6% # Test item 6-15- 1-12

T AMCA: MESE 2267: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.022 V, 7.5% # Test item 7-15- 1- 1

T AMCA: MESE 2267: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.7% # Test item 7-15- 1- 2

T AMCA: MESE 2267: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.8% # Test item 7-15- 1- 3

T AMCA: MESE 2267: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.022 V, 7.4% # Test item 7-15- 1- 4

T AMCA: MESE 2267: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.8% # Test item 7-15- 1- 5

T AMCA: MESE 2267: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.8% # Test item 7-15- 1- 6

T AMCA: MESE 2267: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.023 V, 7.7% # Test item 7-15- 1- 7

T AMCA: MESE 2267: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.6% # Test item 7-15- 1- 8

T AMCA: MESE 2267: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.8% # Test item 7-15- 1- 9

T AMCA: MESE 2267: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.023 V, 7.7% # Test item 7-15- 1-10

T AMCA: MESE 2267: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.3% # Test item 7-15- 1-11

T AMCA: MESE 2267: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.8% # Test item 7-15- 1-12

T AMCA: MESE 2268: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 8-15- 1- 1

T AMCA: MESE 2268: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.0% # Test item 8-15- 1- 2

T AMCA: MESE 2268: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.7% # Test item 8-15- 1- 3

T AMCA: MESE 2268: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 8-15- 1- 4

T AMCA: MESE 2268: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.8% # Test item 8-15- 1- 5

T AMCA: MESE 2268: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.8% # Test item 8-15- 1- 6

T AMCA: MESE 2268: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 4.9% # Test item 8-15- 1- 7

T AMCA: MESE 2268: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.5% # Test item 8-15- 1- 8

T AMCA: MESE 2268: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.8% # Test item 8-15- 1- 9

T AMCA: MESE 2268: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 4.9% # Test item 8-15- 1-10

T AMCA: MESE 2268: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.4% # Test item 8-15- 1-11

T AMCA: MESE 2268: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.8% # Test item 8-15- 1-12

T AMCA: MESE 2261: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.721 MOhm, 18.6% # Test item 1-15- 2- 1

T AMCA: MESE 2261: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.843 MOhm, 10.4% # Test item 1-15- 2- 2

T AMCA: MESE 2261: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.812 MOhm, 12.6% # Test item 1-15- 2- 3

T AMCA: MESE 2261: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.605 MOhm, 26.3% # Test item 1-15- 2- 4

T AMCA: MESE 2262: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.779 MOhm, 14.7% # Test item 2-15- 2- 1

T AMCA: MESE 2262: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.828 MOhm, 11.4% # Test item 2-15- 2- 2

T AMCA: MESE 2262: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.681 MOhm, 21.3% # Test item 2-15- 2- 3

T AMCA: MESE 2262: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.825 MOhm, 11.7% # Test item 2-15- 2- 4

T AMCA: MESE 2263: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.845 MOhm, 10.3% # Test item 3-15- 2- 1

T AMCA: MESE 2263: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.861 MOhm, 9.3% # Test item 3-15- 2- 2

T AMCA: MESE 2263: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.803 MOhm, 13.1% # Test item 3-15- 2- 3

T AMCA: MESE 2263: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.891 MOhm, 7.3% # Test item 3-15- 2- 4

T AMCA: MESE 2264: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.773 MOhm, 15.2% # Test item 4-15- 2- 1

T AMCA: MESE 2264: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.844 MOhm, 10.4% # Test item 4-15- 2- 2

T AMCA: MESE 2264: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.802 MOhm, 13.2% # Test item 4-15- 2- 3

T AMCA: MESE 2264: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.822 MOhm, 11.9% # Test item 4-15- 2- 4

T AMCA: MESE 2265: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.856 MOhm, 9.6% # Test item 5-15- 2- 1

T AMCA: MESE 2265: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.850 MOhm, 10.0% # Test item 5-15- 2- 2

T AMCA: MESE 2265: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.902 MOhm, 6.5% # Test item 5-15- 2- 3

T AMCA: MESE 2265: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.787 MOhm, 14.2% # Test item 5-15- 2- 4

T AMCA: MESE 2266: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.668 MOhm, 22.2% # Test item 6-15- 2- 1

T AMCA: MESE 2266: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.780 MOhm, 14.7% # Test item 6-15- 2- 2

T AMCA: MESE 2266: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.762 MOhm, 15.9% # Test item 6-15- 2- 3

T AMCA: MESE 2266: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.693 MOhm, 20.4% # Test item 6-15- 2- 4

T AMCA: MESE 2267: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.764 MOhm, 15.7% # Test item 7-15- 2- 1

T AMCA: MESE 2267: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.840 MOhm, 10.7% # Test item 7-15- 2- 2

T AMCA: MESE 2267: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.712 MOhm, 19.2% # Test item 7-15- 2- 3

T AMCA: MESE 2267: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.787 MOhm, 14.2% # Test item 7-15- 2- 4

T AMCA: MESE 2268: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.716 MOhm, 18.9% # Test item 8-15- 2- 1

T AMCA: MESE 2268: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.686 MOhm, 20.9% # Test item 8-15- 2- 2

T AMCA: MESE 2268: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.786 MOhm, 14.3% # Test item 8-15- 2- 3

T AMCA: MESE 2268: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.812 MOhm, 12.6% # Test item 8-15- 2- 4

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.034V, neg = 0.034V # Test item 1-15- 3- 1

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.707 V, 19.3% >> pos = 2.388V, neg = -2.319V # Test item 1-15- 3- 2

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.034V, neg = 0.034V # Test item 1-15- 3- 3

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.5% >> pos = 0.817V, neg = -0.749V # Test item 1-15- 3- 4

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.034V, neg = 0.034V # Test item 1-15- 3- 5

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.2% >> pos = 0.132V, neg = -0.064V # Test item 1-15- 3- 6

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.034V, neg = -0.034V # Test item 1-15- 3- 7

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.706 V, 19.6% >> pos = 2.319V, neg = -2.387V # Test item 1-15- 3- 8

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.034V, neg = -0.034V # Test item 1-15- 3- 9

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.6% >> pos = 0.748V, neg = -0.817V # Test item 1-15- 3-10

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.034V, neg = -0.034V # Test item 1-15- 3-11

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.2% >> pos = 0.064V, neg = -0.132V # Test item 1-15- 3-12

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.034V, neg = 0.034V # Test item 1-15- 3-13

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.706 V, 19.5% >> pos = 2.387V, neg = -2.320V # Test item 1-15- 3-14

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.034V, neg = 0.034V # Test item 1-15- 3-15

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.6% >> pos = 0.816V, neg = -0.749V # Test item 1-15- 3-16

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.034V, neg = 0.034V # Test item 1-15- 3-17

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.2% >> pos = 0.132V, neg = -0.064V # Test item 1-15- 3-18

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.034V, neg = -0.034V # Test item 1-15- 3-19

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.706 V, 19.6% >> pos = 2.319V, neg = -2.387V # Test item 1-15- 3-20

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.034V, neg = -0.034V # Test item 1-15- 3-21

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.7% >> pos = 0.749V, neg = -0.816V # Test item 1-15- 3-22

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.034V, neg = -0.034V # Test item 1-15- 3-23

T AMCA: MESE 2261: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.2% >> pos = 0.064V, neg = -0.132V # Test item 1-15- 3-24

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.027V, neg = 0.027V # Test item 2-15- 3- 1

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.708 V, 19.1% >> pos = 2.381V, neg = -2.327V # Test item 2-15- 3- 2

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.027V, neg = 0.027V # Test item 2-15- 3- 3

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.2% >> pos = 0.810V, neg = -0.756V # Test item 2-15- 3- 4

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.027V, neg = 0.027V # Test item 2-15- 3- 5

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.7% >> pos = 0.125V, neg = -0.071V # Test item 2-15- 3- 6

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 2-15- 3- 7

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.708 V, 19.2% >> pos = 2.327V, neg = -2.381V # Test item 2-15- 3- 8

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 2-15- 3- 9

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.2% >> pos = 0.756V, neg = -0.810V # Test item 2-15- 3-10

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 2-15- 3-11

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.6% >> pos = 0.071V, neg = -0.125V # Test item 2-15- 3-12

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.028V, neg = 0.028V # Test item 2-15- 3-13

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.708 V, 19.2% >> pos = 2.382V, neg = -2.326V # Test item 2-15- 3-14

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.028V, neg = 0.028V # Test item 2-15- 3-15

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.1% >> pos = 0.811V, neg = -0.755V # Test item 2-15- 3-16

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.028V, neg = 0.028V # Test item 2-15- 3-17

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.8% >> pos = 0.126V, neg = -0.070V # Test item 2-15- 3-18

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.028V, neg = -0.028V # Test item 2-15- 3-19

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.709 V, 19.0% >> pos = 2.326V, neg = -2.382V # Test item 2-15- 3-20

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.028V, neg = -0.028V # Test item 2-15- 3-21

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.1% >> pos = 0.755V, neg = -0.811V # Test item 2-15- 3-22

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.028V, neg = -0.028V # Test item 2-15- 3-23

T AMCA: MESE 2262: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.9% >> pos = 0.070V, neg = -0.126V # Test item 2-15- 3-24

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.036V, neg = 0.036V # Test item 3-15- 3- 1

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.694 V, 22.1% >> pos = 2.383V, neg = -2.311V # Test item 3-15- 3- 2

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.036V, neg = 0.036V # Test item 3-15- 3- 3

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.561 V, 24.4% >> pos = 0.817V, neg = -0.744V # Test item 3-15- 3- 4

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.036V, neg = 0.036V # Test item 3-15- 3- 5

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.1% >> pos = 0.134V, neg = -0.061V # Test item 3-15- 3- 6

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.036V, neg = -0.036V # Test item 3-15- 3- 7

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.693 V, 22.2% >> pos = 2.310V, neg = -2.383V # Test item 3-15- 3- 8

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.036V, neg = -0.036V # Test item 3-15- 3- 9

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.561 V, 24.4% >> pos = 0.744V, neg = -0.817V # Test item 3-15- 3-10

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.036V, neg = -0.036V # Test item 3-15- 3-11

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.2% >> pos = 0.061V, neg = -0.134V # Test item 3-15- 3-12

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.036V, neg = 0.036V # Test item 3-15- 3-13

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.693 V, 22.3% >> pos = 2.383V, neg = -2.310V # Test item 3-15- 3-14

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.036V, neg = 0.036V # Test item 3-15- 3-15

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.561 V, 24.5% >> pos = 0.817V, neg = -0.744V # Test item 3-15- 3-16

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.036V, neg = 0.036V # Test item 3-15- 3-17

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.0% >> pos = 0.134V, neg = -0.061V # Test item 3-15- 3-18

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.036V, neg = -0.036V # Test item 3-15- 3-19

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.693 V, 22.3% >> pos = 2.310V, neg = -2.383V # Test item 3-15- 3-20

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.036V, neg = -0.036V # Test item 3-15- 3-21

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.561 V, 24.5% >> pos = 0.744V, neg = -0.817V # Test item 3-15- 3-22

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.036V, neg = -0.036V # Test item 3-15- 3-23

T AMCA: MESE 2263: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.1% >> pos = 0.061V, neg = -0.134V # Test item 3-15- 3-24

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.026V, neg = 0.026V # Test item 4-15- 3- 1

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.696 V, 21.7% >> pos = 2.374V, neg = -2.321V # Test item 4-15- 3- 2

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.026V, neg = 0.026V # Test item 4-15- 3- 3

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.7% >> pos = 0.807V, neg = -0.755V # Test item 4-15- 3- 4

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.026V, neg = 0.026V # Test item 4-15- 3- 5

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 22.6% >> pos = 0.124V, neg = -0.071V # Test item 4-15- 3- 6

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.026V, neg = -0.026V # Test item 4-15- 3- 7

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.696 V, 21.6% >> pos = 2.322V, neg = -2.375V # Test item 4-15- 3- 8

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.026V, neg = -0.026V # Test item 4-15- 3- 9

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.6% >> pos = 0.755V, neg = -0.808V # Test item 4-15- 3-10

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.026V, neg = -0.026V # Test item 4-15- 3-11

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 22.5% >> pos = 0.071V, neg = -0.124V # Test item 4-15- 3-12

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.025V, neg = 0.025V # Test item 4-15- 3-13

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.695 V, 21.8% >> pos = 2.373V, neg = -2.322V # Test item 4-15- 3-14

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 4-15- 3-15

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.8% >> pos = 0.807V, neg = -0.755V # Test item 4-15- 3-16

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.026V, neg = 0.026V # Test item 4-15- 3-17

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 22.5% >> pos = 0.123V, neg = -0.072V # Test item 4-15- 3-18

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.026V, neg = -0.026V # Test item 4-15- 3-19

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.695 V, 21.9% >> pos = 2.322V, neg = -2.373V # Test item 4-15- 3-20

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 4-15- 3-21

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.8% >> pos = 0.755V, neg = -0.807V # Test item 4-15- 3-22

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.026V, neg = -0.026V # Test item 4-15- 3-23

T AMCA: MESE 2264: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 22.5% >> pos = 0.072V, neg = -0.123V # Test item 4-15- 3-24

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 5-15- 3- 1

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.704 V, 20.1% >> pos = 2.377V, neg = -2.326V # Test item 5-15- 3- 2

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 5-15- 3- 3

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.1% >> pos = 0.808V, neg = -0.757V # Test item 5-15- 3- 4

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 5-15- 3- 5

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.9% >> pos = 0.123V, neg = -0.073V # Test item 5-15- 3- 6

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 5-15- 3- 7

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.705 V, 19.8% >> pos = 2.327V, neg = -2.378V # Test item 5-15- 3- 8

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 5-15- 3- 9

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.0% >> pos = 0.757V, neg = -0.808V # Test item 5-15- 3-10

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 5-15- 3-11

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.7% >> pos = 0.072V, neg = -0.123V # Test item 5-15- 3-12

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.024V, neg = 0.024V # Test item 5-15- 3-13

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.704 V, 20.0% >> pos = 2.377V, neg = -2.327V # Test item 5-15- 3-14

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.024V, neg = 0.024V # Test item 5-15- 3-15

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.9% >> pos = 0.807V, neg = -0.758V # Test item 5-15- 3-16

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.024V, neg = 0.024V # Test item 5-15- 3-17

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.5% >> pos = 0.123V, neg = -0.073V # Test item 5-15- 3-18

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 5-15- 3-19

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.705 V, 19.8% >> pos = 2.328V, neg = -2.377V # Test item 5-15- 3-20

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 5-15- 3-21

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.8% >> pos = 0.758V, neg = -0.807V # Test item 5-15- 3-22

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 5-15- 3-23

T AMCA: MESE 2265: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.6% >> pos = 0.073V, neg = -0.123V # Test item 5-15- 3-24

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 6-15- 3- 1

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.3% >> pos = 2.377V, neg = -2.326V # Test item 6-15- 3- 2

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 6-15- 3- 3

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.2% >> pos = 0.808V, neg = -0.757V # Test item 6-15- 3- 4

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 6-15- 3- 5

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.9% >> pos = 0.123V, neg = -0.073V # Test item 6-15- 3- 6

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 6-15- 3- 7

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.704 V, 20.0% >> pos = 2.326V, neg = -2.377V # Test item 6-15- 3- 8

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 6-15- 3- 9

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.1% >> pos = 0.757V, neg = -0.808V # Test item 6-15- 3-10

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 6-15- 3-11

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.0% >> pos = 0.072V, neg = -0.123V # Test item 6-15- 3-12

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 6-15- 3-13

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.3% >> pos = 2.376V, neg = -2.327V # Test item 6-15- 3-14

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 6-15- 3-15

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.1% >> pos = 0.807V, neg = -0.758V # Test item 6-15- 3-16

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 6-15- 3-17

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.7% >> pos = 0.123V, neg = -0.073V # Test item 6-15- 3-18

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 6-15- 3-19

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.1% >> pos = 2.327V, neg = -2.376V # Test item 6-15- 3-20

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 6-15- 3-21

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.1% >> pos = 0.758V, neg = -0.807V # Test item 6-15- 3-22

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 6-15- 3-23

T AMCA: MESE 2266: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.8% >> pos = 0.073V, neg = -0.123V # Test item 6-15- 3-24

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.007V, neg = -0.008V # Test item 7-15- 3- 1

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.691 V, 22.7% >> pos = 2.338V, neg = -2.353V # Test item 7-15- 3- 2

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.008V, neg = -0.008V # Test item 7-15- 3- 3

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.561 V, 24.6% >> pos = 0.773V, neg = -0.788V # Test item 7-15- 3- 4

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = -0.008V, neg = -0.008V # Test item 7-15- 3- 5

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.6% >> pos = 0.090V, neg = -0.105V # Test item 7-15- 3- 6

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.008V, neg = 0.008V # Test item 7-15- 3- 7

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.692 V, 22.6% >> pos = 2.353V, neg = -2.338V # Test item 7-15- 3- 8

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.007V, neg = 0.008V # Test item 7-15- 3- 9

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.561 V, 24.6% >> pos = 0.788V, neg = -0.773V # Test item 7-15- 3-10

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.007V, neg = 0.007V # Test item 7-15- 3-11

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.6% >> pos = 0.105V, neg = -0.090V # Test item 7-15- 3-12

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.008V, neg = -0.008V # Test item 7-15- 3-13

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.691 V, 22.7% >> pos = 2.338V, neg = -2.353V # Test item 7-15- 3-14

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.008V, neg = -0.008V # Test item 7-15- 3-15

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.561 V, 24.7% >> pos = 0.772V, neg = -0.788V # Test item 7-15- 3-16

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.008V, neg = -0.008V # Test item 7-15- 3-17

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.4% >> pos = 0.090V, neg = -0.106V # Test item 7-15- 3-18

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.008V, neg = 0.008V # Test item 7-15- 3-19

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.693 V, 22.4% >> pos = 2.354V, neg = -2.339V # Test item 7-15- 3-20

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.008V, neg = 0.008V # Test item 7-15- 3-21

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.561 V, 24.6% >> pos = 0.788V, neg = -0.773V # Test item 7-15- 3-22

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.008V, neg = 0.008V # Test item 7-15- 3-23

T AMCA: MESE 2267: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.5% >> pos = 0.105V, neg = -0.090V # Test item 7-15- 3-24

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.023V, neg = 0.023V # Test item 8-15- 3- 1

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.708 V, 19.2% >> pos = 2.377V, neg = -2.331V # Test item 8-15- 3- 2

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.023V, neg = 0.023V # Test item 8-15- 3- 3

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.3% >> pos = 0.806V, neg = -0.760V # Test item 8-15- 3- 4

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.023V, neg = 0.023V # Test item 8-15- 3- 5

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.7% >> pos = 0.121V, neg = -0.075V # Test item 8-15- 3- 6

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.023V, neg = -0.023V # Test item 8-15- 3- 7

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.708 V, 19.1% >> pos = 2.331V, neg = -2.377V # Test item 8-15- 3- 8

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.023V, neg = -0.023V # Test item 8-15- 3- 9

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.2% >> pos = 0.760V, neg = -0.806V # Test item 8-15- 3-10

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.023V, neg = -0.023V # Test item 8-15- 3-11

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.5% >> pos = 0.075V, neg = -0.121V # Test item 8-15- 3-12

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.023V, neg = 0.023V # Test item 8-15- 3-13

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.708 V, 19.2% >> pos = 2.377V, neg = -2.331V # Test item 8-15- 3-14

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.023V, neg = 0.023V # Test item 8-15- 3-15

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.2% >> pos = 0.806V, neg = -0.760V # Test item 8-15- 3-16

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.023V, neg = 0.023V # Test item 8-15- 3-17

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.6% >> pos = 0.121V, neg = -0.075V # Test item 8-15- 3-18

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = -0.023V, neg = -0.023V # Test item 8-15- 3-19

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.708 V, 19.2% >> pos = 2.331V, neg = -2.377V # Test item 8-15- 3-20

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.023V, neg = -0.023V # Test item 8-15- 3-21

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.1% >> pos = 0.760V, neg = -0.806V # Test item 8-15- 3-22

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.023V, neg = -0.023V # Test item 8-15- 3-23

T AMCA: MESE 2268: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.7% >> pos = 0.075V, neg = -0.121V # Test item 8-15- 3-24

T AMCA: MESE 2261: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 2.9% # Test item 1-16- 1- 1

T AMCA: MESE 2261: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.4% # Test item 1-16- 1- 2

T AMCA: MESE 2261: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.6% # Test item 1-16- 1- 3

T AMCA: MESE 2261: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 2.9% # Test item 1-16- 1- 4

T AMCA: MESE 2261: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.5% # Test item 1-16- 1- 5

T AMCA: MESE 2261: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.6% # Test item 1-16- 1- 6

T AMCA: MESE 2261: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 2.9% # Test item 1-16- 1- 7

T AMCA: MESE 2261: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.5% # Test item 1-16- 1- 8

T AMCA: MESE 2261: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.6% # Test item 1-16- 1- 9

T AMCA: MESE 2261: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 2.9% # Test item 1-16- 1-10

T AMCA: MESE 2261: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.7% # Test item 1-16- 1-11

T AMCA: MESE 2261: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.6% # Test item 1-16- 1-12

T AMCA: MESE 2262: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.5% # Test item 2-16- 1- 1

T AMCA: MESE 2262: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.4% # Test item 2-16- 1- 2

T AMCA: MESE 2262: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.5% # Test item 2-16- 1- 3

T AMCA: MESE 2262: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.5% # Test item 2-16- 1- 4

T AMCA: MESE 2262: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.5% # Test item 2-16- 1- 5

T AMCA: MESE 2262: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.4% # Test item 2-16- 1- 6

T AMCA: MESE 2262: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.6% # Test item 2-16- 1- 7

T AMCA: MESE 2262: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.4% # Test item 2-16- 1- 8

T AMCA: MESE 2262: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.5% # Test item 2-16- 1- 9

T AMCA: MESE 2262: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.5% # Test item 2-16- 1-10

T AMCA: MESE 2262: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.4% # Test item 2-16- 1-11

T AMCA: MESE 2262: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.4% # Test item 2-16- 1-12

T AMCA: MESE 2263: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.8% # Test item 3-16- 1- 1

T AMCA: MESE 2263: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.8% # Test item 3-16- 1- 2

T AMCA: MESE 2263: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.3% # Test item 3-16- 1- 3

T AMCA: MESE 2263: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 2.9% # Test item 3-16- 1- 4

T AMCA: MESE 2263: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.2% # Test item 3-16- 1- 5

T AMCA: MESE 2263: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.4% # Test item 3-16- 1- 6

T AMCA: MESE 2263: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 2.8% # Test item 3-16- 1- 7

T AMCA: MESE 2263: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.2% # Test item 3-16- 1- 8

T AMCA: MESE 2263: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.4% # Test item 3-16- 1- 9

T AMCA: MESE 2263: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 2.8% # Test item 3-16- 1-10

T AMCA: MESE 2263: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.0% # Test item 3-16- 1-11

T AMCA: MESE 2263: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.3% # Test item 3-16- 1-12

T AMCA: MESE 2264: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.4% # Test item 4-16- 1- 1

T AMCA: MESE 2264: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 10.2% # Test item 4-16- 1- 2

T AMCA: MESE 2264: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.990 V, 3.2% # Test item 4-16- 1- 3

T AMCA: MESE 2264: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.4% # Test item 4-16- 1- 4

T AMCA: MESE 2264: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 10.3% # Test item 4-16- 1- 5

T AMCA: MESE 2264: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.990 V, 3.2% # Test item 4-16- 1- 6

T AMCA: MESE 2264: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.4% # Test item 4-16- 1- 7

T AMCA: MESE 2264: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.9% # Test item 4-16- 1- 8

T AMCA: MESE 2264: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.991 V, 3.1% # Test item 4-16- 1- 9

T AMCA: MESE 2264: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.5% # Test item 4-16- 1-10

T AMCA: MESE 2264: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 10.2% # Test item 4-16- 1-11

T AMCA: MESE 2264: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.990 V, 3.2% # Test item 4-16- 1-12

T AMCA: MESE 2265: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.3% # Test item 5-16- 1- 1

T AMCA: MESE 2265: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 11.7% # Test item 5-16- 1- 2

T AMCA: MESE 2265: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.990 V, 3.5% # Test item 5-16- 1- 3

T AMCA: MESE 2265: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.4% # Test item 5-16- 1- 4

T AMCA: MESE 2265: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 11.8% # Test item 5-16- 1- 5

T AMCA: MESE 2265: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.989 V, 3.5% # Test item 5-16- 1- 6

T AMCA: MESE 2265: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.4% # Test item 5-16- 1- 7

T AMCA: MESE 2265: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 11.8% # Test item 5-16- 1- 8

T AMCA: MESE 2265: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.990 V, 3.4% # Test item 5-16- 1- 9

T AMCA: MESE 2265: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.3% # Test item 5-16- 1-10

T AMCA: MESE 2265: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 12.0% # Test item 5-16- 1-11

T AMCA: MESE 2265: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.990 V, 3.5% # Test item 5-16- 1-12

T AMCA: MESE 2266: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 2.0% # Test item 6-16- 1- 1

T AMCA: MESE 2266: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.7% # Test item 6-16- 1- 2

T AMCA: MESE 2266: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.8% # Test item 6-16- 1- 3

T AMCA: MESE 2266: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 2.0% # Test item 6-16- 1- 4

T AMCA: MESE 2266: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.8% # Test item 6-16- 1- 5

T AMCA: MESE 2266: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 0.9% # Test item 6-16- 1- 6

T AMCA: MESE 2266: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 2.1% # Test item 6-16- 1- 7

T AMCA: MESE 2266: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.7% # Test item 6-16- 1- 8

T AMCA: MESE 2266: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.8% # Test item 6-16- 1- 9

T AMCA: MESE 2266: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 2.0% # Test item 6-16- 1-10

T AMCA: MESE 2266: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.4% # Test item 6-16- 1-11

T AMCA: MESE 2266: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.8% # Test item 6-16- 1-12

T AMCA: MESE 2267: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.6% # Test item 7-16- 1- 1

T AMCA: MESE 2267: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.1% # Test item 7-16- 1- 2

T AMCA: MESE 2267: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.1% # Test item 7-16- 1- 3

T AMCA: MESE 2267: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.7% # Test item 7-16- 1- 4

T AMCA: MESE 2267: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.8% # Test item 7-16- 1- 5

T AMCA: MESE 2267: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.1% # Test item 7-16- 1- 6

T AMCA: MESE 2267: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.6% # Test item 7-16- 1- 7

T AMCA: MESE 2267: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.9% # Test item 7-16- 1- 8

T AMCA: MESE 2267: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.1% # Test item 7-16- 1- 9

T AMCA: MESE 2267: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.6% # Test item 7-16- 1-10

T AMCA: MESE 2267: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.1% # Test item 7-16- 1-11

T AMCA: MESE 2267: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.1% # Test item 7-16- 1-12

T AMCA: MESE 2268: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.1% # Test item 8-16- 1- 1

T AMCA: MESE 2268: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.4% # Test item 8-16- 1- 2

T AMCA: MESE 2268: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.4% # Test item 8-16- 1- 3

T AMCA: MESE 2268: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.2% # Test item 8-16- 1- 4

T AMCA: MESE 2268: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.5% # Test item 8-16- 1- 5

T AMCA: MESE 2268: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.4% # Test item 8-16- 1- 6

T AMCA: MESE 2268: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.2% # Test item 8-16- 1- 7

T AMCA: MESE 2268: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.3% # Test item 8-16- 1- 8

T AMCA: MESE 2268: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.4% # Test item 8-16- 1- 9

T AMCA: MESE 2268: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.3% # Test item 8-16- 1-10

T AMCA: MESE 2268: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.4% # Test item 8-16- 1-11

T AMCA: MESE 2268: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.4% # Test item 8-16- 1-12

T AMCA: MESE 2261: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9873.791 Ohm, 12.6% >> MV = 1.786V, offset = -0.189V # Test item 1-16- 2- 1

T AMCA: MESE 2261: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.942 Ohm, 18.2% >> MV = 0.174V, offset = 0.003V # Test item 1-16- 2- 2

T AMCA: MESE 2261: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.353 Ohm, 14.4% >> MV = 0.137V, offset = 0.004V # Test item 1-16- 2- 3

T AMCA: MESE 2261: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9881.974 Ohm, 11.8% >> MV = 1.785V, offset = -0.192V # Test item 1-16- 2- 4

T AMCA: MESE 2261: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.110 Ohm, 15.3% >> MV = 0.174V, offset = 0.003V # Test item 1-16- 2- 5

T AMCA: MESE 2261: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.353 Ohm, 14.4% >> MV = 0.137V, offset = 0.004V # Test item 1-16- 2- 6

T AMCA: MESE 2262: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9903.374 Ohm, 9.7% >> MV = 1.794V, offset = -0.187V # Test item 2-16- 2- 1

T AMCA: MESE 2262: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.194 Ohm, 13.9% >> MV = 0.173V, offset = 0.001V # Test item 2-16- 2- 2

T AMCA: MESE 2262: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.521 Ohm, 10.6% >> MV = 0.135V, offset = 0.002V # Test item 2-16- 2- 3

T AMCA: MESE 2262: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9902.115 Ohm, 9.8% >> MV = 1.792V, offset = -0.189V # Test item 2-16- 2- 4

T AMCA: MESE 2262: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.026 Ohm, 16.8% >> MV = 0.173V, offset = 0.002V # Test item 2-16- 2- 5

T AMCA: MESE 2262: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.479 Ohm, 11.6% >> MV = 0.136V, offset = 0.002V # Test item 2-16- 2- 6

T AMCA: MESE 2263: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9847.984 Ohm, 15.2% >> MV = 1.758V, offset = -0.212V # Test item 3-16- 2- 1

T AMCA: MESE 2263: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.320 Ohm, 11.7% >> MV = 0.173V, offset = 0.001V # Test item 3-16- 2- 2

T AMCA: MESE 2263: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.605 Ohm, 8.8% >> MV = 0.135V, offset = 0.001V # Test item 3-16- 2- 3

T AMCA: MESE 2263: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9846.097 Ohm, 15.4% >> MV = 1.760V, offset = -0.209V # Test item 3-16- 2- 4

T AMCA: MESE 2263: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.278 Ohm, 12.5% >> MV = 0.172V, offset = 0.001V # Test item 3-16- 2- 5

T AMCA: MESE 2263: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.563 Ohm, 9.7% >> MV = 0.135V, offset = 0.002V # Test item 3-16- 2- 6

T AMCA: MESE 2264: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9878.197 Ohm, 12.2% >> MV = 1.781V, offset = -0.195V # Test item 4-16- 2- 1

T AMCA: MESE 2264: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.690 Ohm, 22.6% >> MV = 0.175V, offset = 0.005V # Test item 4-16- 2- 2

T AMCA: MESE 2264: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.228 Ohm, 17.2% >> MV = 0.139V, offset = 0.006V # Test item 4-16- 2- 3

T AMCA: MESE 2264: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9883.861 Ohm, 11.6% >> MV = 1.782V, offset = -0.195V # Test item 4-16- 2- 4

T AMCA: MESE 2264: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.942 Ohm, 18.2% >> MV = 0.176V, offset = 0.005V # Test item 4-16- 2- 5

T AMCA: MESE 2264: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.395 Ohm, 13.4% >> MV = 0.139V, offset = 0.006V # Test item 4-16- 2- 6

T AMCA: MESE 2265: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9838.543 Ohm, 16.1% >> MV = 1.770V, offset = -0.198V # Test item 5-16- 2- 1

T AMCA: MESE 2265: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.236 Ohm, 13.2% >> MV = 0.178V, offset = 0.006V # Test item 5-16- 2- 2

T AMCA: MESE 2265: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.479 Ohm, 11.6% >> MV = 0.141V, offset = 0.008V # Test item 5-16- 2- 3

T AMCA: MESE 2265: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9835.396 Ohm, 16.5% >> MV = 1.771V, offset = -0.196V # Test item 5-16- 2- 4

T AMCA: MESE 2265: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.404 Ohm, 10.3% >> MV = 0.179V, offset = 0.006V # Test item 5-16- 2- 5

T AMCA: MESE 2265: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.605 Ohm, 8.8% >> MV = 0.142V, offset = 0.008V # Test item 5-16- 2- 6

T AMCA: MESE 2266: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9872.532 Ohm, 12.7% >> MV = 1.779V, offset = -0.196V # Test item 6-16- 2- 1

T AMCA: MESE 2266: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.529 Ohm, 8.1% >> MV = 0.173V, offset = 0.001V # Test item 6-16- 2- 2

T AMCA: MESE 2266: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.899 Ohm, 2.2% >> MV = 0.136V, offset = 0.001V # Test item 6-16- 2- 3

T AMCA: MESE 2266: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9868.756 Ohm, 13.1% >> MV = 1.778V, offset = -0.196V # Test item 6-16- 2- 4

T AMCA: MESE 2266: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.278 Ohm, 12.5% >> MV = 0.173V, offset = 0.001V # Test item 6-16- 2- 5

T AMCA: MESE 2266: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.773 Ohm, 5.0% >> MV = 0.136V, offset = 0.001V # Test item 6-16- 2- 6

T AMCA: MESE 2267: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9851.132 Ohm, 14.9% >> MV = 2.194V, offset = 0.224V # Test item 7-16- 2- 1

T AMCA: MESE 2267: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 58.033 Ohm, 0.6% >> MV = 0.182V, offset = 0.008V # Test item 7-16- 2- 2

T AMCA: MESE 2267: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 45.277 Ohm, 6.1% >> MV = 0.144V, offset = 0.008V # Test item 7-16- 2- 3

T AMCA: MESE 2267: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9850.502 Ohm, 14.9% >> MV = 2.192V, offset = 0.222V # Test item 7-16- 2- 4

T AMCA: MESE 2267: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.194 Ohm, 13.9% >> MV = 0.180V, offset = 0.008V # Test item 7-16- 2- 5

T AMCA: MESE 2267: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.605 Ohm, 8.8% >> MV = 0.142V, offset = 0.008V # Test item 7-16- 2- 6

T AMCA: MESE 2268: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9885.121 Ohm, 11.5% >> MV = 1.799V, offset = -0.179V # Test item 8-16- 2- 1

T AMCA: MESE 2268: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.110 Ohm, 15.3% >> MV = 0.172V, offset = 0.000V # Test item 8-16- 2- 2

T AMCA: MESE 2268: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.395 Ohm, 13.4% >> MV = 0.135V, offset = 0.002V # Test item 8-16- 2- 3

T AMCA: MESE 2268: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9888.897 Ohm, 11.1% >> MV = 1.796V, offset = -0.182V # Test item 8-16- 2- 4

T AMCA: MESE 2268: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.194 Ohm, 13.9% >> MV = 0.172V, offset = 0.000V # Test item 8-16- 2- 5

T AMCA: MESE 2268: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.479 Ohm, 11.6% >> MV = 0.135V, offset = 0.001V # Test item 8-16- 2- 6

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.8% >> pos = 0.066V, neg = 0.070V # Test item 1-16- 3- 1

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.349 V, 46.6% >> pos = 1.742V, neg = -1.607V # Test item 1-16- 3- 2

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = 0.047V, neg = 0.046V # Test item 1-16- 3- 3

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.665 V, 40.8% >> pos = 0.883V, neg = -0.782V # Test item 1-16- 3- 4

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.9% >> pos = 0.039V, neg = 0.036V # Test item 1-16- 3- 5

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.832 V, 39.7% >> pos = 0.454V, neg = -0.378V # Test item 1-16- 3- 6

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.034V, neg = 0.034V # Test item 1-16- 3- 7

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.262 V, 46.2% >> pos = 0.165V, neg = -0.097V # Test item 1-16- 3- 8

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.031V, neg = 0.031V # Test item 1-16- 3- 9

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 22.8% >> pos = 0.084V, neg = -0.021V # Test item 1-16- 3-10

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.5% >> pos = 0.008V, neg = 0.009V # Test item 1-16- 3-11

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.365 V, 51.6% >> pos = 1.688V, neg = -1.677V # Test item 1-16- 3-12

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.6% >> pos = 0.008V, neg = 0.002V # Test item 1-16- 3-13

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.672 V, 44.8% >> pos = 0.826V, neg = -0.846V # Test item 1-16- 3-14

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% >> pos = -0.019V, neg = -0.021V # Test item 1-16- 3-15

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.836 V, 44.5% >> pos = 0.399V, neg = -0.437V # Test item 1-16- 3-16

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.1% >> pos = -0.024V, neg = -0.022V # Test item 1-16- 3-17

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.260 V, 41.7% >> pos = 0.106V, neg = -0.154V # Test item 1-16- 3-18

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = -0.026V, neg = -0.027V # Test item 1-16- 3-19

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.103 V, 13.9% >> pos = 0.025V, neg = -0.078V # Test item 1-16- 3-20

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.4% >> pos = 0.068V, neg = 0.065V # Test item 1-16- 3-21

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.347 V, 46.0% >> pos = 1.740V, neg = -1.607V # Test item 1-16- 3-22

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% >> pos = 0.048V, neg = 0.049V # Test item 1-16- 3-23

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.663 V, 39.3% >> pos = 0.879V, neg = -0.783V # Test item 1-16- 3-24

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.9% >> pos = 0.043V, neg = 0.037V # Test item 1-16- 3-25

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.829 V, 35.7% >> pos = 0.453V, neg = -0.376V # Test item 1-16- 3-26

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.034V, neg = 0.034V # Test item 1-16- 3-27

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.260 V, 42.0% >> pos = 0.164V, neg = -0.096V # Test item 1-16- 3-28

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.032V, neg = 0.032V # Test item 1-16- 3-29

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 18.3% >> pos = 0.083V, neg = -0.021V # Test item 1-16- 3-30

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.5% >> pos = 0.007V, neg = 0.006V # Test item 1-16- 3-31

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.362 V, 50.7% >> pos = 1.690V, neg = -1.672V # Test item 1-16- 3-32

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% >> pos = -0.008V, neg = -0.010V # Test item 1-16- 3-33

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.668 V, 42.2% >> pos = 0.824V, neg = -0.844V # Test item 1-16- 3-34

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.1% >> pos = -0.021V, neg = -0.019V # Test item 1-16- 3-35

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.832 V, 40.6% >> pos = 0.397V, neg = -0.435V # Test item 1-16- 3-36

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = -0.023V, neg = -0.024V # Test item 1-16- 3-37

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.259 V, 36.6% >> pos = 0.106V, neg = -0.153V # Test item 1-16- 3-38

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.0% >> pos = -0.027V, neg = -0.026V # Test item 1-16- 3-39

T AMCA: MESE 2261: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 18.4% >> pos = 0.026V, neg = -0.078V # Test item 1-16- 3-40

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = 0.083V, neg = 0.082V # Test item 2-16- 3- 1

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.373 V, 54.0% >> pos = 1.776V, neg = -1.596V # Test item 2-16- 3- 2

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.4% >> pos = 0.056V, neg = 0.057V # Test item 2-16- 3- 3

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.670 V, 43.5% >> pos = 0.889V, neg = -0.780V # Test item 2-16- 3- 4

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.2% >> pos = 0.045V, neg = 0.042V # Test item 2-16- 3- 5

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.834 V, 42.3% >> pos = 0.461V, neg = -0.373V # Test item 2-16- 3- 6

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.036V, neg = 0.036V # Test item 2-16- 3- 7

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.263 V, 52.9% >> pos = 0.169V, neg = -0.095V # Test item 2-16- 3- 8

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.032V, neg = 0.032V # Test item 2-16- 3- 9

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 26.0% >> pos = 0.086V, neg = -0.019V # Test item 2-16- 3-10

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.006 V, 5.6% >> pos = 0.022V, neg = 0.028V # Test item 2-16- 3-11

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.355 V, 48.3% >> pos = 1.704V, neg = -1.651V # Test item 2-16- 3-12

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.0% >> pos = 0.013V, neg = 0.015V # Test item 2-16- 3-13

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.666 V, 41.0% >> pos = 0.830V, neg = -0.835V # Test item 2-16- 3-14

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = -0.016V, neg = -0.017V # Test item 2-16- 3-15

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.835 V, 44.1% >> pos = 0.402V, neg = -0.433V # Test item 2-16- 3-16

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = -0.023V, neg = -0.024V # Test item 2-16- 3-17

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.263 V, 51.6% >> pos = 0.109V, neg = -0.154V # Test item 2-16- 3-18

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.6% >> pos = -0.027V, neg = -0.024V # Test item 2-16- 3-19

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.106 V, 30.5% >> pos = 0.027V, neg = -0.079V # Test item 2-16- 3-20

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.6% >> pos = 0.080V, neg = 0.084V # Test item 2-16- 3-21

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.358 V, 49.3% >> pos = 1.764V, neg = -1.594V # Test item 2-16- 3-22

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.5% >> pos = 0.055V, neg = 0.055V # Test item 2-16- 3-23

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.668 V, 42.5% >> pos = 0.890V, neg = -0.778V # Test item 2-16- 3-24

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.043V, neg = 0.043V # Test item 2-16- 3-25

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.835 V, 43.4% >> pos = 0.460V, neg = -0.375V # Test item 2-16- 3-26

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.035V, neg = 0.035V # Test item 2-16- 3-27

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.268 V, 73.4% >> pos = 0.174V, neg = -0.095V # Test item 2-16- 3-28

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = 0.032V, neg = 0.034V # Test item 2-16- 3-29

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.097 V, 17.4% >> pos = 0.085V, neg = -0.012V # Test item 2-16- 3-30

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 7.6% >> pos = 0.027V, neg = 0.020V # Test item 2-16- 3-31

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.364 V, 51.2% >> pos = 1.712V, neg = -1.652V # Test item 2-16- 3-32

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = -0.001V, neg = -0.002V # Test item 2-16- 3-33

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.664 V, 39.8% >> pos = 0.828V, neg = -0.836V # Test item 2-16- 3-34

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.8% >> pos = -0.013V, neg = -0.017V # Test item 2-16- 3-35

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.836 V, 45.6% >> pos = 0.401V, neg = -0.435V # Test item 2-16- 3-36

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.5% >> pos = -0.023V, neg = -0.024V # Test item 2-16- 3-37

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.262 V, 47.1% >> pos = 0.107V, neg = -0.154V # Test item 2-16- 3-38

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% >> pos = -0.027V, neg = -0.027V # Test item 2-16- 3-39

T AMCA: MESE 2262: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 23.8% >> pos = 0.026V, neg = -0.079V # Test item 2-16- 3-40

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.097V, neg = 0.098V # Test item 3-16- 3- 1

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.339 V, 43.6% >> pos = 1.767V, neg = -1.572V # Test item 3-16- 3- 2

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.5% >> pos = 0.065V, neg = 0.064V # Test item 3-16- 3- 3

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.660 V, 37.3% >> pos = 0.893V, neg = -0.766V # Test item 3-16- 3- 4

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.1% >> pos = 0.053V, neg = 0.049V # Test item 3-16- 3- 5

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.834 V, 42.4% >> pos = 0.466V, neg = -0.368V # Test item 3-16- 3- 6

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.7% >> pos = 0.039V, neg = 0.041V # Test item 3-16- 3- 7

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.261 V, 44.8% >> pos = 0.171V, neg = -0.090V # Test item 3-16- 3- 8

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.5% >> pos = 0.038V, neg = 0.036V # Test item 3-16- 3- 9

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.106 V, 30.6% >> pos = 0.090V, neg = -0.016V # Test item 3-16- 3-10

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.4% >> pos = 0.033V, neg = 0.036V # Test item 3-16- 3-11

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.359 V, 49.8% >> pos = 1.717V, neg = -1.642V # Test item 3-16- 3-12

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = 0.014V, neg = 0.014V # Test item 3-16- 3-13

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.665 V, 40.6% >> pos = 0.833V, neg = -0.832V # Test item 3-16- 3-14

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.7% >> pos = -0.015V, neg = -0.019V # Test item 3-16- 3-15

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.832 V, 40.5% >> pos = 0.400V, neg = -0.432V # Test item 3-16- 3-16

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = -0.025V, neg = -0.024V # Test item 3-16- 3-17

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.261 V, 45.9% >> pos = 0.105V, neg = -0.156V # Test item 3-16- 3-18

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.3% >> pos = -0.028V, neg = -0.030V # Test item 3-16- 3-19

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 23.9% >> pos = 0.023V, neg = -0.082V # Test item 3-16- 3-20

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = 0.100V, neg = 0.100V # Test item 3-16- 3-21

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.352 V, 47.5% >> pos = 1.774V, neg = -1.578V # Test item 3-16- 3-22

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.064V, neg = 0.065V # Test item 3-16- 3-23

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.665 V, 40.5% >> pos = 0.897V, neg = -0.768V # Test item 3-16- 3-24

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.6% >> pos = 0.048V, neg = 0.050V # Test item 3-16- 3-25

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.830 V, 37.8% >> pos = 0.465V, neg = -0.365V # Test item 3-16- 3-26

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = 0.042V, neg = 0.041V # Test item 3-16- 3-27

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.260 V, 40.2% >> pos = 0.171V, neg = -0.089V # Test item 3-16- 3-28

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.7% >> pos = 0.038V, neg = 0.035V # Test item 3-16- 3-29

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 21.9% >> pos = 0.089V, neg = -0.015V # Test item 3-16- 3-30

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.7% >> pos = 0.029V, neg = 0.025V # Test item 3-16- 3-31

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.354 V, 48.0% >> pos = 1.706V, neg = -1.648V # Test item 3-16- 3-32

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.7% >> pos = -0.002V, neg = 0.001V # Test item 3-16- 3-33

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.661 V, 38.4% >> pos = 0.831V, neg = -0.831V # Test item 3-16- 3-34

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.2% >> pos = -0.016V, neg = -0.015V # Test item 3-16- 3-35

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.831 V, 39.0% >> pos = 0.399V, neg = -0.432V # Test item 3-16- 3-36

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.3% >> pos = -0.022V, neg = -0.024V # Test item 3-16- 3-37

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.260 V, 41.9% >> pos = 0.105V, neg = -0.155V # Test item 3-16- 3-38

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = -0.030V, neg = -0.030V # Test item 3-16- 3-39

T AMCA: MESE 2263: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 27.0% >> pos = 0.024V, neg = -0.081V # Test item 3-16- 3-40

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% >> pos = 0.064V, neg = 0.062V # Test item 4-16- 3- 1

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.326 V, 39.2% >> pos = 1.726V, neg = -1.599V # Test item 4-16- 3- 2

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.4% >> pos = 0.046V, neg = 0.047V # Test item 4-16- 3- 3

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.653 V, 33.4% >> pos = 0.873V, neg = -0.781V # Test item 4-16- 3- 4

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.7% >> pos = 0.039V, neg = 0.038V # Test item 4-16- 3- 5

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.830 V, 36.9% >> pos = 0.453V, neg = -0.377V # Test item 4-16- 3- 6

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.6% >> pos = 0.035V, neg = 0.033V # Test item 4-16- 3- 7

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.260 V, 40.0% >> pos = 0.164V, neg = -0.096V # Test item 4-16- 3- 8

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.031V, neg = 0.031V # Test item 4-16- 3- 9

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.103 V, 13.4% >> pos = 0.084V, neg = -0.019V # Test item 4-16- 3-10

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = 0.005V, neg = 0.004V # Test item 4-16- 3-11

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.362 V, 50.6% >> pos = 1.685V, neg = -1.677V # Test item 4-16- 3-12

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = 0.001V, neg = 0.001V # Test item 4-16- 3-13

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.662 V, 38.5% >> pos = 0.817V, neg = -0.845V # Test item 4-16- 3-14

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% >> pos = -0.020V, neg = -0.022V # Test item 4-16- 3-15

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.830 V, 37.3% >> pos = 0.394V, neg = -0.436V # Test item 4-16- 3-16

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = -0.026V, neg = -0.026V # Test item 4-16- 3-17

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.261 V, 42.7% >> pos = 0.103V, neg = -0.157V # Test item 4-16- 3-18

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.5% >> pos = -0.028V, neg = -0.029V # Test item 4-16- 3-19

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 24.5% >> pos = 0.025V, neg = -0.080V # Test item 4-16- 3-20

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = 0.067V, neg = 0.066V # Test item 4-16- 3-21

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.333 V, 41.7% >> pos = 1.732V, neg = -1.602V # Test item 4-16- 3-22

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.5% >> pos = 0.047V, neg = 0.046V # Test item 4-16- 3-23

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.656 V, 35.2% >> pos = 0.875V, neg = -0.781V # Test item 4-16- 3-24

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.6% >> pos = 0.039V, neg = 0.038V # Test item 4-16- 3-25

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.825 V, 31.5% >> pos = 0.451V, neg = -0.374V # Test item 4-16- 3-26

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% >> pos = 0.035V, neg = 0.034V # Test item 4-16- 3-27

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.260 V, 38.0% >> pos = 0.163V, neg = -0.096V # Test item 4-16- 3-28

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = 0.032V, neg = 0.031V # Test item 4-16- 3-29

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 25.4% >> pos = 0.084V, neg = -0.021V # Test item 4-16- 3-30

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.6% >> pos = 0.006V, neg = -0.001V # Test item 4-16- 3-31

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.356 V, 48.7% >> pos = 1.681V, neg = -1.675V # Test item 4-16- 3-32

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = -0.014V, neg = -0.014V # Test item 4-16- 3-33

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.668 V, 42.6% >> pos = 0.821V, neg = -0.847V # Test item 4-16- 3-34

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = -0.023V, neg = -0.023V # Test item 4-16- 3-35

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.832 V, 40.3% >> pos = 0.392V, neg = -0.440V # Test item 4-16- 3-36

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = -0.025V, neg = -0.026V # Test item 4-16- 3-37

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.262 V, 47.9% >> pos = 0.104V, neg = -0.158V # Test item 4-16- 3-38

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = -0.028V, neg = -0.029V # Test item 4-16- 3-39

T AMCA: MESE 2264: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 23.3% >> pos = 0.024V, neg = -0.080V # Test item 4-16- 3-40

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.1% >> pos = 0.063V, neg = 0.060V # Test item 5-16- 3- 1

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.373 V, 54.2% >> pos = 1.746V, neg = -1.627V # Test item 5-16- 3- 2

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = 0.044V, neg = 0.043V # Test item 5-16- 3- 3

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.671 V, 44.6% >> pos = 0.879V, neg = -0.792V # Test item 5-16- 3- 4

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.4% >> pos = 0.038V, neg = 0.036V # Test item 5-16- 3- 5

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.836 V, 45.0% >> pos = 0.453V, neg = -0.383V # Test item 5-16- 3- 6

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.2% >> pos = 0.030V, neg = 0.032V # Test item 5-16- 3- 7

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.262 V, 48.9% >> pos = 0.164V, neg = -0.098V # Test item 5-16- 3- 8

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = 0.031V, neg = 0.030V # Test item 5-16- 3- 9

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 26.4% >> pos = 0.084V, neg = -0.022V # Test item 5-16- 3-10

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.014 V, 14.4% >> pos = 0.000V, neg = -0.014V # Test item 5-16- 3-11

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.358 V, 49.3% >> pos = 1.681V, neg = -1.676V # Test item 5-16- 3-12

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = 0.000V, neg = -0.001V # Test item 5-16- 3-13

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.665 V, 40.8% >> pos = 0.818V, neg = -0.847V # Test item 5-16- 3-14

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = -0.023V, neg = -0.022V # Test item 5-16- 3-15

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.832 V, 40.1% >> pos = 0.394V, neg = -0.438V # Test item 5-16- 3-16

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = -0.025V, neg = -0.026V # Test item 5-16- 3-17

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.261 V, 43.9% >> pos = 0.104V, neg = -0.157V # Test item 5-16- 3-18

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.5% >> pos = -0.032V, neg = -0.030V # Test item 5-16- 3-19

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.103 V, 13.2% >> pos = 0.023V, neg = -0.079V # Test item 5-16- 3-20

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = 0.060V, neg = 0.061V # Test item 5-16- 3-21

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.367 V, 52.2% >> pos = 1.741V, neg = -1.626V # Test item 5-16- 3-22

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.5% >> pos = 0.043V, neg = 0.045V # Test item 5-16- 3-23

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.667 V, 41.9% >> pos = 0.878V, neg = -0.789V # Test item 5-16- 3-24

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.036V, neg = 0.035V # Test item 5-16- 3-25

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.835 V, 44.1% >> pos = 0.453V, neg = -0.382V # Test item 5-16- 3-26

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.3% >> pos = 0.034V, neg = 0.034V # Test item 5-16- 3-27

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.262 V, 47.9% >> pos = 0.164V, neg = -0.098V # Test item 5-16- 3-28

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.3% >> pos = 0.029V, neg = 0.031V # Test item 5-16- 3-29

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.106 V, 28.1% >> pos = 0.083V, neg = -0.022V # Test item 5-16- 3-30

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.005 V, 4.9% >> pos = -0.002V, neg = 0.003V # Test item 5-16- 3-31

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.361 V, 50.4% >> pos = 1.675V, neg = -1.686V # Test item 5-16- 3-32

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = -0.016V, neg = -0.018V # Test item 5-16- 3-33

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.668 V, 42.5% >> pos = 0.818V, neg = -0.850V # Test item 5-16- 3-34

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = -0.023V, neg = -0.024V # Test item 5-16- 3-35

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.834 V, 42.2% >> pos = 0.394V, neg = -0.440V # Test item 5-16- 3-36

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.4% >> pos = -0.027V, neg = -0.025V # Test item 5-16- 3-37

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.262 V, 48.4% >> pos = 0.106V, neg = -0.157V # Test item 5-16- 3-38

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = -0.028V, neg = -0.028V # Test item 5-16- 3-39

T AMCA: MESE 2265: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 21.5% >> pos = 0.024V, neg = -0.080V # Test item 5-16- 3-40

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.005 V, 5.3% >> pos = 0.087V, neg = 0.092V # Test item 6-16- 3- 1

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.356 V, 48.7% >> pos = 1.771V, neg = -1.585V # Test item 6-16- 3- 2

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.0% >> pos = 0.058V, neg = 0.060V # Test item 6-16- 3- 3

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.662 V, 38.5% >> pos = 0.889V, neg = -0.772V # Test item 6-16- 3- 4

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% >> pos = 0.044V, neg = 0.043V # Test item 6-16- 3- 5

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.830 V, 37.9% >> pos = 0.460V, neg = -0.370V # Test item 6-16- 3- 6

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.007 V, 7.0% >> pos = 0.037V, neg = 0.044V # Test item 6-16- 3- 7

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.258 V, 31.3% >> pos = 0.164V, neg = -0.094V # Test item 6-16- 3- 8

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% >> pos = 0.033V, neg = 0.034V # Test item 6-16- 3- 9

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 21.1% >> pos = 0.085V, neg = -0.020V # Test item 6-16- 3-10

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = 0.030V, neg = 0.030V # Test item 6-16- 3-11

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.330 V, 40.5% >> pos = 1.690V, neg = -1.640V # Test item 6-16- 3-12

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.5% >> pos = 0.015V, neg = 0.017V # Test item 6-16- 3-13

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.659 V, 36.9% >> pos = 0.834V, neg = -0.825V # Test item 6-16- 3-14

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.7% >> pos = -0.015V, neg = -0.011V # Test item 6-16- 3-15

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.831 V, 38.6% >> pos = 0.402V, neg = -0.428V # Test item 6-16- 3-16

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.4% >> pos = -0.022V, neg = -0.021V # Test item 6-16- 3-17

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.264 V, 56.8% >> pos = 0.110V, neg = -0.154V # Test item 6-16- 3-18

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = -0.025V, neg = -0.024V # Test item 6-16- 3-19

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 19.1% >> pos = 0.027V, neg = -0.077V # Test item 6-16- 3-20

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.016 V, 16.5% >> pos = 0.103V, neg = 0.086V # Test item 6-16- 3-21

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.359 V, 49.8% >> pos = 1.765V, neg = -1.594V # Test item 6-16- 3-22

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = 0.058V, neg = 0.059V # Test item 6-16- 3-23

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.664 V, 40.1% >> pos = 0.891V, neg = -0.773V # Test item 6-16- 3-24

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = 0.043V, neg = 0.042V # Test item 6-16- 3-25

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.835 V, 44.2% >> pos = 0.463V, neg = -0.372V # Test item 6-16- 3-26

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.6% >> pos = 0.035V, neg = 0.037V # Test item 6-16- 3-27

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.262 V, 46.4% >> pos = 0.168V, neg = -0.094V # Test item 6-16- 3-28

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.9% >> pos = 0.041V, neg = 0.032V # Test item 6-16- 3-29

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 21.1% >> pos = 0.085V, neg = -0.020V # Test item 6-16- 3-30

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.7% >> pos = 0.030V, neg = 0.034V # Test item 6-16- 3-31

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.339 V, 43.5% >> pos = 1.700V, neg = -1.639V # Test item 6-16- 3-32

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.3% >> pos = 0.001V, neg = -0.000V # Test item 6-16- 3-33

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.665 V, 40.4% >> pos = 0.833V, neg = -0.831V # Test item 6-16- 3-34

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.7% >> pos = -0.012V, neg = -0.014V # Test item 6-16- 3-35

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.828 V, 35.5% >> pos = 0.400V, neg = -0.429V # Test item 6-16- 3-36

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = -0.021V, neg = -0.022V # Test item 6-16- 3-37

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.261 V, 44.6% >> pos = 0.110V, neg = -0.151V # Test item 6-16- 3-38

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = -0.025V, neg = -0.025V # Test item 6-16- 3-39

T AMCA: MESE 2266: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 19.7% >> pos = 0.027V, neg = -0.077V # Test item 6-16- 3-40

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = 0.049V, neg = 0.049V # Test item 7-16- 3- 1

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.333 V, 41.7% >> pos = 1.719V, neg = -1.615V # Test item 7-16- 3- 2

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = 0.021V, neg = 0.022V # Test item 7-16- 3- 3

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.660 V, 37.6% >> pos = 0.851V, neg = -0.809V # Test item 7-16- 3- 4

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.4% >> pos = 0.011V, neg = 0.008V # Test item 7-16- 3- 5

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.832 V, 39.4% >> pos = 0.423V, neg = -0.409V # Test item 7-16- 3- 6

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.000V, neg = -0.000V # Test item 7-16- 3- 7

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.261 V, 42.1% >> pos = 0.131V, neg = -0.130V # Test item 7-16- 3- 8

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.5% >> pos = -0.003V, neg = -0.004V # Test item 7-16- 3- 9

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 23.5% >> pos = 0.048V, neg = -0.056V # Test item 7-16- 3-10

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.3% >> pos = 0.062V, neg = 0.060V # Test item 7-16- 3-11

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.348 V, 46.3% >> pos = 1.738V, neg = -1.610V # Test item 7-16- 3-12

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = 0.031V, neg = 0.031V # Test item 7-16- 3-13

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.655 V, 34.4% >> pos = 0.862V, neg = -0.793V # Test item 7-16- 3-14

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.8% >> pos = 0.021V, neg = 0.018V # Test item 7-16- 3-15

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.836 V, 44.5% >> pos = 0.441V, neg = -0.395V # Test item 7-16- 3-16

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = 0.013V, neg = 0.012V # Test item 7-16- 3-17

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.261 V, 43.3% >> pos = 0.144V, neg = -0.117V # Test item 7-16- 3-18

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = 0.010V, neg = 0.008V # Test item 7-16- 3-19

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 17.6% >> pos = 0.062V, neg = -0.041V # Test item 7-16- 3-20

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.5% >> pos = 0.049V, neg = 0.050V # Test item 7-16- 3-21

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.333 V, 41.4% >> pos = 1.717V, neg = -1.616V # Test item 7-16- 3-22

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.3% >> pos = 0.023V, neg = 0.026V # Test item 7-16- 3-23

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.661 V, 38.1% >> pos = 0.853V, neg = -0.808V # Test item 7-16- 3-24

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.006 V, 5.8% >> pos = 0.002V, neg = 0.007V # Test item 7-16- 3-25

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.830 V, 37.3% >> pos = 0.421V, neg = -0.408V # Test item 7-16- 3-26

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.000V, neg = 0.000V # Test item 7-16- 3-27

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.260 V, 41.3% >> pos = 0.130V, neg = -0.130V # Test item 7-16- 3-28

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = -0.003V, neg = -0.003V # Test item 7-16- 3-29

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 25.0% >> pos = 0.049V, neg = -0.056V # Test item 7-16- 3-30

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.6% >> pos = 0.065V, neg = 0.069V # Test item 7-16- 3-31

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.341 V, 43.9% >> pos = 1.733V, neg = -1.607V # Test item 7-16- 3-32

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.5% >> pos = 0.033V, neg = 0.034V # Test item 7-16- 3-33

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.661 V, 38.1% >> pos = 0.865V, neg = -0.795V # Test item 7-16- 3-34

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.011 V, 11.4% >> pos = 0.011V, neg = 0.023V # Test item 7-16- 3-35

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.834 V, 42.6% >> pos = 0.439V, neg = -0.395V # Test item 7-16- 3-36

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.1% >> pos = 0.011V, neg = 0.013V # Test item 7-16- 3-37

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.268 V, 71.5% >> pos = 0.144V, neg = -0.124V # Test item 7-16- 3-38

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.3% >> pos = 0.012V, neg = 0.010V # Test item 7-16- 3-39

T AMCA: MESE 2267: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.103 V, 13.4% >> pos = 0.061V, neg = -0.041V # Test item 7-16- 3-40

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.017 V, 17.2% >> pos = 0.054V, neg = 0.071V # Test item 8-16- 3- 1

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.398 V, 61.7% >> pos = 1.771V, neg = -1.626V # Test item 8-16- 3- 2

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% >> pos = 0.050V, neg = 0.048V # Test item 8-16- 3- 3

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.680 V, 50.1% >> pos = 0.892V, neg = -0.788V # Test item 8-16- 3- 4

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = 0.039V, neg = 0.038V # Test item 8-16- 3- 5

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.843 V, 53.4% >> pos = 0.461V, neg = -0.382V # Test item 8-16- 3- 6

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.3% >> pos = 0.035V, neg = 0.032V # Test item 8-16- 3- 7

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.264 V, 56.3% >> pos = 0.165V, neg = -0.099V # Test item 8-16- 3- 8

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.1% >> pos = 0.029V, neg = 0.031V # Test item 8-16- 3- 9

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.106 V, 31.6% >> pos = 0.083V, neg = -0.023V # Test item 8-16- 3-10

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.8% >> pos = 0.015V, neg = 0.010V # Test item 8-16- 3-11

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.398 V, 62.0% >> pos = 1.713V, neg = -1.685V # Test item 8-16- 3-12

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.9% >> pos = 0.009V, neg = 0.006V # Test item 8-16- 3-13

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.685 V, 53.2% >> pos = 0.836V, neg = -0.850V # Test item 8-16- 3-14

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.0% >> pos = -0.016V, neg = -0.013V # Test item 8-16- 3-15

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.844 V, 55.0% >> pos = 0.408V, neg = -0.436V # Test item 8-16- 3-16

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = -0.022V, neg = -0.023V # Test item 8-16- 3-17

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.263 V, 53.2% >> pos = 0.109V, neg = -0.154V # Test item 8-16- 3-18

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.2% >> pos = -0.021V, neg = -0.024V # Test item 8-16- 3-19

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.106 V, 28.4% >> pos = 0.029V, neg = -0.077V # Test item 8-16- 3-20

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.4% >> pos = 0.068V, neg = 0.072V # Test item 8-16- 3-21

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.390 V, 59.5% >> pos = 1.767V, neg = -1.624V # Test item 8-16- 3-22

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = 0.050V, neg = 0.049V # Test item 8-16- 3-23

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.684 V, 52.7% >> pos = 0.890V, neg = -0.794V # Test item 8-16- 3-24

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.9% >> pos = 0.038V, neg = 0.032V # Test item 8-16- 3-25

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.838 V, 47.0% >> pos = 0.453V, neg = -0.384V # Test item 8-16- 3-26

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = 0.032V, neg = 0.032V # Test item 8-16- 3-27

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.263 V, 52.7% >> pos = 0.164V, neg = -0.099V # Test item 8-16- 3-28

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.5% >> pos = 0.031V, neg = 0.030V # Test item 8-16- 3-29

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 26.8% >> pos = 0.083V, neg = -0.023V # Test item 8-16- 3-30

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.6% >> pos = 0.011V, neg = 0.012V # Test item 8-16- 3-31

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.396 V, 61.2% >> pos = 1.711V, neg = -1.685V # Test item 8-16- 3-32

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.4% >> pos = -0.008V, neg = -0.007V # Test item 8-16- 3-33

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.684 V, 52.5% >> pos = 0.836V, neg = -0.848V # Test item 8-16- 3-34

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = -0.017V, neg = -0.016V # Test item 8-16- 3-35

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.843 V, 53.7% >> pos = 0.403V, neg = -0.440V # Test item 8-16- 3-36

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.5% >> pos = -0.023V, neg = -0.022V # Test item 8-16- 3-37

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.263 V, 52.3% >> pos = 0.109V, neg = -0.154V # Test item 8-16- 3-38

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.6% >> pos = -0.023V, neg = -0.026V # Test item 8-16- 3-39

T AMCA: MESE 2268: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.107 V, 35.1% >> pos = 0.030V, neg = -0.077V # Test item 8-16- 3-40

T AMCA: MESE 2261: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.816 V, 16.5% >> POS = 0.861V, NEG = 0.044V # Test item 1-16- 4- 1

T AMCA: MESE 2261: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.801 V, 1.4% >> POS = 0.846V, NEG = 0.044V # Test item 1-16- 4- 2

T AMCA: MESE 2261: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.781 V, 18.5% >> POS = 0.815V, NEG = 0.034V # Test item 1-16- 4- 3

T AMCA: MESE 2261: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.830 V, 30.5% >> POS = 0.880V, NEG = 0.050V # Test item 1-16- 4- 4

T AMCA: MESE 2262: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.821 V, 20.8% >> POS = 0.873V, NEG = 0.052V # Test item 2-16- 4- 1

T AMCA: MESE 2262: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.801 V, 0.9% >> POS = 0.851V, NEG = 0.050V # Test item 2-16- 4- 2

T AMCA: MESE 2262: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.783 V, 17.4% >> POS = 0.824V, NEG = 0.041V # Test item 2-16- 4- 3

T AMCA: MESE 2262: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.833 V, 32.6% >> POS = 0.890V, NEG = 0.058V # Test item 2-16- 4- 4

T AMCA: MESE 2263: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.817 V, 17.2% >> POS = 0.876V, NEG = 0.059V # Test item 3-16- 4- 1

T AMCA: MESE 2263: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.799 V, 0.9% >> POS = 0.859V, NEG = 0.060V # Test item 3-16- 4- 2

T AMCA: MESE 2263: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.777 V, 22.7% >> POS = 0.827V, NEG = 0.049V # Test item 3-16- 4- 3

T AMCA: MESE 2263: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.830 V, 30.4% >> POS = 0.896V, NEG = 0.065V # Test item 3-16- 4- 4

T AMCA: MESE 2264: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.814 V, 13.7% >> POS = 0.856V, NEG = 0.043V # Test item 4-16- 4- 1

T AMCA: MESE 2264: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.793 V, 7.1% >> POS = 0.837V, NEG = 0.044V # Test item 4-16- 4- 2

T AMCA: MESE 2264: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.772 V, 28.3% >> POS = 0.807V, NEG = 0.035V # Test item 4-16- 4- 3

T AMCA: MESE 2264: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.830 V, 29.5% >> POS = 0.876V, NEG = 0.047V # Test item 4-16- 4- 4

T AMCA: MESE 2265: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.820 V, 19.8% >> POS = 0.860V, NEG = 0.040V # Test item 5-16- 4- 1

T AMCA: MESE 2265: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.801 V, 0.8% >> POS = 0.840V, NEG = 0.039V # Test item 5-16- 4- 2

T AMCA: MESE 2265: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.782 V, 17.5% >> POS = 0.811V, NEG = 0.029V # Test item 5-16- 4- 3

T AMCA: MESE 2265: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.835 V, 35.3% >> POS = 0.877V, NEG = 0.042V # Test item 5-16- 4- 4

T AMCA: MESE 2266: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.817 V, 17.2% >> POS = 0.872V, NEG = 0.055V # Test item 6-16- 4- 1

T AMCA: MESE 2266: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.801 V, 1.4% >> POS = 0.855V, NEG = 0.054V # Test item 6-16- 4- 2

T AMCA: MESE 2266: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.780 V, 20.5% >> POS = 0.822V, NEG = 0.043V # Test item 6-16- 4- 3

T AMCA: MESE 2266: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.835 V, 34.8% >> POS = 0.892V, NEG = 0.057V # Test item 6-16- 4- 4

T AMCA: MESE 2267: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.812 V, 11.6% >> POS = 0.832V, NEG = 0.020V # Test item 7-16- 4- 1

T AMCA: MESE 2267: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.797 V, 3.2% >> POS = 0.815V, NEG = 0.019V # Test item 7-16- 4- 2

T AMCA: MESE 2267: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.775 V, 24.8% >> POS = 0.784V, NEG = 0.009V # Test item 7-16- 4- 3

T AMCA: MESE 2267: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.830 V, 30.1% >> POS = 0.853V, NEG = 0.023V # Test item 7-16- 4- 4

T AMCA: MESE 2268: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.829 V, 28.6% >> POS = 0.874V, NEG = 0.045V # Test item 8-16- 4- 1

T AMCA: MESE 2268: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.812 V, 11.8% >> POS = 0.856V, NEG = 0.044V # Test item 8-16- 4- 2

T AMCA: MESE 2268: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.791 V, 9.5% >> POS = 0.825V, NEG = 0.035V # Test item 8-16- 4- 3

T AMCA: MESE 2268: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.845 V, 44.7% >> POS = 0.893V, NEG = 0.048V # Test item 8-16- 4- 4

T AMCA: MESE 2261: Signal\_Input/Output\_relay\_test\_Resistance\_A+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5022.560 Ohm, 0.3% >> vOffset = -0.110V, vMeas = 2.401V, iForce = 0.500mA # Test item 1-20- 1- 1

T AMCA: MESE 2261: Signal\_Input/Output\_relay\_test\_Resistance\_A-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5023.567 Ohm, 0.1% >> vOffset = -0.111V, vMeas = 2.401V, iForce = 0.500mA # Test item 1-20- 1- 2

T AMCA: MESE 2261: Signal\_Input/Output\_relay\_test\_Resistance\_AA+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1005.318 Ohm, 5.3% >> vOffset = -0.017V, vMeas = 0.485V, iForce = 0.500mA # Test item 1-20- 1- 3

T AMCA: MESE 2261: Signal\_Input/Output\_relay\_test\_Resistance\_AA-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1006.073 Ohm, 6.1% >> vOffset = -0.017V, vMeas = 0.486V, iForce = 0.500mA # Test item 1-20- 1- 4

T AMCA: MESE 2262: Signal\_Input/Output\_relay\_test\_Resistance\_B+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5027.092 Ohm, 0.6% >> vOffset = -0.110V, vMeas = 2.403V, iForce = 0.500mA # Test item 2-20- 1- 1

T AMCA: MESE 2262: Signal\_Input/Output\_relay\_test\_Resistance\_B-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5029.358 Ohm, 1.1% >> vOffset = -0.111V, vMeas = 2.404V, iForce = 0.500mA # Test item 2-20- 1- 2

T AMCA: MESE 2262: Signal\_Input/Output\_relay\_test\_Resistance\_BB+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1005.821 Ohm, 5.8% >> vOffset = -0.019V, vMeas = 0.484V, iForce = 0.500mA # Test item 2-20- 1- 3

T AMCA: MESE 2262: Signal\_Input/Output\_relay\_test\_Resistance\_BB-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1005.318 Ohm, 5.3% >> vOffset = -0.019V, vMeas = 0.484V, iForce = 0.500mA # Test item 2-20- 1- 4

T AMCA: MESE 2263: Signal\_Input/Output\_relay\_test\_Resistance\_C+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5011.482 Ohm, 2.5% >> vOffset = -0.121V, vMeas = 2.384V, iForce = 0.500mA # Test item 3-20- 1- 1

T AMCA: MESE 2263: Signal\_Input/Output\_relay\_test\_Resistance\_C-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5011.734 Ohm, 2.4% >> vOffset = -0.122V, vMeas = 2.384V, iForce = 0.500mA # Test item 3-20- 1- 2

T AMCA: MESE 2263: Signal\_Input/Output\_relay\_test\_Resistance\_CC+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1004.562 Ohm, 4.6% >> vOffset = -0.022V, vMeas = 0.481V, iForce = 0.500mA # Test item 3-20- 1- 3

T AMCA: MESE 2263: Signal\_Input/Output\_relay\_test\_Resistance\_CC-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1002.045 Ohm, 2.0% >> vOffset = -0.021V, vMeas = 0.480V, iForce = 0.500mA # Test item 3-20- 1- 4

T AMCA: MESE 2264: Signal\_Input/Output\_relay\_test\_Resistance\_D+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5023.315 Ohm, 0.1% >> vOffset = -0.112V, vMeas = 2.400V, iForce = 0.500mA # Test item 4-20- 1- 1

T AMCA: MESE 2264: Signal\_Input/Output\_relay\_test\_Resistance\_D-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5023.567 Ohm, 0.1% >> vOffset = -0.112V, vMeas = 2.400V, iForce = 0.500mA # Test item 4-20- 1- 2

T AMCA: MESE 2264: Signal\_Input/Output\_relay\_test\_Resistance\_DD+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1002.296 Ohm, 2.3% >> vOffset = -0.016V, vMeas = 0.485V, iForce = 0.500mA # Test item 4-20- 1- 3

T AMCA: MESE 2264: Signal\_Input/Output\_relay\_test\_Resistance\_DD-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1005.066 Ohm, 5.1% >> vOffset = -0.016V, vMeas = 0.487V, iForce = 0.500mA # Test item 4-20- 1- 4

T AMCA: MESE 2265: Signal\_Input/Output\_relay\_test\_Resistance\_E+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5008.461 Ohm, 3.1% >> vOffset = -0.115V, vMeas = 2.389V, iForce = 0.500mA # Test item 5-20- 1- 1

T AMCA: MESE 2265: Signal\_Input/Output\_relay\_test\_Resistance\_E-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5011.734 Ohm, 2.4% >> vOffset = -0.116V, vMeas = 2.390V, iForce = 0.500mA # Test item 5-20- 1- 2

T AMCA: MESE 2265: Signal\_Input/Output\_relay\_test\_Resistance\_EE+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5011.482 Ohm, 2.5% >> vOffset = -0.116V, vMeas = 2.389V, iForce = 0.500mA # Test item 5-20- 1- 3

T AMCA: MESE 2265: Signal\_Input/Output\_relay\_test\_Resistance\_EE-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5007.454 Ohm, 3.3% >> vOffset = -0.116V, vMeas = 2.388V, iForce = 0.500mA # Test item 5-20- 1- 4

T AMCA: MESE 2266: Signal\_Input/Output\_relay\_test\_Resistance\_F+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5024.574 Ohm, 0.1% >> vOffset = -0.119V, vMeas = 2.394V, iForce = 0.500mA # Test item 6-20- 1- 1

T AMCA: MESE 2266: Signal\_Input/Output\_relay\_test\_Resistance\_F-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5020.798 Ohm, 0.6% >> vOffset = -0.117V, vMeas = 2.393V, iForce = 0.500mA # Test item 6-20- 1- 2

T AMCA: MESE 2266: Signal\_Input/Output\_relay\_test\_Resistance\_FF+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5022.309 Ohm, 0.3% >> vOffset = -0.118V, vMeas = 2.393V, iForce = 0.500mA # Test item 6-20- 1- 3

T AMCA: MESE 2266: Signal\_Input/Output\_relay\_test\_Resistance\_FF-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5023.063 Ohm, 0.2% >> vOffset = -0.118V, vMeas = 2.393V, iForce = 0.500mA # Test item 6-20- 1- 4

T AMCA: MESE 2267: Signal\_Input/Output\_relay\_test\_Resistance\_G+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5017.021 Ohm, 1.4% >> vOffset = 0.097V, vMeas = 2.605V, iForce = 0.500mA # Test item 7-20- 1- 1

T AMCA: MESE 2267: Signal\_Input/Output\_relay\_test\_Resistance\_G-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5016.518 Ohm, 1.5% >> vOffset = 0.097V, vMeas = 2.605V, iForce = 0.500mA # Test item 7-20- 1- 2

T AMCA: MESE 2267: Signal\_Input/Output\_relay\_test\_Resistance\_GG+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5017.273 Ohm, 1.3% >> vOffset = 0.097V, vMeas = 2.606V, iForce = 0.500mA # Test item 7-20- 1- 3

T AMCA: MESE 2267: Signal\_Input/Output\_relay\_test\_Resistance\_GG-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5016.770 Ohm, 1.4% >> vOffset = 0.097V, vMeas = 2.605V, iForce = 0.500mA # Test item 7-20- 1- 4

T AMCA: MESE 2268: Signal\_Input/Output\_relay\_test\_Resistance\_H+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5027.344 Ohm, 0.7% >> vOffset = -0.111V, vMeas = 2.403V, iForce = 0.500mA # Test item 8-20- 1- 1

T AMCA: MESE 2268: Signal\_Input/Output\_relay\_test\_Resistance\_H-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5027.847 Ohm, 0.8% >> vOffset = -0.111V, vMeas = 2.403V, iForce = 0.500mA # Test item 8-20- 1- 2

T AMCA: MESE 2268: Signal\_Input/Output\_relay\_test\_Resistance\_HH+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5026.588 Ohm, 0.5% >> vOffset = -0.111V, vMeas = 2.402V, iForce = 0.500mA # Test item 8-20- 1- 3

T AMCA: MESE 2268: Signal\_Input/Output\_relay\_test\_Resistance\_HH-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5026.337 Ohm, 0.5% >> vOffset = -0.110V, vMeas = 2.403V, iForce = 0.500mA # Test item 8-20- 1- 4

T AMCA: MCE 2260: Common\_trigger\_input\_relay\_test\_Common\_Trigger\_1: R\_exp = 470.000 Ohm, [369.000 ... 571.000 Ohm] : R\_meas = 469.719 Ohm, 0.3% >> vMeas = 1.404V, vOffset = -0.005V, usedUnit = 1 # Test item 0-21- 1- 1

T AMCA: MCE 2260: Common\_trigger\_input\_relay\_test\_Common\_Trigger\_2: R\_exp = 470.000 Ohm, [369.000 ... 571.000 Ohm] : R\_meas = 469.719 Ohm, 0.3% >> vMeas = 1.404V, vOffset = -0.006V, usedUnit = 1 # Test item 0-21- 1- 2

T AMCA: MCE 2270: Unit\_FPGA\_configuration\_test\_Unit\_1: exp = passed: meas = passed # Test item 1- 1- 1- 1

T AMCA: MCE 2270: Unit\_FPGA\_configuration\_test\_Unit\_2: exp = passed: meas = passed # Test item 2- 1- 1- 1

T AMCA: MCE 2270: Unit\_FPGA\_configuration\_test\_Unit\_3: exp = passed: meas = passed # Test item 3- 1- 1- 1

T AMCA: MCE 2270: Unit\_FPGA\_configuration\_test\_Unit\_4: exp = passed: meas = passed # Test item 4- 1- 1- 1

T AMCA: MCE 2270: Unit\_FPGA\_configuration\_test\_Unit\_5: exp = passed: meas = passed # Test item 5- 1- 1- 1

T AMCA: MCE 2270: Unit\_FPGA\_configuration\_test\_Unit\_6: exp = passed: meas = passed # Test item 6- 1- 1- 1

T AMCA: MCE 2270: Unit\_FPGA\_configuration\_test\_Unit\_7: exp = passed: meas = passed # Test item 7- 1- 1- 1

T AMCA: MCE 2270: Unit\_FPGA\_configuration\_test\_Unit\_8: exp = passed: meas = passed # Test item 8- 1- 1- 1

T AMCA: MCE 2270: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Data\_Bus\_Test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 0- 1- 2- 1

T AMCA: MCE 2270: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Data\_Bus\_Test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 1- 2- 2

T AMCA: MCE 2270: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 1- 2- 3

T AMCA: MESE 2271: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 1- 8- 1

T AMCA: MESE 2272: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 1- 8- 1

T AMCA: MESE 2273: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 1- 8- 1

T AMCA: MESE 2274: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 1- 8- 1

T AMCA: MESE 2275: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 1- 8- 1

T AMCA: MESE 2276: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 1- 8- 1

T AMCA: MESE 2277: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 1- 8- 1

T AMCA: MESE 2278: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 1- 8- 1

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_DGND\_B\_(U10144): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.017 V, 3.4% # Test item 0- 1- 3- 1

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_(U10145): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.020 V, 4.0% # Test item 1- 1- 3- 2

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_IF\_(U10145): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.035 V, 6.9% # Test item 2- 1- 3- 3

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_(U811): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.006 V, 1.2% # Test item 3- 1- 3- 4

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.977 V, 3.1% # Test item 0- 1- 3- 5

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 0.996 V, 13.9% # Test item 1- 1- 3- 6

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_B: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.987 V, 7.1% # Test item 2- 1- 3- 7

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_B: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.018 V, 7.9% # Test item 3- 1- 3- 8

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5VD\_B: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.977 V, 17.7% # Test item 4- 1- 3- 9

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_B: V\_exp = 1.020 V, [0.918 ... 1.122 V] : V\_meas = 1.034 V, 13.7% # Test item 5- 1- 3-10

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_TEMP\_BUSIF: V\_exp = 0.750 V, [0.500 ... 1.000 V] : V\_meas = 0.632 V, 47.2% # Test item 6- 1- 3-11

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5.5VA: V\_exp = 1.050 V, [0.945 ... 1.155 V] : V\_meas = 1.054 V, 4.0% # Test item 7- 1- 3-12

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5.5VA: V\_exp = -1.140 V, [-1.254 ... -1.026 V] : V\_meas = -1.119 V, 18.6% # Test item 8- 1- 3-13

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+10VA: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 1.000 V, 0.2% # Test item 9- 1- 3-14

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.2VA\_IF: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.995 V, 4.7% # Test item 10- 1- 3-15

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U13: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.977 V, 13.1% # Test item 11- 1- 3-16

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U24: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.971 V, 19.2% # Test item 12- 1- 3-17

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U57: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.980 V, 10.1% # Test item 13- 1- 3-18

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U68: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.979 V, 11.1% # Test item 14- 1- 3-19

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U13: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.003 V, 13.1% # Test item 15- 1- 3-20

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U24: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.006 V, 16.2% # Test item 16- 1- 3-21

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U57: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.002 V, 12.1% # Test item 17- 1- 3-22

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U68: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.007 V, 17.2% # Test item 18- 1- 3-23

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_F\_TG: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 0.999 V, 39.4% # Test item 19- 1- 3-24

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.5VD\_F\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.977 V, 23.0% # Test item 20- 1- 3-25

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_F: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.015 V, 24.0% # Test item 21- 1- 3-26

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.5VD\_F: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.978 V, 22.0% # Test item 22- 1- 3-27

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5VD\_F: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.951 V, 9.4% # Test item 23- 1- 3-28

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5VA\_F: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.961 V, 0.8% # Test item 24- 1- 3-29

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5VA\_F: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.958 V, 1.8% # Test item 25- 1- 3-30

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VA\_F: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.016 V, 23.3% # Test item 26- 1- 3-31

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.961 V, 19.4% # Test item 27- 1- 3-32

T AMCA: MCE 2270: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_IF\_IF2: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.968 V, 32.0% # Test item 28- 1- 3-33

T AMCA: MCE 2270: Unit\_bus\_data\_line\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 0- 1- 4- 1

T AMCA: MCE 2270: Unit\_bus\_data\_line\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 1- 4- 2

T AMCA: MCE 2270: Unit\_bus\_address/control\_line\_test\_WE\_line\_and\_bit\_0\_of\_addr.\_bus: Reg\_exp = 0x00030002: Reg\_meas = 0x00030002 # Test item 0- 1- 5- 1

T AMCA: MCE 2270: Unit\_bus\_address/control\_line\_test\_OE\_line\_\_unit\_select\_line\_for\_unit\_1--4\_and\_bit\_1\_of\_addr.\_bus: Reg\_exp = 0x0002F004: Reg\_meas = 0x0002F004 # Test item 1- 1- 5- 2

T AMCA: MCE 2270: Unit\_bus\_address/control\_line\_test\_Unit\_select\_line\_for\_unit\_5--8\_and\_bit\_2\_of\_addr.\_bus: Reg\_exp = 0x00020F08: Reg\_meas = 0x00020F08 # Test item 2- 1- 5- 3

T AMCA: MCE 2270: Unit\_bus\_address/control\_line\_test\_Reset: Reg\_exp = 0x00060000: Reg\_meas = 0x00060000 # Test item 3- 1- 5- 4

T AMCA: MCE 2270: Analog\_interface\_module\_EEPROM\_access\_test\_Analog\_Interface\_Module: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 0- 1- 6- 1

T AMCA: MCE 2270: Front\_module\_serial\_bus\_test\_1k\_Ohm\_Rref\_connect\_relay\_on\_test: Reg\_exp = 0x00000003: Reg\_meas = 0x00000003 # Test item 0- 1- 7- 1

T AMCA: MCE 2270: Temperature\_sensor\_test\_START: V\_exp = 0.912 V, [0.450 ... 1.375 V] : V\_meas = 0.792 V, 26.1% >> degree = 33.680degree # Test item 0- 2- 3- 1

T AMCA: MCE 2270: MCLK\_test\_START: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.019 V, 19.0% >> D\_MCLK\_DC = 0.934V, D\_MCLK\_DC\* = 0.953V # Test item 0- 2- 4- 1

T AMCA: MCE 2270: MCLK\_test\_STOP: V\_exp = 0.300 V, [0.200 ... 19.980 V] : V\_meas = 0.337 V, 0.2% >> D\_MCLK\_DC = 0.754V, D\_MCLK\_DC\* = 1.091V # Test item 0- 2- 4- 2

T AMCA: MCE 2270: DC\_rail\_path\_test: R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1000.833 Ohm, 0.8% # Test item 0- 2- 8- 1

T AMCA: MCE 2270: DC\_rail\_path\_test\_GND\_connection\_switch\_On: R\_exp = 5.000 Ohm, [0.000 ... 10.000 Ohm] : R\_meas = 0.833 Ohm, 83.3% # Test item 0- 2- 8- 2

T AMCA: MESE 2271: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 3- 1- 1

T AMCA: MESE 2272: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 3- 1- 1

T AMCA: MESE 2273: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 3- 1- 1

T AMCA: MESE 2274: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 3- 1- 1

T AMCA: MESE 2275: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 3- 1- 1

T AMCA: MESE 2276: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 3- 1- 1

T AMCA: MESE 2277: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 3- 1- 1

T AMCA: MESE 2278: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 3- 1- 1

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.988 V, 7.7% # Test item 1- 3- 2- 1

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.988 V, 7.7% # Test item 1- 3- 2- 2

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.038 V, 27.2% # Test item 1- 3- 2- 3

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.015 V, 5.0% # Test item 1- 3- 2- 4

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.974 V, 6.1% # Test item 1- 3- 2- 5

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.980 V, 20.0% # Test item 1- 3- 2- 6

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.974 V, 14.8% # Test item 1- 3- 2- 7

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.962 V, 1.8% # Test item 1- 3- 2- 8

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.019 V, 39.3% # Test item 2- 3- 2- 1

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.023 V, 43.4% # Test item 2- 3- 2- 2

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.054 V, 43.1% # Test item 2- 3- 2- 3

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.007 V, 3.0% # Test item 2- 3- 2- 4

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.977 V, 3.1% # Test item 2- 3- 2- 5

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.975 V, 25.0% # Test item 2- 3- 2- 6

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.973 V, 13.8% # Test item 2- 3- 2- 7

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.952 V, 8.6% # Test item 2- 3- 2- 8

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.028 V, 48.5% # Test item 3- 3- 2- 1

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.027 V, 47.4% # Test item 3- 3- 2- 2

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.064 V, 53.0% # Test item 3- 3- 2- 3

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.005 V, 5.0% # Test item 3- 3- 2- 4

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.976 V, 4.1% # Test item 3- 3- 2- 5

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.968 V, 32.0% # Test item 3- 3- 2- 6

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.969 V, 9.6% # Test item 3- 3- 2- 7

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.952 V, 8.6% # Test item 3- 3- 2- 8

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.998 V, 17.9% # Test item 4- 3- 2- 1

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.993 V, 12.8% # Test item 4- 3- 2- 2

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.039 V, 29.2% # Test item 4- 3- 2- 3

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.013 V, 3.0% # Test item 4- 3- 2- 4

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.977 V, 3.1% # Test item 4- 3- 2- 5

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.975 V, 25.0% # Test item 4- 3- 2- 6

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.967 V, 7.6% # Test item 4- 3- 2- 7

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.962 V, 1.8% # Test item 4- 3- 2- 8

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.035 V, 55.6% # Test item 5- 3- 2- 1

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.032 V, 52.6% # Test item 5- 3- 2- 2

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.071 V, 59.9% # Test item 5- 3- 2- 3

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.009 V, 1.0% # Test item 5- 3- 2- 4

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.977 V, 3.1% # Test item 5- 3- 2- 5

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.977 V, 23.0% # Test item 5- 3- 2- 6

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.965 V, 5.5% # Test item 5- 3- 2- 7

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.955 V, 5.5% # Test item 5- 3- 2- 8

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.022 V, 42.3% # Test item 6- 3- 2- 1

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.026 V, 46.4% # Test item 6- 3- 2- 2

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.066 V, 55.0% # Test item 6- 3- 2- 3

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.017 V, 6.9% # Test item 6- 3- 2- 4

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.974 V, 6.1% # Test item 6- 3- 2- 5

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.977 V, 23.0% # Test item 6- 3- 2- 6

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.977 V, 18.0% # Test item 6- 3- 2- 7

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.986 V, 26.8% # Test item 6- 3- 2- 8

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.028 V, 48.5% # Test item 7- 3- 2- 1

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.030 V, 50.5% # Test item 7- 3- 2- 2

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.065 V, 54.0% # Test item 7- 3- 2- 3

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.014 V, 4.0% # Test item 7- 3- 2- 4

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.971 V, 9.2% # Test item 7- 3- 2- 5

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.980 V, 20.0% # Test item 7- 3- 2- 6

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.972 V, 12.8% # Test item 7- 3- 2- 7

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.949 V, 11.7% # Test item 7- 3- 2- 8

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.019 V, 39.3% # Test item 8- 3- 2- 1

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.032 V, 52.6% # Test item 8- 3- 2- 2

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.064 V, 53.0% # Test item 8- 3- 2- 3

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.010 V, 0.0% # Test item 8- 3- 2- 4

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.972 V, 8.2% # Test item 8- 3- 2- 5

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.982 V, 18.0% # Test item 8- 3- 2- 6

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.976 V, 16.9% # Test item 8- 3- 2- 7

T AMCA: MCE 2270: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.947 V, 13.8% # Test item 8- 3- 2- 8

T AMCA: MESE 2271: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 3- 3- 1

T AMCA: MESE 2272: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 3- 3- 1

T AMCA: MESE 2273: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 3- 3- 1

T AMCA: MESE 2274: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 3- 3- 1

T AMCA: MESE 2275: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 3- 3- 1

T AMCA: MESE 2276: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 3- 3- 1

T AMCA: MESE 2277: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 3- 3- 1

T AMCA: MESE 2278: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 3- 3- 1

T AMCA: MESE 2271: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 1- 3- 4- 1

T AMCA: MESE 2271: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 3- 4- 2

T AMCA: MESE 2272: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 2- 3- 4- 1

T AMCA: MESE 2272: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 2- 3- 4- 2

T AMCA: MESE 2273: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 3- 3- 4- 1

T AMCA: MESE 2273: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 3- 3- 4- 2

T AMCA: MESE 2274: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 4- 3- 4- 1

T AMCA: MESE 2274: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 4- 3- 4- 2

T AMCA: MESE 2275: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 5- 3- 4- 1

T AMCA: MESE 2275: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 5- 3- 4- 2

T AMCA: MESE 2276: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 6- 3- 4- 1

T AMCA: MESE 2276: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 6- 3- 4- 2

T AMCA: MESE 2277: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 7- 3- 4- 1

T AMCA: MESE 2277: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 7- 3- 4- 2

T AMCA: MESE 2278: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 8- 3- 4- 1

T AMCA: MESE 2278: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 8- 3- 4- 2

T AMCA: MESE 2271: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 3- 5- 1

T AMCA: MESE 2272: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 3- 5- 1

T AMCA: MESE 2273: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 3- 5- 1

T AMCA: MESE 2274: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 3- 5- 1

T AMCA: MESE 2275: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 3- 5- 1

T AMCA: MESE 2276: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 3- 5- 1

T AMCA: MESE 2277: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 3- 5- 1

T AMCA: MESE 2278: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 3- 5- 1

T AMCA: MESE 2271: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 3- 5- 2

T AMCA: MESE 2272: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 3- 5- 2

T AMCA: MESE 2273: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 3- 5- 2

T AMCA: MESE 2274: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 3- 5- 2

T AMCA: MESE 2275: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 3- 5- 2

T AMCA: MESE 2276: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 3- 5- 2

T AMCA: MESE 2277: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 3- 5- 2

T AMCA: MESE 2278: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 3- 5- 2

T AMCA: MESE 2271: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 3- 5- 3

T AMCA: MESE 2272: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 3- 5- 3

T AMCA: MESE 2273: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 3- 5- 3

T AMCA: MESE 2274: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 3- 5- 3

T AMCA: MESE 2275: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 3- 5- 3

T AMCA: MESE 2276: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 3- 5- 3

T AMCA: MESE 2277: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 3- 5- 3

T AMCA: MESE 2278: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 3- 5- 3

T AMCA: MESE 2271: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 3- 5- 4

T AMCA: MESE 2272: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 3- 5- 4

T AMCA: MESE 2273: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 3- 5- 4

T AMCA: MESE 2274: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 3- 5- 4

T AMCA: MESE 2275: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 3- 5- 4

T AMCA: MESE 2276: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 3- 5- 4

T AMCA: MESE 2277: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 3- 5- 4

T AMCA: MESE 2278: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 3- 5- 4

T AMCA: MESE 2271: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 1- 3- 6- 1

T AMCA: MESE 2272: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 2- 3- 6- 1

T AMCA: MESE 2273: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 3- 3- 6- 1

T AMCA: MESE 2274: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 4- 3- 6- 1

T AMCA: MESE 2275: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 5- 3- 6- 1

T AMCA: MESE 2276: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 6- 3- 6- 1

T AMCA: MESE 2277: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 7- 3- 6- 1

T AMCA: MESE 2278: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 8- 3- 6- 1

T AMCA: MESE 2271: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 1- 3- 6- 2

T AMCA: MESE 2272: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 2- 3- 6- 2

T AMCA: MESE 2273: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 3- 3- 6- 2

T AMCA: MESE 2274: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 4- 3- 6- 2

T AMCA: MESE 2275: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 5- 3- 6- 2

T AMCA: MESE 2276: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 6- 3- 6- 2

T AMCA: MESE 2277: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 7- 3- 6- 2

T AMCA: MESE 2278: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 8- 3- 6- 2

T AMCA: MESE 2271: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 3- 7- 1

T AMCA: MESE 2272: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 3- 7- 1

T AMCA: MESE 2273: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 3- 7- 1

T AMCA: MESE 2274: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 3- 7- 1

T AMCA: MESE 2275: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 3- 7- 1

T AMCA: MESE 2276: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 3- 7- 1

T AMCA: MESE 2277: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 3- 7- 1

T AMCA: MESE 2278: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 3- 7- 1

T AMCA: MESE 2271: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 1-22- 1- 1

T AMCA: MESE 2271: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 1-22- 1- 2

T AMCA: MESE 2272: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 2-22- 1- 1

T AMCA: MESE 2272: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 2-22- 1- 2

T AMCA: MESE 2273: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 3-22- 1- 1

T AMCA: MESE 2273: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 3-22- 1- 2

T AMCA: MESE 2274: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 4-22- 1- 1

T AMCA: MESE 2274: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 4-22- 1- 2

T AMCA: MESE 2275: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 5-22- 1- 1

T AMCA: MESE 2275: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 5-22- 1- 2

T AMCA: MESE 2276: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 6-22- 1- 1

T AMCA: MESE 2276: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 6-22- 1- 2

T AMCA: MESE 2277: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 7-22- 1- 1

T AMCA: MESE 2277: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 7-22- 1- 2

T AMCA: MESE 2278: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 8-22- 1- 1

T AMCA: MESE 2278: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 8-22- 1- 2

T AMCA: MESE 2271: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.891 V, 7.4% # Test item 1- 4- 1- 1

T AMCA: MESE 2271: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.848 V, 7.6% # Test item 1- 4- 1- 2

T AMCA: MESE 2272: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.901 V, 10.9% # Test item 2- 4- 1- 1

T AMCA: MESE 2272: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.839 V, 10.8% # Test item 2- 4- 1- 2

T AMCA: MESE 2273: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.895 V, 8.8% # Test item 3- 4- 1- 1

T AMCA: MESE 2273: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.847 V, 8.0% # Test item 3- 4- 1- 2

T AMCA: MESE 2274: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.904 V, 11.9% # Test item 4- 4- 1- 1

T AMCA: MESE 2274: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.838 V, 11.1% # Test item 4- 4- 1- 2

T AMCA: MESE 2275: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.892 V, 7.6% # Test item 5- 4- 1- 1

T AMCA: MESE 2275: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.846 V, 8.3% # Test item 5- 4- 1- 2

T AMCA: MESE 2276: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.904 V, 11.8% # Test item 6- 4- 1- 1

T AMCA: MESE 2276: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.840 V, 10.5% # Test item 6- 4- 1- 2

T AMCA: MESE 2277: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.891 V, 7.2% # Test item 7- 4- 1- 1

T AMCA: MESE 2277: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.848 V, 7.6% # Test item 7- 4- 1- 2

T AMCA: MESE 2278: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.905 V, 12.3% # Test item 8- 4- 1- 1

T AMCA: MESE 2278: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.835 V, 12.3% # Test item 8- 4- 1- 2

T AMCA: MESE 2271: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.8% # Test item 1- 4- 2- 1

T AMCA: MESE 2271: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.024 V, 23.8% # Test item 1- 4- 2- 2

T AMCA: MESE 2271: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.970 V, 10.1% # Test item 1- 4- 2- 3

T AMCA: MESE 2272: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.028 V, 9.2% # Test item 2- 4- 2- 1

T AMCA: MESE 2272: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.034 V, 34.5% # Test item 2- 4- 2- 2

T AMCA: MESE 2272: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.958 V, 13.9% # Test item 2- 4- 2- 3

T AMCA: MESE 2273: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.020 V, 6.5% # Test item 3- 4- 2- 1

T AMCA: MESE 2273: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.025 V, 24.7% # Test item 3- 4- 2- 2

T AMCA: MESE 2273: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.970 V, 10.0% # Test item 3- 4- 2- 3

T AMCA: MESE 2274: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.030 V, 10.0% # Test item 4- 4- 2- 1

T AMCA: MESE 2274: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.036 V, 36.1% # Test item 4- 4- 2- 2

T AMCA: MESE 2274: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.958 V, 14.1% # Test item 4- 4- 2- 3

T AMCA: MESE 2275: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.018 V, 5.9% # Test item 5- 4- 2- 1

T AMCA: MESE 2275: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.024 V, 24.2% # Test item 5- 4- 2- 2

T AMCA: MESE 2275: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.969 V, 10.4% # Test item 5- 4- 2- 3

T AMCA: MESE 2276: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.028 V, 9.4% # Test item 6- 4- 2- 1

T AMCA: MESE 2276: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.033 V, 33.2% # Test item 6- 4- 2- 2

T AMCA: MESE 2276: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.961 V, 12.9% # Test item 6- 4- 2- 3

T AMCA: MESE 2277: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.7% # Test item 7- 4- 2- 1

T AMCA: MESE 2277: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.021 V, 21.4% # Test item 7- 4- 2- 2

T AMCA: MESE 2277: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.974 V, 8.7% # Test item 7- 4- 2- 3

T AMCA: MESE 2271: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.885 mA, 38.3% # Test item 1- 4- 3- 1

T AMCA: MESE 2271: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.013 mA, 8.6% # Test item 1- 4- 3- 2

T AMCA: MESE 2271: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.837 mA, 36.1% # Test item 1- 4- 3- 3

T AMCA: MESE 2271: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.006 mA, 4.1% # Test item 1- 4- 3- 4

T AMCA: MESE 2272: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.895 mA, 35.0% # Test item 2- 4- 3- 1

T AMCA: MESE 2272: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.015 mA, 10.0% # Test item 2- 4- 3- 2

T AMCA: MESE 2272: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.824 mA, 39.1% # Test item 2- 4- 3- 3

T AMCA: MESE 2272: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.002 mA, 1.4% # Test item 2- 4- 3- 4

T AMCA: MESE 2273: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.884 mA, 38.7% # Test item 3- 4- 3- 1

T AMCA: MESE 2273: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.010 mA, 6.8% # Test item 3- 4- 3- 2

T AMCA: MESE 2273: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.837 mA, 36.3% # Test item 3- 4- 3- 3

T AMCA: MESE 2273: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.009 mA, 5.9% # Test item 3- 4- 3- 4

T AMCA: MESE 2274: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.895 mA, 35.0% # Test item 4- 4- 3- 1

T AMCA: MESE 2274: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.011 mA, 7.6% # Test item 4- 4- 3- 2

T AMCA: MESE 2274: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.827 mA, 38.3% # Test item 4- 4- 3- 3

T AMCA: MESE 2274: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.005 mA, 3.6% # Test item 4- 4- 3- 4

T AMCA: MESE 2275: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.880 mA, 40.0% # Test item 5- 4- 3- 1

T AMCA: MESE 2275: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.009 mA, 6.3% # Test item 5- 4- 3- 2

T AMCA: MESE 2275: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.838 mA, 36.1% # Test item 5- 4- 3- 3

T AMCA: MESE 2275: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.010 mA, 6.6% # Test item 5- 4- 3- 4

T AMCA: MESE 2276: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.891 mA, 36.4% # Test item 6- 4- 3- 1

T AMCA: MESE 2276: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.009 mA, 5.8% # Test item 6- 4- 3- 2

T AMCA: MESE 2276: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.831 mA, 37.6% # Test item 6- 4- 3- 3

T AMCA: MESE 2276: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.008 mA, 5.6% # Test item 6- 4- 3- 4

T AMCA: MESE 2277: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.882 mA, 39.4% # Test item 7- 4- 3- 1

T AMCA: MESE 2277: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.010 mA, 6.4% # Test item 7- 4- 3- 2

T AMCA: MESE 2277: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.840 mA, 35.5% # Test item 7- 4- 3- 3

T AMCA: MESE 2277: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.008 mA, 5.2% # Test item 7- 4- 3- 4

T AMCA: MESE 2278: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.899 mA, 33.8% # Test item 8- 4- 3- 1

T AMCA: MESE 2278: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.013 mA, 8.8% # Test item 8- 4- 3- 2

T AMCA: MESE 2278: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.821 mA, 39.9% # Test item 8- 4- 3- 3

T AMCA: MESE 2278: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.003 mA, 2.1% # Test item 8- 4- 3- 4

T AMCA: MESE 2271: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.982 V, 6.1% # Test item 1- 4- 4- 1

T AMCA: MESE 2271: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.275 V, 10.3% # Test item 1- 4- 4- 2

T AMCA: MESE 2271: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.012 V, 2.7% # Test item 1- 4- 4- 3

T AMCA: MESE 2271: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.275 V, 10.8% # Test item 1- 4- 4- 4

T AMCA: MESE 2272: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.977 V, 7.5% # Test item 2- 4- 4- 1

T AMCA: MESE 2272: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.276 V, 14.9% # Test item 2- 4- 4- 2

T AMCA: MESE 2272: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.020 V, 4.5% # Test item 2- 4- 4- 3

T AMCA: MESE 2272: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.272 V, 0.6% # Test item 2- 4- 4- 4

T AMCA: MESE 2273: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.982 V, 5.8% # Test item 3- 4- 4- 1

T AMCA: MESE 2273: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.273 V, 2.0% # Test item 3- 4- 4- 2

T AMCA: MESE 2273: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.018 V, 4.0% # Test item 3- 4- 4- 3

T AMCA: MESE 2273: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.274 V, 6.6% # Test item 3- 4- 4- 4

T AMCA: MESE 2274: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.973 V, 9.2% # Test item 4- 4- 4- 1

T AMCA: MESE 2274: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.273 V, 4.8% # Test item 4- 4- 4- 2

T AMCA: MESE 2274: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.023 V, 5.1% # Test item 4- 4- 4- 3

T AMCA: MESE 2274: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.275 V, 10.3% # Test item 4- 4- 4- 4

T AMCA: MESE 2275: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.983 V, 5.8% # Test item 5- 4- 4- 1

T AMCA: MESE 2275: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.275 V, 10.8% # Test item 5- 4- 4- 2

T AMCA: MESE 2275: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.014 V, 3.1% # Test item 5- 4- 4- 3

T AMCA: MESE 2275: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.273 V, 5.2% # Test item 5- 4- 4- 4

T AMCA: MESE 2276: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.976 V, 8.2% # Test item 6- 4- 4- 1

T AMCA: MESE 2276: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.273 V, 2.5% # Test item 6- 4- 4- 2

T AMCA: MESE 2276: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.025 V, 5.4% # Test item 6- 4- 4- 3

T AMCA: MESE 2276: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.275 V, 9.4% # Test item 6- 4- 4- 4

T AMCA: MESE 2277: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.986 V, 4.5% # Test item 7- 4- 4- 1

T AMCA: MESE 2277: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.274 V, 7.5% # Test item 7- 4- 4- 2

T AMCA: MESE 2277: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.017 V, 3.8% # Test item 7- 4- 4- 3

T AMCA: MESE 2277: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.273 V, 4.8% # Test item 7- 4- 4- 4

T AMCA: MESE 2278: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.975 V, 8.5% # Test item 8- 4- 4- 1

T AMCA: MESE 2278: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.275 V, 11.7% # Test item 8- 4- 4- 2

T AMCA: MESE 2278: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.026 V, 5.8% # Test item 8- 4- 4- 3

T AMCA: MESE 2278: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.274 V, 5.7% # Test item 8- 4- 4- 4

T AMCA: MESE 2271: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.993 kOhm, 7.0% # Test item 1- 4- 5- 1

T AMCA: MESE 2272: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.992 kOhm, 8.0% # Test item 2- 4- 5- 1

T AMCA: MESE 2273: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.993 kOhm, 6.7% # Test item 3- 4- 5- 1

T AMCA: MESE 2274: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.990 kOhm, 9.7% # Test item 4- 4- 5- 1

T AMCA: MESE 2275: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.994 kOhm, 6.3% # Test item 5- 4- 5- 1

T AMCA: MESE 2276: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.991 kOhm, 8.5% # Test item 6- 4- 5- 1

T AMCA: MESE 2277: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.995 kOhm, 4.8% # Test item 7- 4- 5- 1

T AMCA: MESE 2278: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.991 kOhm, 8.7% # Test item 8- 4- 5- 1

T AMCA: MCE 2270: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_1: R\_exp = 10220.000 Ohm, [9198.000 ... 11242.000 Ohm] : R\_meas = 9961.280 Ohm, 25.3% >> vMeas = 2.812V, vOffset = -0.176V, usedUnit = 1 # Test item 0- 5- 1- 1

T AMCA: MCE 2270: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_1\_(with\_GND): R\_exp = 59.000 Ohm, [49.000 ... 69.000 Ohm] : R\_meas = 58.201 Ohm, 8.0% >> vMeas = 0.178V, vOffset = 0.003V, usedUnit = 1 # Test item 0- 5- 1- 2

T AMCA: MCE 2270: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_2: R\_exp = 10220.000 Ohm, [9198.000 ... 11242.000 Ohm] : R\_meas = 9966.315 Ohm, 24.8% >> vMeas = 2.814V, vOffset = -0.176V, usedUnit = 1 # Test item 0- 5- 1- 3

T AMCA: MCE 2270: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_2\_(with\_GND): R\_exp = 59.000 Ohm, [49.000 ... 69.000 Ohm] : R\_meas = 58.369 Ohm, 6.3% >> vMeas = 0.178V, vOffset = 0.003V, usedUnit = 1 # Test item 0- 5- 1- 4

T AMCA: MCE 2270: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_\_0.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 1

T AMCA: MCE 2270: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_\_1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 2

T AMCA: MCE 2270: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_-1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 3

T AMCA: MCE 2270: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_\_0.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 4

T AMCA: MCE 2270: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_\_1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 5

T AMCA: MCE 2270: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_-1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 6

T AMCA: MESE 2271: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.497 V, 2.1% >> vOffset = -0.015V # Test item 1- 2- 9- 1

T AMCA: MESE 2271: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 20.1% >> vOffset = 0.002V # Test item 1- 2- 9- 2

T AMCA: MESE 2272: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.498 V, 1.3% >> vOffset = -0.020V # Test item 2- 2- 9- 1

T AMCA: MESE 2272: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.003 V, 25.2% >> vOffset = 0.003V # Test item 2- 2- 9- 2

T AMCA: MESE 2273: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 0.6% >> vOffset = -0.017V # Test item 3- 2- 9- 1

T AMCA: MESE 2273: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 23.9% >> vOffset = 0.001V # Test item 3- 2- 9- 2

T AMCA: MESE 2274: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.497 V, 1.8% >> vOffset = -0.025V # Test item 4- 2- 9- 1

T AMCA: MESE 2274: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 21.4% >> vOffset = 0.003V # Test item 4- 2- 9- 2

T AMCA: MESE 2275: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.498 V, 1.6% >> vOffset = -0.014V # Test item 5- 2- 9- 1

T AMCA: MESE 2275: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 20.1% >> vOffset = 0.006V # Test item 5- 2- 9- 2

T AMCA: MESE 2276: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 1.0% >> vOffset = -0.023V # Test item 6- 2- 9- 1

T AMCA: MESE 2276: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 23.9% >> vOffset = 0.005V # Test item 6- 2- 9- 2

T AMCA: MESE 2277: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 0.6% >> vOffset = -0.013V # Test item 7- 2- 9- 1

T AMCA: MESE 2277: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 22.7% >> vOffset = 0.005V # Test item 7- 2- 9- 2

T AMCA: MESE 2278: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.498 V, 1.1% >> vOffset = -0.023V # Test item 8- 2- 9- 1

T AMCA: MESE 2278: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 21.4% >> vOffset = 0.006V # Test item 8- 2- 9- 2

T AMCA: MESE 2271: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 1- 7- 1- 1

T AMCA: MESE 2272: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 2- 7- 1- 1

T AMCA: MESE 2273: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 3- 7- 1- 1

T AMCA: MESE 2274: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 4- 7- 1- 1

T AMCA: MESE 2275: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 5- 7- 1- 1

T AMCA: MESE 2276: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 6- 7- 1- 1

T AMCA: MESE 2277: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 7- 7- 1- 1

T AMCA: MESE 2278: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 8- 7- 1- 1

T AMCA: MESE 2271: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 1- 7- 1- 2

T AMCA: MESE 2272: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 2- 7- 1- 2

T AMCA: MESE 2273: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 3- 7- 1- 2

T AMCA: MESE 2274: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 4- 7- 1- 2

T AMCA: MESE 2275: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 5- 7- 1- 2

T AMCA: MESE 2276: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 6- 7- 1- 2

T AMCA: MESE 2277: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 7- 7- 1- 2

T AMCA: MESE 2278: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 8- 7- 1- 2

T AMCA: MESE 2271: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 1- 7- 2- 1

T AMCA: MESE 2272: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 2- 7- 2- 1

T AMCA: MESE 2273: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 3- 7- 2- 1

T AMCA: MESE 2274: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 4- 7- 2- 1

T AMCA: MESE 2275: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 5- 7- 2- 1

T AMCA: MESE 2276: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 6- 7- 2- 1

T AMCA: MESE 2277: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 7- 7- 2- 1

T AMCA: MESE 2278: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 8- 7- 2- 1

T AMCA: MESE 2271: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 7- 3- 1

T AMCA: MESE 2271: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 7- 3- 2

T AMCA: MESE 2272: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 7- 3- 1

T AMCA: MESE 2272: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 7- 3- 2

T AMCA: MESE 2273: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 7- 3- 1

T AMCA: MESE 2273: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 7- 3- 2

T AMCA: MESE 2274: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 7- 3- 1

T AMCA: MESE 2274: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 7- 3- 2

T AMCA: MESE 2275: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 7- 3- 1

T AMCA: MESE 2275: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 7- 3- 2

T AMCA: MESE 2276: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 7- 3- 1

T AMCA: MESE 2276: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 7- 3- 2

T AMCA: MESE 2277: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 7- 3- 1

T AMCA: MESE 2277: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 7- 3- 2

T AMCA: MESE 2278: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 7- 3- 1

T AMCA: MESE 2278: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 7- 3- 2

T AMCA: MESE 2271: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 1- 8- 1- 1

T AMCA: MESE 2271: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 1- 8- 1- 2

T AMCA: MESE 2271: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 1- 8- 1- 3

T AMCA: MESE 2271: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 4

T AMCA: MESE 2271: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 1- 8- 1- 5

T AMCA: MESE 2271: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 6

T AMCA: MESE 2271: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 1- 8- 1- 7

T AMCA: MESE 2271: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 8

T AMCA: MESE 2271: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 9

T AMCA: MESE 2272: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 2- 8- 1- 1

T AMCA: MESE 2272: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 2- 8- 1- 2

T AMCA: MESE 2272: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 2- 8- 1- 3

T AMCA: MESE 2272: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 4

T AMCA: MESE 2272: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 2- 8- 1- 5

T AMCA: MESE 2272: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 6

T AMCA: MESE 2272: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 2- 8- 1- 7

T AMCA: MESE 2272: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 8

T AMCA: MESE 2272: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 9

T AMCA: MESE 2273: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 3- 8- 1- 1

T AMCA: MESE 2273: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 3- 8- 1- 2

T AMCA: MESE 2273: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 3- 8- 1- 3

T AMCA: MESE 2273: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 4

T AMCA: MESE 2273: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 3- 8- 1- 5

T AMCA: MESE 2273: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 6

T AMCA: MESE 2273: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 3- 8- 1- 7

T AMCA: MESE 2273: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 8

T AMCA: MESE 2273: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 9

T AMCA: MESE 2274: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 4- 8- 1- 1

T AMCA: MESE 2274: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 4- 8- 1- 2

T AMCA: MESE 2274: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 4- 8- 1- 3

T AMCA: MESE 2274: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 4

T AMCA: MESE 2274: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 4- 8- 1- 5

T AMCA: MESE 2274: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 6

T AMCA: MESE 2274: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 4- 8- 1- 7

T AMCA: MESE 2274: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 8

T AMCA: MESE 2274: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 9

T AMCA: MESE 2275: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 5- 8- 1- 1

T AMCA: MESE 2275: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 5- 8- 1- 2

T AMCA: MESE 2275: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 5- 8- 1- 3

T AMCA: MESE 2275: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 4

T AMCA: MESE 2275: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 5- 8- 1- 5

T AMCA: MESE 2275: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 6

T AMCA: MESE 2275: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 5- 8- 1- 7

T AMCA: MESE 2275: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 8

T AMCA: MESE 2275: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 9

T AMCA: MESE 2276: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 6- 8- 1- 1

T AMCA: MESE 2276: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 6- 8- 1- 2

T AMCA: MESE 2276: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 6- 8- 1- 3

T AMCA: MESE 2276: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 4

T AMCA: MESE 2276: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 6- 8- 1- 5

T AMCA: MESE 2276: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 6

T AMCA: MESE 2276: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 6- 8- 1- 7

T AMCA: MESE 2276: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 8

T AMCA: MESE 2276: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 9

T AMCA: MESE 2277: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 7- 8- 1- 1

T AMCA: MESE 2277: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 7- 8- 1- 2

T AMCA: MESE 2277: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 7- 8- 1- 3

T AMCA: MESE 2277: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 4

T AMCA: MESE 2277: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 7- 8- 1- 5

T AMCA: MESE 2277: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 6

T AMCA: MESE 2277: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 7- 8- 1- 7

T AMCA: MESE 2277: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 8

T AMCA: MESE 2277: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 9

T AMCA: MESE 2278: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 8- 8- 1- 1

T AMCA: MESE 2278: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 8- 8- 1- 2

T AMCA: MESE 2278: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 8- 8- 1- 3

T AMCA: MESE 2278: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 4

T AMCA: MESE 2278: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 8- 8- 1- 5

T AMCA: MESE 2278: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 6

T AMCA: MESE 2278: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 8- 8- 1- 7

T AMCA: MESE 2278: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 8

T AMCA: MESE 2278: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 9

T AMCA: MESE 2271: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 1, masterModule = 1, slaveUnit = 3, slaveModule = 1 # Test item 1- 8- 3- 1

T AMCA: MESE 2271: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 1, masterModule = 1, slaveUnit = 3, slaveModule = 1 # Test item 1- 8- 3- 2

T AMCA: MESE 2273: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 3, masterModule = 1, slaveUnit = 2, slaveModule = 2 # Test item 3- 8- 3- 3

T AMCA: MESE 2273: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 3, masterModule = 1, slaveUnit = 2, slaveModule = 2 # Test item 3- 8- 3- 4

T AMCA: MESE 2272: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 2, masterModule = 2, slaveUnit = 4, slaveModule = 2 # Test item 2- 8- 3- 5

T AMCA: MESE 2272: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 2, masterModule = 2, slaveUnit = 4, slaveModule = 2 # Test item 2- 8- 3- 6

T AMCA: MESE 2274: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 4, masterModule = 2, slaveUnit = 5, slaveModule = 3 # Test item 4- 8- 3- 7

T AMCA: MESE 2274: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 4, masterModule = 2, slaveUnit = 5, slaveModule = 3 # Test item 4- 8- 3- 8

T AMCA: MESE 2275: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 5, masterModule = 3, slaveUnit = 7, slaveModule = 3 # Test item 5- 8- 3- 9

T AMCA: MESE 2275: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 5, masterModule = 3, slaveUnit = 7, slaveModule = 3 # Test item 5- 8- 3-10

T AMCA: MESE 2277: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 7, masterModule = 3, slaveUnit = 8, slaveModule = 4 # Test item 7- 8- 3-11

T AMCA: MESE 2277: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 7, masterModule = 3, slaveUnit = 8, slaveModule = 4 # Test item 7- 8- 3-12

T AMCA: MESE 2278: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 8, masterModule = 4, slaveUnit = 6, slaveModule = 4 # Test item 8- 8- 3-13

T AMCA: MESE 2278: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 8, masterModule = 4, slaveUnit = 6, slaveModule = 4 # Test item 8- 8- 3-14

T AMCA: MESE 2276: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 6, masterModule = 4, slaveUnit = 1, slaveModule = 1 # Test item 6- 8- 3-15

T AMCA: MESE 2276: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 6, masterModule = 4, slaveUnit = 1, slaveModule = 1 # Test item 6- 8- 3-16

T AMCA: MESE 2271: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.812 ns, 9.4% >> short = 58842, long = 29987 # Test item 1- 8- 6- 1

T AMCA: MESE 2272: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.891 ns, 5.4% >> short = 59429, long = 30019 # Test item 2- 8- 6- 1

T AMCA: MESE 2273: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 10.158 ns, 5.3% >> short = 58657, long = 29430 # Test item 3- 8- 6- 1

T AMCA: MESE 2274: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.969 ns, 1.6% >> short = 59654, long = 29960 # Test item 4- 8- 6- 1

T AMCA: MESE 2275: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 10.235 ns, 7.8% >> short = 58506, long = 29282 # Test item 5- 8- 6- 1

T AMCA: MESE 2276: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 10.031 ns, 1.0% >> short = 60720, long = 30132 # Test item 6- 8- 6- 1

T AMCA: MESE 2277: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 10.351 ns, 11.7% >> short = 58932, long = 29223 # Test item 7- 8- 6- 1

T AMCA: MESE 2278: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.993 ns, 0.4% >> short = 59985, long = 30007 # Test item 8- 8- 6- 1

T AMCA: MESE 2271: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 1- 8- 7- 1

T AMCA: MESE 2271: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18676, 33.1% # Test item 1- 8- 7- 2

T AMCA: MESE 2271: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x000048F4: Reg\_meas = 0x000048F4 # Test item 1- 8- 7- 3

T AMCA: MESE 2271: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 8- 7- 4

T AMCA: MESE 2272: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 2- 8- 7- 1

T AMCA: MESE 2272: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18604, 34.9% # Test item 2- 8- 7- 2

T AMCA: MESE 2272: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x000048AC: Reg\_meas = 0x000048AC # Test item 2- 8- 7- 3

T AMCA: MESE 2272: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 8- 7- 4

T AMCA: MESE 2273: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 3- 8- 7- 1

T AMCA: MESE 2273: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18007, 49.8% # Test item 3- 8- 7- 2

T AMCA: MESE 2273: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004657: Reg\_meas = 0x00004657 # Test item 3- 8- 7- 3

T AMCA: MESE 2273: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 8- 7- 4

T AMCA: MESE 2274: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 4- 8- 7- 1

T AMCA: MESE 2274: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18366, 40.8% # Test item 4- 8- 7- 2

T AMCA: MESE 2274: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x000047BE: Reg\_meas = 0x000047BE # Test item 4- 8- 7- 3

T AMCA: MESE 2274: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 8- 7- 4

T AMCA: MESE 2275: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 5- 8- 7- 1

T AMCA: MESE 2275: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18448, 38.8% # Test item 5- 8- 7- 2

T AMCA: MESE 2275: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004810: Reg\_meas = 0x00004810 # Test item 5- 8- 7- 3

T AMCA: MESE 2275: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 8- 7- 4

T AMCA: MESE 2276: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 6- 8- 7- 1

T AMCA: MESE 2276: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17573, 60.7% # Test item 6- 8- 7- 2

T AMCA: MESE 2276: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x000044A5: Reg\_meas = 0x000044A5 # Test item 6- 8- 7- 3

T AMCA: MESE 2276: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 8- 7- 4

T AMCA: MESE 2277: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 7- 8- 7- 1

T AMCA: MESE 2277: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17815, 54.6% # Test item 7- 8- 7- 2

T AMCA: MESE 2277: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004597: Reg\_meas = 0x00004597 # Test item 7- 8- 7- 3

T AMCA: MESE 2277: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 8- 7- 4

T AMCA: MESE 2278: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 8- 8- 7- 1

T AMCA: MESE 2278: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18288, 42.8% # Test item 8- 8- 7- 2

T AMCA: MESE 2278: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004770: Reg\_meas = 0x00004770 # Test item 8- 8- 7- 3

T AMCA: MESE 2278: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 8- 7- 4

T AMCA: MESE 2271: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 9- 1- 1

T AMCA: MESE 2271: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 9- 1- 2

T AMCA: MESE 2271: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 3

T AMCA: MESE 2271: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 4

T AMCA: MESE 2271: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 5

T AMCA: MESE 2271: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 6

T AMCA: MESE 2271: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 7

T AMCA: MESE 2271: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 1- 9- 1- 8

T AMCA: MESE 2271: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 9

T AMCA: MESE 2271: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1-10

T AMCA: MESE 2271: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 1- 9- 1-11

T AMCA: MESE 2271: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 1- 9- 1-12

T AMCA: MESE 2271: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 1- 9- 1-13

T AMCA: MESE 2271: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 9- 1-14

T AMCA: MESE 2272: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 9- 1- 1

T AMCA: MESE 2272: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 9- 1- 2

T AMCA: MESE 2272: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 3

T AMCA: MESE 2272: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 4

T AMCA: MESE 2272: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 5

T AMCA: MESE 2272: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 6

T AMCA: MESE 2272: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 7

T AMCA: MESE 2272: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 2- 9- 1- 8

T AMCA: MESE 2272: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 9

T AMCA: MESE 2272: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1-10

T AMCA: MESE 2272: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 2- 9- 1-11

T AMCA: MESE 2272: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 2- 9- 1-12

T AMCA: MESE 2272: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 2- 9- 1-13

T AMCA: MESE 2272: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 9- 1-14

T AMCA: MESE 2273: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 9- 1- 1

T AMCA: MESE 2273: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 9- 1- 2

T AMCA: MESE 2273: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 3

T AMCA: MESE 2273: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 4

T AMCA: MESE 2273: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 5

T AMCA: MESE 2273: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 6

T AMCA: MESE 2273: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 7

T AMCA: MESE 2273: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 3- 9- 1- 8

T AMCA: MESE 2273: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 9

T AMCA: MESE 2273: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1-10

T AMCA: MESE 2273: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 3- 9- 1-11

T AMCA: MESE 2273: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 3- 9- 1-12

T AMCA: MESE 2273: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 3- 9- 1-13

T AMCA: MESE 2273: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 9- 1-14

T AMCA: MESE 2274: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 9- 1- 1

T AMCA: MESE 2274: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 9- 1- 2

T AMCA: MESE 2274: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 3

T AMCA: MESE 2274: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 4

T AMCA: MESE 2274: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 5

T AMCA: MESE 2274: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 6

T AMCA: MESE 2274: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 7

T AMCA: MESE 2274: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 4- 9- 1- 8

T AMCA: MESE 2274: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 9

T AMCA: MESE 2274: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1-10

T AMCA: MESE 2274: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 4- 9- 1-11

T AMCA: MESE 2274: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 4- 9- 1-12

T AMCA: MESE 2274: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 4- 9- 1-13

T AMCA: MESE 2274: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 9- 1-14

T AMCA: MESE 2275: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 9- 1- 1

T AMCA: MESE 2275: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 9- 1- 2

T AMCA: MESE 2275: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 3

T AMCA: MESE 2275: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 4

T AMCA: MESE 2275: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 5

T AMCA: MESE 2275: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 6

T AMCA: MESE 2275: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 7

T AMCA: MESE 2275: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 5- 9- 1- 8

T AMCA: MESE 2275: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 9

T AMCA: MESE 2275: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1-10

T AMCA: MESE 2275: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 5- 9- 1-11

T AMCA: MESE 2275: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 5- 9- 1-12

T AMCA: MESE 2275: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 5- 9- 1-13

T AMCA: MESE 2275: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 9- 1-14

T AMCA: MESE 2276: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 9- 1- 1

T AMCA: MESE 2276: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 9- 1- 2

T AMCA: MESE 2276: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 3

T AMCA: MESE 2276: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 4

T AMCA: MESE 2276: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 5

T AMCA: MESE 2276: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 6

T AMCA: MESE 2276: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 7

T AMCA: MESE 2276: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 6- 9- 1- 8

T AMCA: MESE 2276: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 9

T AMCA: MESE 2276: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1-10

T AMCA: MESE 2276: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 6- 9- 1-11

T AMCA: MESE 2276: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 6- 9- 1-12

T AMCA: MESE 2276: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 6- 9- 1-13

T AMCA: MESE 2276: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 9- 1-14

T AMCA: MESE 2277: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 9- 1- 1

T AMCA: MESE 2277: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 9- 1- 2

T AMCA: MESE 2277: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 3

T AMCA: MESE 2277: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 4

T AMCA: MESE 2277: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 5

T AMCA: MESE 2277: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 6

T AMCA: MESE 2277: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 7

T AMCA: MESE 2277: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 7- 9- 1- 8

T AMCA: MESE 2277: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 9

T AMCA: MESE 2277: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1-10

T AMCA: MESE 2277: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 7- 9- 1-11

T AMCA: MESE 2277: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 7- 9- 1-12

T AMCA: MESE 2277: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 7- 9- 1-13

T AMCA: MESE 2277: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 9- 1-14

T AMCA: MESE 2278: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 9- 1- 1

T AMCA: MESE 2278: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 9- 1- 2

T AMCA: MESE 2278: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 3

T AMCA: MESE 2278: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 4

T AMCA: MESE 2278: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 5

T AMCA: MESE 2278: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 6

T AMCA: MESE 2278: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 7

T AMCA: MESE 2278: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 8- 9- 1- 8

T AMCA: MESE 2278: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 9

T AMCA: MESE 2278: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1-10

T AMCA: MESE 2278: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 8- 9- 1-11

T AMCA: MESE 2278: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 8- 9- 1-12

T AMCA: MESE 2278: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 8- 9- 1-13

T AMCA: MESE 2278: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 9- 1-14

T AMCA: MESE 2271: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.958 V, 0.0% # Test item 1-17- 1- 1

T AMCA: MESE 2271: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.961 V, 2.7% # Test item 1-17- 1- 2

T AMCA: MESE 2271: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.954 V, 3.6% # Test item 1-17- 1- 3

T AMCA: MESE 2271: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.955 V, 2.7% # Test item 1-17- 1- 4

T AMCA: MESE 2271: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.074 V, 6.7% # Test item 1-17- 1- 5

T AMCA: MESE 2271: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.981 V, 1.8% # Test item 1-17- 1- 6

T AMCA: MESE 2271: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.964 V, 8.2% # Test item 1-17- 1- 7

T AMCA: MESE 2271: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.917 V, 9.2% # Test item 1-17- 1- 8

T AMCA: MESE 2271: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.974 V, 4.5% # Test item 1-17- 1- 9

T AMCA: MESE 2271: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.983 V, 9.1% # Test item 1-17- 1-10

T AMCA: MESE 2271: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.974 V, 1.8% # Test item 1-17- 1-11

T AMCA: MESE 2272: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.962 V, 3.6% # Test item 2-17- 1- 1

T AMCA: MESE 2272: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.960 V, 1.8% # Test item 2-17- 1- 2

T AMCA: MESE 2272: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.957 V, 0.9% # Test item 2-17- 1- 3

T AMCA: MESE 2272: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.956 V, 1.8% # Test item 2-17- 1- 4

T AMCA: MESE 2272: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.065 V, 3.3% # Test item 2-17- 1- 5

T AMCA: MESE 2272: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.978 V, 0.9% # Test item 2-17- 1- 6

T AMCA: MESE 2272: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.970 V, 2.7% # Test item 2-17- 1- 7

T AMCA: MESE 2272: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.898 V, 0.1% # Test item 2-17- 1- 8

T AMCA: MESE 2272: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.968 V, 10.0% # Test item 2-17- 1- 9

T AMCA: MESE 2272: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.985 V, 7.3% # Test item 2-17- 1-10

T AMCA: MESE 2272: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.971 V, 4.5% # Test item 2-17- 1-11

T AMCA: MESE 2273: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.959 V, 0.9% # Test item 3-17- 1- 1

T AMCA: MESE 2273: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.961 V, 2.7% # Test item 3-17- 1- 2

T AMCA: MESE 2273: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.964 V, 5.5% # Test item 3-17- 1- 3

T AMCA: MESE 2273: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.951 V, 6.4% # Test item 3-17- 1- 4

T AMCA: MESE 2273: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.057 V, 10.0% # Test item 3-17- 1- 5

T AMCA: MESE 2273: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.986 V, 6.4% # Test item 3-17- 1- 6

T AMCA: MESE 2273: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.966 V, 6.4% # Test item 3-17- 1- 7

T AMCA: MESE 2273: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.648 V, 13.9% # Test item 3-17- 1- 8

T AMCA: MESE 2273: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.974 V, 4.5% # Test item 3-17- 1- 9

T AMCA: MESE 2273: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.711 V, 15.5% # Test item 3-17- 1-10

T AMCA: MESE 2274: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.959 V, 0.9% # Test item 4-17- 1- 1

T AMCA: MESE 2274: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.959 V, 0.9% # Test item 4-17- 1- 2

T AMCA: MESE 2274: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.963 V, 4.5% # Test item 4-17- 1- 3

T AMCA: MESE 2274: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.963 V, 4.5% # Test item 4-17- 1- 4

T AMCA: MESE 2274: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.056 V, 10.9% # Test item 4-17- 1- 5

T AMCA: MESE 2274: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.983 V, 3.6% # Test item 4-17- 1- 6

T AMCA: MESE 2274: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.971 V, 1.8% # Test item 4-17- 1- 7

T AMCA: MESE 2274: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.638 V, 4.8% # Test item 4-17- 1- 8

T AMCA: MESE 2274: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.975 V, 3.6% # Test item 4-17- 1- 9

T AMCA: MESE 2274: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.709 V, 17.3% # Test item 4-17- 1-10

T AMCA: MESE 2275: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.950 V, 7.3% # Test item 5-17- 1- 1

T AMCA: MESE 2275: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.955 V, 2.7% # Test item 5-17- 1- 2

T AMCA: MESE 2275: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.951 V, 6.4% # Test item 5-17- 1- 3

T AMCA: MESE 2275: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.958 V, 0.0% # Test item 5-17- 1- 4

T AMCA: MESE 2275: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.061 V, 7.8% # Test item 5-17- 1- 5

T AMCA: MESE 2275: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.976 V, 2.7% # Test item 5-17- 1- 6

T AMCA: MESE 2275: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.976 V, 2.7% # Test item 5-17- 1- 7

T AMCA: MESE 2275: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.898 V, 0.1% # Test item 5-17- 1- 8

T AMCA: MESE 2275: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.974 V, 4.5% # Test item 5-17- 1- 9

T AMCA: MESE 2275: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.996 V, 2.7% # Test item 5-17- 1-10

T AMCA: MESE 2275: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.961 V, 13.6% # Test item 5-17- 1-11

T AMCA: MESE 2276: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.958 V, 0.0% # Test item 6-17- 1- 1

T AMCA: MESE 2276: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.963 V, 4.5% # Test item 6-17- 1- 2

T AMCA: MESE 2276: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.956 V, 1.8% # Test item 6-17- 1- 3

T AMCA: MESE 2276: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.953 V, 4.5% # Test item 6-17- 1- 4

T AMCA: MESE 2276: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.066 V, 1.8% # Test item 6-17- 1- 5

T AMCA: MESE 2276: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.977 V, 1.8% # Test item 6-17- 1- 6

T AMCA: MESE 2276: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.972 V, 0.9% # Test item 6-17- 1- 7

T AMCA: MESE 2276: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.637 V, 3.9% # Test item 6-17- 1- 8

T AMCA: MESE 2276: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.978 V, 0.9% # Test item 6-17- 1- 9

T AMCA: MESE 2276: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.707 V, 19.1% # Test item 6-17- 1-10

T AMCA: MESE 2277: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.961 V, 2.7% # Test item 7-17- 1- 1

T AMCA: MESE 2277: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.956 V, 1.8% # Test item 7-17- 1- 2

T AMCA: MESE 2277: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.963 V, 4.5% # Test item 7-17- 1- 3

T AMCA: MESE 2277: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.954 V, 3.6% # Test item 7-17- 1- 4

T AMCA: MESE 2277: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.062 V, 5.5% # Test item 7-17- 1- 5

T AMCA: MESE 2277: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.978 V, 0.9% # Test item 7-17- 1- 6

T AMCA: MESE 2277: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.965 V, 7.3% # Test item 7-17- 1- 7

T AMCA: MESE 2277: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.634 V, 1.1% # Test item 7-17- 1- 8

T AMCA: MESE 2277: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.981 V, 1.8% # Test item 7-17- 1- 9

T AMCA: MESE 2277: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.709 V, 17.3% # Test item 7-17- 1-10

T AMCA: MESE 2278: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.964 V, 5.5% # Test item 8-17- 1- 1

T AMCA: MESE 2278: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.953 V, 4.5% # Test item 8-17- 1- 2

T AMCA: MESE 2278: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.958 V, 0.0% # Test item 8-17- 1- 3

T AMCA: MESE 2278: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.954 V, 3.6% # Test item 8-17- 1- 4

T AMCA: MESE 2278: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.071 V, 3.3% # Test item 8-17- 1- 5

T AMCA: MESE 2278: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.981 V, 1.8% # Test item 8-17- 1- 6

T AMCA: MESE 2278: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.980 V, 6.4% # Test item 8-17- 1- 7

T AMCA: MESE 2278: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.905 V, 3.4% # Test item 8-17- 1- 8

T AMCA: MESE 2278: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.979 V, 0.0% # Test item 8-17- 1- 9

T AMCA: MESE 2278: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.993 V, 0.0% # Test item 8-17- 1-10

T AMCA: MESE 2278: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.964 V, 10.9% # Test item 8-17- 1-11

T AMCA: MESE 2271: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.7% # Test item 1-15- 1- 1

T AMCA: MESE 2271: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.0% # Test item 1-15- 1- 2

T AMCA: MESE 2271: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.3% # Test item 1-15- 1- 3

T AMCA: MESE 2271: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.6% # Test item 1-15- 1- 4

T AMCA: MESE 2271: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.8% # Test item 1-15- 1- 5

T AMCA: MESE 2271: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.3% # Test item 1-15- 1- 6

T AMCA: MESE 2271: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.5% # Test item 1-15- 1- 7

T AMCA: MESE 2271: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.6% # Test item 1-15- 1- 8

T AMCA: MESE 2271: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.5% # Test item 1-15- 1- 9

T AMCA: MESE 2271: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.6% # Test item 1-15- 1-10

T AMCA: MESE 2271: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.5% # Test item 1-15- 1-11

T AMCA: MESE 2271: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.5% # Test item 1-15- 1-12

T AMCA: MESE 2272: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.005 V, 1.6% # Test item 2-15- 1- 1

T AMCA: MESE 2272: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% # Test item 2-15- 1- 2

T AMCA: MESE 2272: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.4% # Test item 2-15- 1- 3

T AMCA: MESE 2272: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.005 V, 1.6% # Test item 2-15- 1- 4

T AMCA: MESE 2272: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.5% # Test item 2-15- 1- 5

T AMCA: MESE 2272: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.4% # Test item 2-15- 1- 6

T AMCA: MESE 2272: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.3% # Test item 2-15- 1- 7

T AMCA: MESE 2272: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% # Test item 2-15- 1- 8

T AMCA: MESE 2272: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.009 V, 2.9% # Test item 2-15- 1- 9

T AMCA: MESE 2272: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.3% # Test item 2-15- 1-10

T AMCA: MESE 2272: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% # Test item 2-15- 1-11

T AMCA: MESE 2272: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.009 V, 3.0% # Test item 2-15- 1-12

T AMCA: MESE 2273: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.003 V, 1.1% # Test item 3-15- 1- 1

T AMCA: MESE 2273: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.5% # Test item 3-15- 1- 2

T AMCA: MESE 2273: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.014 V, 4.8% # Test item 3-15- 1- 3

T AMCA: MESE 2273: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.003 V, 1.1% # Test item 3-15- 1- 4

T AMCA: MESE 2273: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.4% # Test item 3-15- 1- 5

T AMCA: MESE 2273: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.015 V, 4.9% # Test item 3-15- 1- 6

T AMCA: MESE 2273: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.005 V, 1.5% # Test item 3-15- 1- 7

T AMCA: MESE 2273: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.3% # Test item 3-15- 1- 8

T AMCA: MESE 2273: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.014 V, 4.8% # Test item 3-15- 1- 9

T AMCA: MESE 2273: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.005 V, 1.6% # Test item 3-15- 1-10

T AMCA: MESE 2273: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.3% # Test item 3-15- 1-11

T AMCA: MESE 2273: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.014 V, 4.8% # Test item 3-15- 1-12

T AMCA: MESE 2274: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.7% # Test item 4-15- 1- 1

T AMCA: MESE 2274: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.4% # Test item 4-15- 1- 2

T AMCA: MESE 2274: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.8% # Test item 4-15- 1- 3

T AMCA: MESE 2274: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.6% # Test item 4-15- 1- 4

T AMCA: MESE 2274: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.3% # Test item 4-15- 1- 5

T AMCA: MESE 2274: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.8% # Test item 4-15- 1- 6

T AMCA: MESE 2274: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.4% # Test item 4-15- 1- 7

T AMCA: MESE 2274: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.0% # Test item 4-15- 1- 8

T AMCA: MESE 2274: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.2% # Test item 4-15- 1- 9

T AMCA: MESE 2274: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.4% # Test item 4-15- 1-10

T AMCA: MESE 2274: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.9% # Test item 4-15- 1-11

T AMCA: MESE 2274: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.2% # Test item 4-15- 1-12

T AMCA: MESE 2275: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.3% # Test item 5-15- 1- 1

T AMCA: MESE 2275: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% # Test item 5-15- 1- 2

T AMCA: MESE 2275: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.4% # Test item 5-15- 1- 3

T AMCA: MESE 2275: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.3% # Test item 5-15- 1- 4

T AMCA: MESE 2275: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.9% # Test item 5-15- 1- 5

T AMCA: MESE 2275: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.4% # Test item 5-15- 1- 6

T AMCA: MESE 2275: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 2.1% # Test item 5-15- 1- 7

T AMCA: MESE 2275: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.0% # Test item 5-15- 1- 8

T AMCA: MESE 2275: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.2% # Test item 5-15- 1- 9

T AMCA: MESE 2275: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 1.9% # Test item 5-15- 1-10

T AMCA: MESE 2275: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% # Test item 5-15- 1-11

T AMCA: MESE 2275: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.3% # Test item 5-15- 1-12

T AMCA: MESE 2276: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.002 V, 0.7% # Test item 6-15- 1- 1

T AMCA: MESE 2276: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.0% # Test item 6-15- 1- 2

T AMCA: MESE 2276: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.4% # Test item 6-15- 1- 3

T AMCA: MESE 2276: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.002 V, 0.6% # Test item 6-15- 1- 4

T AMCA: MESE 2276: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.1% # Test item 6-15- 1- 5

T AMCA: MESE 2276: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.3% # Test item 6-15- 1- 6

T AMCA: MESE 2276: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.002 V, 0.6% # Test item 6-15- 1- 7

T AMCA: MESE 2276: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.7% # Test item 6-15- 1- 8

T AMCA: MESE 2276: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.014 V, 4.8% # Test item 6-15- 1- 9

T AMCA: MESE 2276: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.002 V, 0.6% # Test item 6-15- 1-10

T AMCA: MESE 2276: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.5% # Test item 6-15- 1-11

T AMCA: MESE 2276: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.014 V, 4.7% # Test item 6-15- 1-12

T AMCA: MESE 2277: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 2.0% # Test item 7-15- 1- 1

T AMCA: MESE 2277: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.0% # Test item 7-15- 1- 2

T AMCA: MESE 2277: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.016 V, 5.4% # Test item 7-15- 1- 3

T AMCA: MESE 2277: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 1.8% # Test item 7-15- 1- 4

T AMCA: MESE 2277: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.9% # Test item 7-15- 1- 5

T AMCA: MESE 2277: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.016 V, 5.3% # Test item 7-15- 1- 6

T AMCA: MESE 2277: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.005 V, 1.6% # Test item 7-15- 1- 7

T AMCA: MESE 2277: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.9% # Test item 7-15- 1- 8

T AMCA: MESE 2277: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.015 V, 4.9% # Test item 7-15- 1- 9

T AMCA: MESE 2277: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.005 V, 1.5% # Test item 7-15- 1-10

T AMCA: MESE 2277: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.9% # Test item 7-15- 1-11

T AMCA: MESE 2277: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.015 V, 5.0% # Test item 7-15- 1-12

T AMCA: MESE 2278: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.3% # Test item 8-15- 1- 1

T AMCA: MESE 2278: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% # Test item 8-15- 1- 2

T AMCA: MESE 2278: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.3% # Test item 8-15- 1- 3

T AMCA: MESE 2278: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.2% # Test item 8-15- 1- 4

T AMCA: MESE 2278: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% # Test item 8-15- 1- 5

T AMCA: MESE 2278: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.3% # Test item 8-15- 1- 6

T AMCA: MESE 2278: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 2.0% # Test item 8-15- 1- 7

T AMCA: MESE 2278: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% # Test item 8-15- 1- 8

T AMCA: MESE 2278: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.012 V, 4.0% # Test item 8-15- 1- 9

T AMCA: MESE 2278: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 1.9% # Test item 8-15- 1-10

T AMCA: MESE 2278: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% # Test item 8-15- 1-11

T AMCA: MESE 2278: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.012 V, 4.0% # Test item 8-15- 1-12

T AMCA: MESE 2271: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.741 MOhm, 17.3% # Test item 1-15- 2- 1

T AMCA: MESE 2271: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.875 MOhm, 8.3% # Test item 1-15- 2- 2

T AMCA: MESE 2271: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.963 MOhm, 2.5% # Test item 1-15- 2- 3

T AMCA: MESE 2271: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.768 MOhm, 15.4% # Test item 1-15- 2- 4

T AMCA: MESE 2272: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.780 MOhm, 14.7% # Test item 2-15- 2- 1

T AMCA: MESE 2272: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.718 MOhm, 18.8% # Test item 2-15- 2- 2

T AMCA: MESE 2272: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.666 MOhm, 22.3% # Test item 2-15- 2- 3

T AMCA: MESE 2272: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.857 MOhm, 9.6% # Test item 2-15- 2- 4

T AMCA: MESE 2273: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.812 MOhm, 12.6% # Test item 3-15- 2- 1

T AMCA: MESE 2273: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.768 MOhm, 15.4% # Test item 3-15- 2- 2

T AMCA: MESE 2273: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.718 MOhm, 18.8% # Test item 3-15- 2- 3

T AMCA: MESE 2273: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.807 MOhm, 12.8% # Test item 3-15- 2- 4

T AMCA: MESE 2274: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.749 MOhm, 16.7% # Test item 4-15- 2- 1

T AMCA: MESE 2274: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.762 MOhm, 15.8% # Test item 4-15- 2- 2

T AMCA: MESE 2274: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.953 MOhm, 3.1% # Test item 4-15- 2- 3

T AMCA: MESE 2274: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.802 MOhm, 13.2% # Test item 4-15- 2- 4

T AMCA: MESE 2275: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.802 MOhm, 13.2% # Test item 5-15- 2- 1

T AMCA: MESE 2275: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.813 MOhm, 12.4% # Test item 5-15- 2- 2

T AMCA: MESE 2275: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.779 MOhm, 14.7% # Test item 5-15- 2- 3

T AMCA: MESE 2275: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.787 MOhm, 14.2% # Test item 5-15- 2- 4

T AMCA: MESE 2276: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.696 MOhm, 20.3% # Test item 6-15- 2- 1

T AMCA: MESE 2276: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.743 MOhm, 17.1% # Test item 6-15- 2- 2

T AMCA: MESE 2276: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.886 MOhm, 7.6% # Test item 6-15- 2- 3

T AMCA: MESE 2276: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.946 MOhm, 3.6% # Test item 6-15- 2- 4

T AMCA: MESE 2277: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.838 MOhm, 10.8% # Test item 7-15- 2- 1

T AMCA: MESE 2277: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.867 MOhm, 8.8% # Test item 7-15- 2- 2

T AMCA: MESE 2277: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.763 MOhm, 15.8% # Test item 7-15- 2- 3

T AMCA: MESE 2277: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.860 MOhm, 9.3% # Test item 7-15- 2- 4

T AMCA: MESE 2278: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.707 MOhm, 19.6% # Test item 8-15- 2- 1

T AMCA: MESE 2278: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.735 MOhm, 17.7% # Test item 8-15- 2- 2

T AMCA: MESE 2278: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.797 MOhm, 13.5% # Test item 8-15- 2- 3

T AMCA: MESE 2278: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.777 MOhm, 14.8% # Test item 8-15- 2- 4

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.021V, neg = 0.021V # Test item 1-15- 3- 1

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.714 V, 18.0% >> pos = 2.378V, neg = -2.336V # Test item 1-15- 3- 2

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.021V, neg = 0.021V # Test item 1-15- 3- 3

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 19.9% >> pos = 0.805V, neg = -0.763V # Test item 1-15- 3- 4

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.021V, neg = 0.021V # Test item 1-15- 3- 5

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.2% >> pos = 0.119V, neg = -0.077V # Test item 1-15- 3- 6

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.021V, neg = -0.021V # Test item 1-15- 3- 7

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.714 V, 17.9% >> pos = 2.336V, neg = -2.378V # Test item 1-15- 3- 8

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.021V, neg = -0.021V # Test item 1-15- 3- 9

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 19.9% >> pos = 0.763V, neg = -0.805V # Test item 1-15- 3-10

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.021V, neg = -0.021V # Test item 1-15- 3-11

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.3% >> pos = 0.077V, neg = -0.119V # Test item 1-15- 3-12

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.021V, neg = 0.021V # Test item 1-15- 3-13

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.714 V, 17.9% >> pos = 2.379V, neg = -2.335V # Test item 1-15- 3-14

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.021V, neg = 0.021V # Test item 1-15- 3-15

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 19.9% >> pos = 0.806V, neg = -0.762V # Test item 1-15- 3-16

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.022V, neg = 0.022V # Test item 1-15- 3-17

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.3% >> pos = 0.120V, neg = -0.077V # Test item 1-15- 3-18

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.021V, neg = -0.021V # Test item 1-15- 3-19

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.714 V, 17.9% >> pos = 2.335V, neg = -2.379V # Test item 1-15- 3-20

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.021V, neg = -0.021V # Test item 1-15- 3-21

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 19.9% >> pos = 0.762V, neg = -0.806V # Test item 1-15- 3-22

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.022V, neg = -0.022V # Test item 1-15- 3-23

T AMCA: MESE 2271: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.2% >> pos = 0.077V, neg = -0.120V # Test item 1-15- 3-24

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 2-15- 3- 1

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.1% >> pos = 2.385V, neg = -2.319V # Test item 2-15- 3- 2

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 2-15- 3- 3

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.4% >> pos = 0.815V, neg = -0.749V # Test item 2-15- 3- 4

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 2-15- 3- 5

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.7% >> pos = 0.131V, neg = -0.065V # Test item 2-15- 3- 6

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 2-15- 3- 7

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.704 V, 20.0% >> pos = 2.319V, neg = -2.385V # Test item 2-15- 3- 8

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 2-15- 3- 9

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.3% >> pos = 0.749V, neg = -0.815V # Test item 2-15- 3-10

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 2-15- 3-11

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.8% >> pos = 0.065V, neg = -0.131V # Test item 2-15- 3-12

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 2-15- 3-13

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.704 V, 19.9% >> pos = 2.385V, neg = -2.319V # Test item 2-15- 3-14

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 2-15- 3-15

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.0% >> pos = 0.815V, neg = -0.749V # Test item 2-15- 3-16

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 2-15- 3-17

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.6% >> pos = 0.131V, neg = -0.065V # Test item 2-15- 3-18

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 2-15- 3-19

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.1% >> pos = 2.319V, neg = -2.385V # Test item 2-15- 3-20

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 2-15- 3-21

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.1% >> pos = 0.749V, neg = -0.815V # Test item 2-15- 3-22

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 2-15- 3-23

T AMCA: MESE 2272: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.7% >> pos = 0.065V, neg = -0.131V # Test item 2-15- 3-24

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.028V, neg = 0.028V # Test item 3-15- 3- 1

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.3% >> pos = 2.379V, neg = -2.323V # Test item 3-15- 3- 2

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.028V, neg = 0.028V # Test item 3-15- 3- 3

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.3% >> pos = 0.810V, neg = -0.754V # Test item 3-15- 3- 4

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.028V, neg = 0.028V # Test item 3-15- 3- 5

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.7% >> pos = 0.126V, neg = -0.070V # Test item 3-15- 3- 6

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.028V, neg = -0.028V # Test item 3-15- 3- 7

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.2% >> pos = 2.324V, neg = -2.380V # Test item 3-15- 3- 8

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.028V, neg = -0.028V # Test item 3-15- 3- 9

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.2% >> pos = 0.754V, neg = -0.810V # Test item 3-15- 3-10

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.028V, neg = -0.028V # Test item 3-15- 3-11

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.7% >> pos = 0.070V, neg = -0.126V # Test item 3-15- 3-12

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.027V, neg = 0.027V # Test item 3-15- 3-13

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.702 V, 20.3% >> pos = 2.378V, neg = -2.324V # Test item 3-15- 3-14

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.027V, neg = 0.027V # Test item 3-15- 3-15

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.4% >> pos = 0.809V, neg = -0.755V # Test item 3-15- 3-16

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.027V, neg = 0.027V # Test item 3-15- 3-17

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.0% >> pos = 0.125V, neg = -0.071V # Test item 3-15- 3-18

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 3-15- 3-19

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.3% >> pos = 2.324V, neg = -2.379V # Test item 3-15- 3-20

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 3-15- 3-21

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.4% >> pos = 0.755V, neg = -0.809V # Test item 3-15- 3-22

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 3-15- 3-23

T AMCA: MESE 2273: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.0% >> pos = 0.071V, neg = -0.125V # Test item 3-15- 3-24

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 4-15- 3- 1

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.708 V, 19.2% >> pos = 2.387V, neg = -2.320V # Test item 4-15- 3- 2

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 4-15- 3- 3

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.4% >> pos = 0.816V, neg = -0.750V # Test item 4-15- 3- 4

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 4-15- 3- 5

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.0% >> pos = 0.131V, neg = -0.065V # Test item 4-15- 3- 6

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 4-15- 3- 7

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.706 V, 19.6% >> pos = 2.320V, neg = -2.386V # Test item 4-15- 3- 8

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 4-15- 3- 9

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.5% >> pos = 0.749V, neg = -0.816V # Test item 4-15- 3-10

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 4-15- 3-11

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.9% >> pos = 0.065V, neg = -0.131V # Test item 4-15- 3-12

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 4-15- 3-13

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.706 V, 19.7% >> pos = 2.386V, neg = -2.320V # Test item 4-15- 3-14

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 4-15- 3-15

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.4% >> pos = 0.816V, neg = -0.750V # Test item 4-15- 3-16

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 4-15- 3-17

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.6% >> pos = 0.131V, neg = -0.065V # Test item 4-15- 3-18

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 4-15- 3-19

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.706 V, 19.6% >> pos = 2.320V, neg = -2.386V # Test item 4-15- 3-20

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 4-15- 3-21

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.4% >> pos = 0.750V, neg = -0.816V # Test item 4-15- 3-22

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 4-15- 3-23

T AMCA: MESE 2274: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.7% >> pos = 0.065V, neg = -0.131V # Test item 4-15- 3-24

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 5-15- 3- 1

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.705 V, 19.8% >> pos = 2.377V, neg = -2.328V # Test item 5-15- 3- 2

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 5-15- 3- 3

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.8% >> pos = 0.807V, neg = -0.758V # Test item 5-15- 3- 4

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 5-15- 3- 5

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.3% >> pos = 0.123V, neg = -0.073V # Test item 5-15- 3- 6

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 5-15- 3- 7

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.705 V, 19.8% >> pos = 2.328V, neg = -2.377V # Test item 5-15- 3- 8

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 5-15- 3- 9

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.8% >> pos = 0.758V, neg = -0.807V # Test item 5-15- 3-10

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 5-15- 3-11

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.4% >> pos = 0.073V, neg = -0.123V # Test item 5-15- 3-12

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 5-15- 3-13

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.704 V, 20.0% >> pos = 2.377V, neg = -2.327V # Test item 5-15- 3-14

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 5-15- 3-15

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.0% >> pos = 0.808V, neg = -0.757V # Test item 5-15- 3-16

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 5-15- 3-17

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.5% >> pos = 0.123V, neg = -0.073V # Test item 5-15- 3-18

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 5-15- 3-19

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.705 V, 19.9% >> pos = 2.327V, neg = -2.378V # Test item 5-15- 3-20

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 5-15- 3-21

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 21.9% >> pos = 0.757V, neg = -0.808V # Test item 5-15- 3-22

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 5-15- 3-23

T AMCA: MESE 2275: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.4% >> pos = 0.073V, neg = -0.123V # Test item 5-15- 3-24

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.035V, neg = 0.035V # Test item 6-15- 3- 1

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.669 V, 27.3% >> pos = 2.370V, neg = -2.299V # Test item 6-15- 3- 2

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.035V, neg = 0.035V # Test item 6-15- 3- 3

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.553 V, 29.2% >> pos = 0.812V, neg = -0.741V # Test item 6-15- 3- 4

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.035V, neg = 0.035V # Test item 6-15- 3- 5

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 27.4% >> pos = 0.133V, neg = -0.062V # Test item 6-15- 3- 6

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.035V, neg = -0.035V # Test item 6-15- 3- 7

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.669 V, 27.3% >> pos = 2.299V, neg = -2.370V # Test item 6-15- 3- 8

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.035V, neg = -0.035V # Test item 6-15- 3- 9

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.553 V, 29.2% >> pos = 0.741V, neg = -0.812V # Test item 6-15- 3-10

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.035V, neg = -0.035V # Test item 6-15- 3-11

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.194 V, 27.5% >> pos = 0.062V, neg = -0.133V # Test item 6-15- 3-12

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.036V, neg = 0.036V # Test item 6-15- 3-13

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.669 V, 27.2% >> pos = 2.371V, neg = -2.298V # Test item 6-15- 3-14

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.036V, neg = 0.036V # Test item 6-15- 3-15

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.553 V, 29.1% >> pos = 0.813V, neg = -0.740V # Test item 6-15- 3-16

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.036V, neg = 0.036V # Test item 6-15- 3-17

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.194 V, 27.7% >> pos = 0.133V, neg = -0.061V # Test item 6-15- 3-18

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.036V, neg = -0.036V # Test item 6-15- 3-19

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.669 V, 27.2% >> pos = 2.298V, neg = -2.371V # Test item 6-15- 3-20

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.036V, neg = -0.036V # Test item 6-15- 3-21

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.553 V, 29.1% >> pos = 0.740V, neg = -0.813V # Test item 6-15- 3-22

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.036V, neg = -0.036V # Test item 6-15- 3-23

T AMCA: MESE 2276: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.194 V, 27.5% >> pos = 0.061V, neg = -0.134V # Test item 6-15- 3-24

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.024V, neg = 0.024V # Test item 7-15- 3- 1

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.704 V, 20.1% >> pos = 2.376V, neg = -2.328V # Test item 7-15- 3- 2

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.024V, neg = 0.024V # Test item 7-15- 3- 3

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.1% >> pos = 0.807V, neg = -0.758V # Test item 7-15- 3- 4

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.024V, neg = 0.024V # Test item 7-15- 3- 5

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.6% >> pos = 0.122V, neg = -0.074V # Test item 7-15- 3- 6

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.024V, neg = -0.024V # Test item 7-15- 3- 7

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.2% >> pos = 2.327V, neg = -2.376V # Test item 7-15- 3- 8

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.024V, neg = -0.024V # Test item 7-15- 3- 9

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.1% >> pos = 0.758V, neg = -0.807V # Test item 7-15- 3-10

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.024V, neg = -0.024V # Test item 7-15- 3-11

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.6% >> pos = 0.074V, neg = -0.122V # Test item 7-15- 3-12

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.024V, neg = 0.024V # Test item 7-15- 3-13

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.2% >> pos = 2.376V, neg = -2.327V # Test item 7-15- 3-14

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.024V, neg = 0.024V # Test item 7-15- 3-15

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.2% >> pos = 0.806V, neg = -0.758V # Test item 7-15- 3-16

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.024V, neg = 0.024V # Test item 7-15- 3-17

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.6% >> pos = 0.122V, neg = -0.074V # Test item 7-15- 3-18

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.024V, neg = -0.024V # Test item 7-15- 3-19

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.704 V, 20.1% >> pos = 2.327V, neg = -2.376V # Test item 7-15- 3-20

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.024V, neg = -0.024V # Test item 7-15- 3-21

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.1% >> pos = 0.758V, neg = -0.807V # Test item 7-15- 3-22

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.024V, neg = -0.024V # Test item 7-15- 3-23

T AMCA: MESE 2277: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.5% >> pos = 0.074V, neg = -0.122V # Test item 7-15- 3-24

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.037V, neg = 0.037V # Test item 8-15- 3- 1

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.687 V, 23.6% >> pos = 2.381V, neg = -2.306V # Test item 8-15- 3- 2

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.037V, neg = 0.037V # Test item 8-15- 3- 3

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.559 V, 25.6% >> pos = 0.817V, neg = -0.742V # Test item 8-15- 3- 4

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.037V, neg = 0.037V # Test item 8-15- 3- 5

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 24.1% >> pos = 0.135V, neg = -0.060V # Test item 8-15- 3- 6

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.037V, neg = -0.037V # Test item 8-15- 3- 7

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.686 V, 23.7% >> pos = 2.306V, neg = -2.381V # Test item 8-15- 3- 8

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.037V, neg = -0.037V # Test item 8-15- 3- 9

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.559 V, 25.7% >> pos = 0.742V, neg = -0.817V # Test item 8-15- 3-10

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.037V, neg = -0.037V # Test item 8-15- 3-11

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 24.0% >> pos = 0.060V, neg = -0.135V # Test item 8-15- 3-12

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.038V, neg = 0.038V # Test item 8-15- 3-13

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.686 V, 23.8% >> pos = 2.381V, neg = -2.305V # Test item 8-15- 3-14

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.037V, neg = 0.037V # Test item 8-15- 3-15

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.559 V, 25.8% >> pos = 0.817V, neg = -0.742V # Test item 8-15- 3-16

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.038V, neg = 0.038V # Test item 8-15- 3-17

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 24.3% >> pos = 0.135V, neg = -0.060V # Test item 8-15- 3-18

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.038V, neg = -0.038V # Test item 8-15- 3-19

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.686 V, 23.7% >> pos = 2.305V, neg = -2.381V # Test item 8-15- 3-20

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.037V, neg = -0.038V # Test item 8-15- 3-21

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.559 V, 25.8% >> pos = 0.742V, neg = -0.817V # Test item 8-15- 3-22

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.038V, neg = -0.038V # Test item 8-15- 3-23

T AMCA: MESE 2278: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 24.4% >> pos = 0.060V, neg = -0.135V # Test item 8-15- 3-24

T AMCA: MESE 2271: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.003 V, 0.9% # Test item 1-16- 1- 1

T AMCA: MESE 2271: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.6% # Test item 1-16- 1- 2

T AMCA: MESE 2271: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.6% # Test item 1-16- 1- 3

T AMCA: MESE 2271: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.003 V, 1.0% # Test item 1-16- 1- 4

T AMCA: MESE 2271: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.1% # Test item 1-16- 1- 5

T AMCA: MESE 2271: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.6% # Test item 1-16- 1- 6

T AMCA: MESE 2271: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.003 V, 0.9% # Test item 1-16- 1- 7

T AMCA: MESE 2271: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.8% # Test item 1-16- 1- 8

T AMCA: MESE 2271: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.6% # Test item 1-16- 1- 9

T AMCA: MESE 2271: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.003 V, 1.0% # Test item 1-16- 1-10

T AMCA: MESE 2271: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.9% # Test item 1-16- 1-11

T AMCA: MESE 2271: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.6% # Test item 1-16- 1-12

T AMCA: MESE 2272: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.003 V, 0.9% # Test item 2-16- 1- 1

T AMCA: MESE 2272: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.7% # Test item 2-16- 1- 2

T AMCA: MESE 2272: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.7% # Test item 2-16- 1- 3

T AMCA: MESE 2272: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.003 V, 1.0% # Test item 2-16- 1- 4

T AMCA: MESE 2272: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.7% # Test item 2-16- 1- 5

T AMCA: MESE 2272: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.8% # Test item 2-16- 1- 6

T AMCA: MESE 2272: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.003 V, 0.9% # Test item 2-16- 1- 7

T AMCA: MESE 2272: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.3% # Test item 2-16- 1- 8

T AMCA: MESE 2272: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.7% # Test item 2-16- 1- 9

T AMCA: MESE 2272: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.003 V, 1.0% # Test item 2-16- 1-10

T AMCA: MESE 2272: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.4% # Test item 2-16- 1-11

T AMCA: MESE 2272: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.8% # Test item 2-16- 1-12

T AMCA: MESE 2273: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.002 V, 0.6% # Test item 3-16- 1- 1

T AMCA: MESE 2273: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% # Test item 3-16- 1- 2

T AMCA: MESE 2273: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.2% # Test item 3-16- 1- 3

T AMCA: MESE 2273: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.002 V, 0.6% # Test item 3-16- 1- 4

T AMCA: MESE 2273: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% # Test item 3-16- 1- 5

T AMCA: MESE 2273: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.2% # Test item 3-16- 1- 6

T AMCA: MESE 2273: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.002 V, 0.7% # Test item 3-16- 1- 7

T AMCA: MESE 2273: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.3% # Test item 3-16- 1- 8

T AMCA: MESE 2273: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.2% # Test item 3-16- 1- 9

T AMCA: MESE 2273: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.002 V, 0.6% # Test item 3-16- 1-10

T AMCA: MESE 2273: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.3% # Test item 3-16- 1-11

T AMCA: MESE 2273: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.1% # Test item 3-16- 1-12

T AMCA: MESE 2274: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 2.1% # Test item 4-16- 1- 1

T AMCA: MESE 2274: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.5% # Test item 4-16- 1- 2

T AMCA: MESE 2274: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.6% # Test item 4-16- 1- 3

T AMCA: MESE 2274: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 2.1% # Test item 4-16- 1- 4

T AMCA: MESE 2274: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.5% # Test item 4-16- 1- 5

T AMCA: MESE 2274: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.5% # Test item 4-16- 1- 6

T AMCA: MESE 2274: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.2% # Test item 4-16- 1- 7

T AMCA: MESE 2274: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.7% # Test item 4-16- 1- 8

T AMCA: MESE 2274: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.6% # Test item 4-16- 1- 9

T AMCA: MESE 2274: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.2% # Test item 4-16- 1-10

T AMCA: MESE 2274: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.5% # Test item 4-16- 1-11

T AMCA: MESE 2274: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.7% # Test item 4-16- 1-12

T AMCA: MESE 2275: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.2% # Test item 5-16- 1- 1

T AMCA: MESE 2275: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.3% # Test item 5-16- 1- 2

T AMCA: MESE 2275: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 0.9% # Test item 5-16- 1- 3

T AMCA: MESE 2275: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.2% # Test item 5-16- 1- 4

T AMCA: MESE 2275: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.4% # Test item 5-16- 1- 5

T AMCA: MESE 2275: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 0.9% # Test item 5-16- 1- 6

T AMCA: MESE 2275: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.2% # Test item 5-16- 1- 7

T AMCA: MESE 2275: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.3% # Test item 5-16- 1- 8

T AMCA: MESE 2275: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 0.9% # Test item 5-16- 1- 9

T AMCA: MESE 2275: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.3% # Test item 5-16- 1-10

T AMCA: MESE 2275: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.3% # Test item 5-16- 1-11

T AMCA: MESE 2275: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 1.0% # Test item 5-16- 1-12

T AMCA: MESE 2276: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.6% # Test item 6-16- 1- 1

T AMCA: MESE 2276: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 8.1% # Test item 6-16- 1- 2

T AMCA: MESE 2276: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.993 V, 2.3% # Test item 6-16- 1- 3

T AMCA: MESE 2276: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.6% # Test item 6-16- 1- 4

T AMCA: MESE 2276: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 8.1% # Test item 6-16- 1- 5

T AMCA: MESE 2276: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.993 V, 2.4% # Test item 6-16- 1- 6

T AMCA: MESE 2276: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.6% # Test item 6-16- 1- 7

T AMCA: MESE 2276: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 7.8% # Test item 6-16- 1- 8

T AMCA: MESE 2276: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.993 V, 2.2% # Test item 6-16- 1- 9

T AMCA: MESE 2276: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.6% # Test item 6-16- 1-10

T AMCA: MESE 2276: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 8.1% # Test item 6-16- 1-11

T AMCA: MESE 2276: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.993 V, 2.4% # Test item 6-16- 1-12

T AMCA: MESE 2277: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.2% # Test item 7-16- 1- 1

T AMCA: MESE 2277: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.5% # Test item 7-16- 1- 2

T AMCA: MESE 2277: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.8% # Test item 7-16- 1- 3

T AMCA: MESE 2277: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.1% # Test item 7-16- 1- 4

T AMCA: MESE 2277: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.4% # Test item 7-16- 1- 5

T AMCA: MESE 2277: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.7% # Test item 7-16- 1- 6

T AMCA: MESE 2277: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.3% # Test item 7-16- 1- 7

T AMCA: MESE 2277: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.4% # Test item 7-16- 1- 8

T AMCA: MESE 2277: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.7% # Test item 7-16- 1- 9

T AMCA: MESE 2277: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.2% # Test item 7-16- 1-10

T AMCA: MESE 2277: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.4% # Test item 7-16- 1-11

T AMCA: MESE 2277: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.7% # Test item 7-16- 1-12

T AMCA: MESE 2278: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.5% # Test item 8-16- 1- 1

T AMCA: MESE 2278: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.8% # Test item 8-16- 1- 2

T AMCA: MESE 2278: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.6% # Test item 8-16- 1- 3

T AMCA: MESE 2278: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.6% # Test item 8-16- 1- 4

T AMCA: MESE 2278: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.8% # Test item 8-16- 1- 5

T AMCA: MESE 2278: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.6% # Test item 8-16- 1- 6

T AMCA: MESE 2278: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.7% # Test item 8-16- 1- 7

T AMCA: MESE 2278: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.7% # Test item 8-16- 1- 8

T AMCA: MESE 2278: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.6% # Test item 8-16- 1- 9

T AMCA: MESE 2278: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.5% # Test item 8-16- 1-10

T AMCA: MESE 2278: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.7% # Test item 8-16- 1-11

T AMCA: MESE 2278: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.6% # Test item 8-16- 1-12

T AMCA: MESE 2271: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9842.949 Ohm, 15.7% >> MV = 1.843V, offset = -0.126V # Test item 1-16- 2- 1

T AMCA: MESE 2271: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.942 Ohm, 18.2% >> MV = 0.172V, offset = 0.002V # Test item 1-16- 2- 2

T AMCA: MESE 2271: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.312 Ohm, 15.3% >> MV = 0.135V, offset = 0.002V # Test item 1-16- 2- 3

T AMCA: MESE 2271: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9850.502 Ohm, 14.9% >> MV = 1.849V, offset = -0.121V # Test item 1-16- 2- 4

T AMCA: MESE 2271: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.984 Ohm, 17.5% >> MV = 0.173V, offset = 0.002V # Test item 1-16- 2- 5

T AMCA: MESE 2271: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.437 Ohm, 12.5% >> MV = 0.135V, offset = 0.002V # Test item 1-16- 2- 6

T AMCA: MESE 2272: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9878.197 Ohm, 12.2% >> MV = 1.778V, offset = -0.198V # Test item 2-16- 2- 1

T AMCA: MESE 2272: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.648 Ohm, 23.3% >> MV = 0.172V, offset = 0.003V # Test item 2-16- 2- 2

T AMCA: MESE 2272: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.144 Ohm, 19.0% >> MV = 0.135V, offset = 0.003V # Test item 2-16- 2- 3

T AMCA: MESE 2272: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9880.715 Ohm, 11.9% >> MV = 1.785V, offset = -0.191V # Test item 2-16- 2- 4

T AMCA: MESE 2272: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.606 Ohm, 24.0% >> MV = 0.172V, offset = 0.003V # Test item 2-16- 2- 5

T AMCA: MESE 2272: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.186 Ohm, 18.1% >> MV = 0.135V, offset = 0.003V # Test item 2-16- 2- 6

T AMCA: MESE 2273: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9887.639 Ohm, 11.2% >> MV = 1.837V, offset = -0.140V # Test item 3-16- 2- 1

T AMCA: MESE 2273: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.194 Ohm, 13.9% >> MV = 0.172V, offset = 0.000V # Test item 3-16- 2- 2

T AMCA: MESE 2273: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.563 Ohm, 9.7% >> MV = 0.134V, offset = 0.000V # Test item 3-16- 2- 3

T AMCA: MESE 2273: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9892.044 Ohm, 10.8% >> MV = 1.834V, offset = -0.144V # Test item 3-16- 2- 4

T AMCA: MESE 2273: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.858 Ohm, 19.7% >> MV = 0.171V, offset = 0.000V # Test item 3-16- 2- 5

T AMCA: MESE 2273: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.479 Ohm, 11.6% >> MV = 0.133V, offset = -0.000V # Test item 3-16- 2- 6

T AMCA: MESE 2274: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9848.614 Ohm, 15.1% >> MV = 1.748V, offset = -0.222V # Test item 4-16- 2- 1

T AMCA: MESE 2274: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.732 Ohm, 21.9% >> MV = 0.172V, offset = 0.002V # Test item 4-16- 2- 2

T AMCA: MESE 2274: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.270 Ohm, 16.2% >> MV = 0.135V, offset = 0.002V # Test item 4-16- 2- 3

T AMCA: MESE 2274: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9851.761 Ohm, 14.8% >> MV = 1.749V, offset = -0.222V # Test item 4-16- 2- 4

T AMCA: MESE 2274: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.858 Ohm, 19.7% >> MV = 0.172V, offset = 0.002V # Test item 4-16- 2- 5

T AMCA: MESE 2274: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.270 Ohm, 16.2% >> MV = 0.135V, offset = 0.002V # Test item 4-16- 2- 6

T AMCA: MESE 2275: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9859.314 Ohm, 14.1% >> MV = 1.852V, offset = -0.120V # Test item 5-16- 2- 1

T AMCA: MESE 2275: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.774 Ohm, 21.1% >> MV = 0.176V, offset = 0.005V # Test item 5-16- 2- 2

T AMCA: MESE 2275: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.353 Ohm, 14.4% >> MV = 0.138V, offset = 0.005V # Test item 5-16- 2- 3

T AMCA: MESE 2275: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9860.573 Ohm, 13.9% >> MV = 1.851V, offset = -0.121V # Test item 5-16- 2- 4

T AMCA: MESE 2275: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.816 Ohm, 20.4% >> MV = 0.175V, offset = 0.005V # Test item 5-16- 2- 5

T AMCA: MESE 2275: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.353 Ohm, 14.4% >> MV = 0.138V, offset = 0.005V # Test item 5-16- 2- 6

T AMCA: MESE 2276: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9872.532 Ohm, 12.7% >> MV = 1.775V, offset = -0.200V # Test item 6-16- 2- 1

T AMCA: MESE 2276: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.355 Ohm, 28.4% >> MV = 0.173V, offset = 0.004V # Test item 6-16- 2- 2

T AMCA: MESE 2276: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 43.892 Ohm, 24.6% >> MV = 0.136V, offset = 0.004V # Test item 6-16- 2- 3

T AMCA: MESE 2276: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9876.938 Ohm, 12.3% >> MV = 1.774V, offset = -0.201V # Test item 6-16- 2- 4

T AMCA: MESE 2276: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.396 Ohm, 27.6% >> MV = 0.173V, offset = 0.004V # Test item 6-16- 2- 5

T AMCA: MESE 2276: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 43.976 Ohm, 22.8% >> MV = 0.137V, offset = 0.005V # Test item 6-16- 2- 6

T AMCA: MESE 2277: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9892.044 Ohm, 10.8% >> MV = 1.878V, offset = -0.101V # Test item 7-16- 2- 1

T AMCA: MESE 2277: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.942 Ohm, 18.2% >> MV = 0.175V, offset = 0.004V # Test item 7-16- 2- 2

T AMCA: MESE 2277: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.437 Ohm, 12.5% >> MV = 0.138V, offset = 0.005V # Test item 7-16- 2- 3

T AMCA: MESE 2277: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9880.715 Ohm, 11.9% >> MV = 1.871V, offset = -0.105V # Test item 7-16- 2- 4

T AMCA: MESE 2277: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.236 Ohm, 13.2% >> MV = 0.176V, offset = 0.004V # Test item 7-16- 2- 5

T AMCA: MESE 2277: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.605 Ohm, 8.8% >> MV = 0.138V, offset = 0.004V # Test item 7-16- 2- 6

T AMCA: MESE 2278: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9864.979 Ohm, 13.5% >> MV = 1.768V, offset = -0.205V # Test item 8-16- 2- 1

T AMCA: MESE 2278: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.103 Ohm, 32.7% >> MV = 0.173V, offset = 0.005V # Test item 8-16- 2- 2

T AMCA: MESE 2278: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 43.640 Ohm, 30.2% >> MV = 0.136V, offset = 0.005V # Test item 8-16- 2- 3

T AMCA: MESE 2278: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9871.902 Ohm, 12.8% >> MV = 1.754V, offset = -0.221V # Test item 8-16- 2- 4

T AMCA: MESE 2278: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.103 Ohm, 32.7% >> MV = 0.173V, offset = 0.005V # Test item 8-16- 2- 5

T AMCA: MESE 2278: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 43.808 Ohm, 26.5% >> MV = 0.136V, offset = 0.005V # Test item 8-16- 2- 6

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.9% >> pos = 0.069V, neg = 0.071V # Test item 1-16- 3- 1

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.237 V, 11.6% >> pos = 1.690V, neg = -1.547V # Test item 1-16- 3- 2

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.4% >> pos = 0.044V, neg = 0.046V # Test item 1-16- 3- 3

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.598 V, 1.4% >> pos = 0.845V, neg = -0.753V # Test item 1-16- 3- 4

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.2% >> pos = 0.032V, neg = 0.034V # Test item 1-16- 3- 5

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.800 V, 0.3% >> pos = 0.435V, neg = -0.365V # Test item 1-16- 3- 6

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.9% >> pos = 0.023V, neg = 0.025V # Test item 1-16- 3- 7

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.252 V, 8.6% >> pos = 0.154V, neg = -0.099V # Test item 1-16- 3- 8

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.1% >> pos = 0.024V, neg = 0.022V # Test item 1-16- 3- 9

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.101 V, 3.0% >> pos = 0.074V, neg = -0.026V # Test item 1-16- 3-10

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.006 V, 5.8% >> pos = 0.025V, neg = 0.031V # Test item 1-16- 3-11

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.179 V, 6.5% >> pos = 1.616V, neg = -1.563V # Test item 1-16- 3-12

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.6% >> pos = 0.015V, neg = 0.014V # Test item 1-16- 3-13

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.584 V, 10.3% >> pos = 0.797V, neg = -0.787V # Test item 1-16- 3-14

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.6% >> pos = -0.008V, neg = -0.006V # Test item 1-16- 3-15

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.793 V, 8.3% >> pos = 0.390V, neg = -0.403V # Test item 1-16- 3-16

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = -0.015V, neg = -0.015V # Test item 1-16- 3-17

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 1.4% >> pos = 0.110V, neg = -0.140V # Test item 1-16- 3-18

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = -0.019V, neg = -0.018V # Test item 1-16- 3-19

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.101 V, 3.3% >> pos = 0.032V, neg = -0.068V # Test item 1-16- 3-20

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = 0.072V, neg = 0.073V # Test item 1-16- 3-21

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.240 V, 12.6% >> pos = 1.689V, neg = -1.551V # Test item 1-16- 3-22

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.1% >> pos = 0.044V, neg = 0.046V # Test item 1-16- 3-23

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.597 V, 1.6% >> pos = 0.845V, neg = -0.752V # Test item 1-16- 3-24

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.4% >> pos = 0.034V, neg = 0.032V # Test item 1-16- 3-25

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.802 V, 1.9% >> pos = 0.436V, neg = -0.366V # Test item 1-16- 3-26

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = 0.027V, neg = 0.026V # Test item 1-16- 3-27

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 2.1% >> pos = 0.152V, neg = -0.098V # Test item 1-16- 3-28

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.6% >> pos = 0.024V, neg = 0.022V # Test item 1-16- 3-29

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.101 V, 4.2% >> pos = 0.074V, neg = -0.027V # Test item 1-16- 3-30

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.7% >> pos = 0.030V, neg = 0.032V # Test item 1-16- 3-31

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.171 V, 9.1% >> pos = 1.615V, neg = -1.556V # Test item 1-16- 3-32

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.8% >> pos = 0.009V, neg = 0.004V # Test item 1-16- 3-33

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.585 V, 9.3% >> pos = 0.798V, neg = -0.787V # Test item 1-16- 3-34

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = -0.008V, neg = -0.007V # Test item 1-16- 3-35

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.795 V, 6.6% >> pos = 0.391V, neg = -0.403V # Test item 1-16- 3-36

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = -0.015V, neg = -0.016V # Test item 1-16- 3-37

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 3.7% >> pos = 0.110V, neg = -0.139V # Test item 1-16- 3-38

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.005 V, 5.0% >> pos = -0.022V, neg = -0.017V # Test item 1-16- 3-39

T AMCA: MESE 2271: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.2% >> pos = 0.032V, neg = -0.068V # Test item 1-16- 3-40

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.5% >> pos = 0.051V, neg = 0.054V # Test item 2-16- 3- 1

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.228 V, 8.9% >> pos = 1.668V, neg = -1.560V # Test item 2-16- 3- 2

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.040V, neg = 0.040V # Test item 2-16- 3- 3

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.588 V, 7.3% >> pos = 0.834V, neg = -0.755V # Test item 2-16- 3- 4

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = 0.037V, neg = 0.036V # Test item 2-16- 3- 5

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.800 V, 0.1% >> pos = 0.436V, neg = -0.365V # Test item 2-16- 3- 6

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.7% >> pos = 0.034V, neg = 0.037V # Test item 2-16- 3- 7

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 0.3% >> pos = 0.159V, neg = -0.091V # Test item 2-16- 3- 8

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.032V, neg = 0.032V # Test item 2-16- 3- 9

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.2% >> pos = 0.082V, neg = -0.018V # Test item 2-16- 3-10

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = -0.012V, neg = -0.012V # Test item 2-16- 3-11

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.200 V, 0.1% >> pos = 1.591V, neg = -1.610V # Test item 2-16- 3-12

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = -0.007V, neg = -0.007V # Test item 2-16- 3-13

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.589 V, 7.2% >> pos = 0.774V, neg = -0.814V # Test item 2-16- 3-14

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.1% >> pos = -0.023V, neg = -0.026V # Test item 2-16- 3-15

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.796 V, 4.9% >> pos = 0.372V, neg = -0.425V # Test item 2-16- 3-16

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.5% >> pos = -0.028V, neg = -0.026V # Test item 2-16- 3-17

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 3.5% >> pos = 0.098V, neg = -0.153V # Test item 2-16- 3-18

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = -0.029V, neg = -0.029V # Test item 2-16- 3-19

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.6% >> pos = 0.021V, neg = -0.079V # Test item 2-16- 3-20

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.0% >> pos = 0.051V, neg = 0.049V # Test item 2-16- 3-21

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.231 V, 9.7% >> pos = 1.669V, neg = -1.562V # Test item 2-16- 3-22

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.040V, neg = 0.040V # Test item 2-16- 3-23

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.595 V, 2.9% >> pos = 0.838V, neg = -0.757V # Test item 2-16- 3-24

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.7% >> pos = 0.035V, neg = 0.037V # Test item 2-16- 3-25

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.802 V, 2.5% >> pos = 0.437V, neg = -0.365V # Test item 2-16- 3-26

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = 0.033V, neg = 0.032V # Test item 2-16- 3-27

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 2.4% >> pos = 0.159V, neg = -0.092V # Test item 2-16- 3-28

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.031V, neg = 0.032V # Test item 2-16- 3-29

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 11.8% >> pos = 0.080V, neg = -0.018V # Test item 2-16- 3-30

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.4% >> pos = -0.010V, neg = -0.007V # Test item 2-16- 3-31

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.197 V, 1.0% >> pos = 1.588V, neg = -1.608V # Test item 2-16- 3-32

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = -0.018V, neg = -0.019V # Test item 2-16- 3-33

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.575 V, 15.6% >> pos = 0.773V, neg = -0.802V # Test item 2-16- 3-34

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.4% >> pos = -0.026V, neg = -0.022V # Test item 2-16- 3-35

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.798 V, 2.9% >> pos = 0.371V, neg = -0.426V # Test item 2-16- 3-36

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = -0.028V, neg = -0.028V # Test item 2-16- 3-37

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 8.6% >> pos = 0.096V, neg = -0.152V # Test item 2-16- 3-38

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.9% >> pos = -0.030V, neg = -0.029V # Test item 2-16- 3-39

T AMCA: MESE 2272: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.1% >> pos = 0.023V, neg = -0.077V # Test item 2-16- 3-40

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.6% >> pos = 0.084V, neg = 0.081V # Test item 3-16- 3- 1

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.195 V, 1.7% >> pos = 1.677V, neg = -1.517V # Test item 3-16- 3- 2

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = 0.054V, neg = 0.053V # Test item 3-16- 3- 3

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.591 V, 5.5% >> pos = 0.848V, neg = -0.744V # Test item 3-16- 3- 4

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = 0.039V, neg = 0.039V # Test item 3-16- 3- 5

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.795 V, 6.0% >> pos = 0.435V, neg = -0.360V # Test item 3-16- 3- 6

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = 0.031V, neg = 0.030V # Test item 3-16- 3- 7

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 12.8% >> pos = 0.156V, neg = -0.091V # Test item 3-16- 3- 8

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.028V, neg = 0.028V # Test item 3-16- 3- 9

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.108 V, 42.2% >> pos = 0.087V, neg = -0.022V # Test item 3-16- 3-10

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.1% >> pos = 0.032V, neg = 0.034V # Test item 3-16- 3-11

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.240 V, 12.6% >> pos = 1.652V, neg = -1.588V # Test item 3-16- 3-12

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.016V, neg = 0.016V # Test item 3-16- 3-13

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.597 V, 1.8% >> pos = 0.802V, neg = -0.795V # Test item 3-16- 3-14

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.7% >> pos = -0.008V, neg = -0.006V # Test item 3-16- 3-15

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.801 V, 1.7% >> pos = 0.391V, neg = -0.410V # Test item 3-16- 3-16

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = -0.016V, neg = -0.015V # Test item 3-16- 3-17

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 7.6% >> pos = 0.107V, neg = -0.141V # Test item 3-16- 3-18

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.6% >> pos = -0.020V, neg = -0.026V # Test item 3-16- 3-19

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.6% >> pos = 0.029V, neg = -0.070V # Test item 3-16- 3-20

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.4% >> pos = 0.081V, neg = 0.076V # Test item 3-16- 3-21

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.196 V, 1.2% >> pos = 1.679V, neg = -1.517V # Test item 3-16- 3-22

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.007 V, 6.7% >> pos = 0.044V, neg = 0.050V # Test item 3-16- 3-23

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.589 V, 6.7% >> pos = 0.843V, neg = -0.747V # Test item 3-16- 3-24

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = 0.039V, neg = 0.038V # Test item 3-16- 3-25

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.799 V, 1.0% >> pos = 0.436V, neg = -0.363V # Test item 3-16- 3-26

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.031V, neg = 0.030V # Test item 3-16- 3-27

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 12.5% >> pos = 0.153V, neg = -0.094V # Test item 3-16- 3-28

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.027V, neg = 0.027V # Test item 3-16- 3-29

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.101 V, 4.5% >> pos = 0.078V, neg = -0.023V # Test item 3-16- 3-30

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.7% >> pos = 0.034V, neg = 0.030V # Test item 3-16- 3-31

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.237 V, 11.7% >> pos = 1.652V, neg = -1.585V # Test item 3-16- 3-32

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = 0.005V, neg = 0.004V # Test item 3-16- 3-33

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.603 V, 1.9% >> pos = 0.805V, neg = -0.798V # Test item 3-16- 3-34

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = -0.008V, neg = -0.009V # Test item 3-16- 3-35

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.800 V, 0.1% >> pos = 0.393V, neg = -0.407V # Test item 3-16- 3-36

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = -0.016V, neg = -0.016V # Test item 3-16- 3-37

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.255 V, 18.1% >> pos = 0.108V, neg = -0.146V # Test item 3-16- 3-38

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.6% >> pos = -0.011V, neg = -0.019V # Test item 3-16- 3-39

T AMCA: MESE 2273: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 4.5% >> pos = 0.029V, neg = -0.070V # Test item 3-16- 3-40

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.1% >> pos = 0.085V, neg = 0.087V # Test item 4-16- 3- 1

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.239 V, 12.2% >> pos = 1.710V, neg = -1.529V # Test item 4-16- 3- 2

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% >> pos = 0.061V, neg = 0.060V # Test item 4-16- 3- 3

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.605 V, 3.4% >> pos = 0.862V, neg = -0.743V # Test item 4-16- 3- 4

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.046V, neg = 0.047V # Test item 4-16- 3- 5

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.807 V, 9.2% >> pos = 0.450V, neg = -0.358V # Test item 4-16- 3- 6

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = 0.038V, neg = 0.040V # Test item 4-16- 3- 7

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.252 V, 8.7% >> pos = 0.165V, neg = -0.087V # Test item 4-16- 3- 8

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.036V, neg = 0.035V # Test item 4-16- 3- 9

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.101 V, 4.3% >> pos = 0.086V, neg = -0.015V # Test item 4-16- 3-10

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = 0.019V, neg = 0.020V # Test item 4-16- 3-11

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.248 V, 14.9% >> pos = 1.647V, neg = -1.601V # Test item 4-16- 3-12

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.6% >> pos = 0.013V, neg = 0.011V # Test item 4-16- 3-13

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.613 V, 7.8% >> pos = 0.801V, neg = -0.811V # Test item 4-16- 3-14

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.4% >> pos = -0.020V, neg = -0.017V # Test item 4-16- 3-15

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.806 V, 6.9% >> pos = 0.384V, neg = -0.422V # Test item 4-16- 3-16

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% >> pos = -0.026V, neg = -0.026V # Test item 4-16- 3-17

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 4.4% >> pos = 0.100V, neg = -0.151V # Test item 4-16- 3-18

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = -0.029V, neg = -0.029V # Test item 4-16- 3-19

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.101 V, 2.7% >> pos = 0.022V, neg = -0.079V # Test item 4-16- 3-20

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.6% >> pos = 0.087V, neg = 0.086V # Test item 4-16- 3-21

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.242 V, 13.1% >> pos = 1.709V, neg = -1.533V # Test item 4-16- 3-22

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.060V, neg = 0.059V # Test item 4-16- 3-23

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.601 V, 0.5% >> pos = 0.865V, neg = -0.735V # Test item 4-16- 3-24

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = 0.046V, neg = 0.046V # Test item 4-16- 3-25

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.808 V, 10.4% >> pos = 0.450V, neg = -0.359V # Test item 4-16- 3-26

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = 0.039V, neg = 0.038V # Test item 4-16- 3-27

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 4.8% >> pos = 0.164V, neg = -0.088V # Test item 4-16- 3-28

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = 0.036V, neg = 0.035V # Test item 4-16- 3-29

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.6% >> pos = 0.086V, neg = -0.015V # Test item 4-16- 3-30

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.1% >> pos = 0.023V, neg = 0.017V # Test item 4-16- 3-31

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.250 V, 15.6% >> pos = 1.645V, neg = -1.605V # Test item 4-16- 3-32

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.3% >> pos = -0.002V, neg = -0.006V # Test item 4-16- 3-33

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.615 V, 9.5% >> pos = 0.802V, neg = -0.813V # Test item 4-16- 3-34

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.5% >> pos = -0.019V, neg = -0.018V # Test item 4-16- 3-35

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.809 V, 11.2% >> pos = 0.387V, neg = -0.422V # Test item 4-16- 3-36

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.9% >> pos = -0.027V, neg = -0.026V # Test item 4-16- 3-37

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.256 V, 25.3% >> pos = 0.105V, neg = -0.151V # Test item 4-16- 3-38

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = -0.029V, neg = -0.029V # Test item 4-16- 3-39

T AMCA: MESE 2274: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.101 V, 7.4% >> pos = 0.022V, neg = -0.079V # Test item 4-16- 3-40

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.6% >> pos = 0.069V, neg = 0.071V # Test item 5-16- 3- 1

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.221 V, 6.6% >> pos = 1.682V, neg = -1.539V # Test item 5-16- 3- 2

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% >> pos = 0.047V, neg = 0.045V # Test item 5-16- 3- 3

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.592 V, 4.8% >> pos = 0.840V, neg = -0.753V # Test item 5-16- 3- 4

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = 0.034V, neg = 0.033V # Test item 5-16- 3- 5

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.800 V, 0.2% >> pos = 0.433V, neg = -0.367V # Test item 5-16- 3- 6

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.014 V, 14.1% >> pos = 0.026V, neg = 0.012V # Test item 5-16- 3- 7

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 8.6% >> pos = 0.149V, neg = -0.099V # Test item 5-16- 3- 8

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.021V, neg = 0.021V # Test item 5-16- 3- 9

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 8.3% >> pos = 0.071V, neg = -0.027V # Test item 5-16- 3-10

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.8% >> pos = 0.035V, neg = 0.032V # Test item 5-16- 3-11

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.170 V, 9.3% >> pos = 1.621V, neg = -1.550V # Test item 5-16- 3-12

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.5% >> pos = 0.017V, neg = 0.017V # Test item 5-16- 3-13

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.578 V, 13.6% >> pos = 0.797V, neg = -0.781V # Test item 5-16- 3-14

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.0% >> pos = 0.000V, neg = -0.004V # Test item 5-16- 3-15

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.796 V, 4.8% >> pos = 0.397V, neg = -0.399V # Test item 5-16- 3-16

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.5% >> pos = -0.013V, neg = -0.011V # Test item 5-16- 3-17

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 9.3% >> pos = 0.112V, neg = -0.135V # Test item 5-16- 3-18

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% >> pos = -0.013V, neg = -0.015V # Test item 5-16- 3-19

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 7.5% >> pos = 0.036V, neg = -0.063V # Test item 5-16- 3-20

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.070V, neg = 0.070V # Test item 5-16- 3-21

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.227 V, 8.5% >> pos = 1.685V, neg = -1.542V # Test item 5-16- 3-22

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.6% >> pos = 0.044V, neg = 0.045V # Test item 5-16- 3-23

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.592 V, 4.8% >> pos = 0.841V, neg = -0.752V # Test item 5-16- 3-24

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = 0.033V, neg = 0.033V # Test item 5-16- 3-25

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.800 V, 0.2% >> pos = 0.433V, neg = -0.367V # Test item 5-16- 3-26

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.5% >> pos = 0.024V, neg = 0.025V # Test item 5-16- 3-27

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 4.4% >> pos = 0.148V, neg = -0.101V # Test item 5-16- 3-28

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.023V, neg = 0.023V # Test item 5-16- 3-29

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 2.6% >> pos = 0.071V, neg = -0.029V # Test item 5-16- 3-30

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.4% >> pos = 0.035V, neg = 0.031V # Test item 5-16- 3-31

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.165 V, 11.0% >> pos = 1.616V, neg = -1.549V # Test item 5-16- 3-32

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.006 V, 6.5% >> pos = 0.009V, neg = 0.016V # Test item 5-16- 3-33

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 20.2% >> pos = 0.783V, neg = -0.784V # Test item 5-16- 3-34

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = -0.002V, neg = -0.002V # Test item 5-16- 3-35

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.797 V, 4.0% >> pos = 0.396V, neg = -0.400V # Test item 5-16- 3-36

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.2% >> pos = -0.012V, neg = -0.011V # Test item 5-16- 3-37

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.246 V, 14.7% >> pos = 0.111V, neg = -0.136V # Test item 5-16- 3-38

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.0% >> pos = -0.014V, neg = -0.013V # Test item 5-16- 3-39

T AMCA: MESE 2275: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 3.2% >> pos = 0.035V, neg = -0.064V # Test item 5-16- 3-40

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.0% >> pos = 0.058V, neg = 0.054V # Test item 6-16- 3- 1

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.200 V, 0.1% >> pos = 1.657V, neg = -1.544V # Test item 6-16- 3- 2

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.6% >> pos = 0.044V, neg = 0.041V # Test item 6-16- 3- 3

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.597 V, 1.6% >> pos = 0.843V, neg = -0.755V # Test item 6-16- 3- 4

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.5% >> pos = 0.035V, neg = 0.034V # Test item 6-16- 3- 5

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.795 V, 6.1% >> pos = 0.433V, neg = -0.362V # Test item 6-16- 3- 6

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% >> pos = 0.033V, neg = 0.031V # Test item 6-16- 3- 7

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.252 V, 7.0% >> pos = 0.156V, neg = -0.096V # Test item 6-16- 3- 8

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = 0.030V, neg = 0.030V # Test item 6-16- 3- 9

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.097 V, 16.5% >> pos = 0.078V, neg = -0.019V # Test item 6-16- 3-10

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 10.0% >> pos = 0.009V, neg = -0.001V # Test item 6-16- 3-11

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.246 V, 14.5% >> pos = 1.622V, neg = -1.625V # Test item 6-16- 3-12

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.3% >> pos = 0.001V, neg = -0.001V # Test item 6-16- 3-13

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.600 V, 0.0% >> pos = 0.786V, neg = -0.814V # Test item 6-16- 3-14

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.5% >> pos = -0.018V, neg = -0.018V # Test item 6-16- 3-15

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.804 V, 4.8% >> pos = 0.383V, neg = -0.421V # Test item 6-16- 3-16

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = -0.025V, neg = -0.025V # Test item 6-16- 3-17

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 3.3% >> pos = 0.100V, neg = -0.149V # Test item 6-16- 3-18

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = -0.026V, neg = -0.027V # Test item 6-16- 3-19

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.0% >> pos = 0.023V, neg = -0.077V # Test item 6-16- 3-20

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.9% >> pos = 0.060V, neg = 0.055V # Test item 6-16- 3-21

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.205 V, 1.4% >> pos = 1.658V, neg = -1.546V # Test item 6-16- 3-22

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.041V, neg = 0.041V # Test item 6-16- 3-23

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.591 V, 5.5% >> pos = 0.837V, neg = -0.754V # Test item 6-16- 3-24

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.035V, neg = 0.035V # Test item 6-16- 3-25

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.798 V, 2.4% >> pos = 0.435V, neg = -0.363V # Test item 6-16- 3-26

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.031V, neg = 0.031V # Test item 6-16- 3-27

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 6.8% >> pos = 0.154V, neg = -0.094V # Test item 6-16- 3-28

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.0% >> pos = 0.028V, neg = 0.029V # Test item 6-16- 3-29

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.4% >> pos = 0.079V, neg = -0.021V # Test item 6-16- 3-30

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.7% >> pos = 0.000V, neg = 0.002V # Test item 6-16- 3-31

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.251 V, 15.8% >> pos = 1.626V, neg = -1.625V # Test item 6-16- 3-32

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% >> pos = -0.015V, neg = -0.017V # Test item 6-16- 3-33

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.609 V, 5.8% >> pos = 0.792V, neg = -0.817V # Test item 6-16- 3-34

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.9% >> pos = -0.018V, neg = -0.023V # Test item 6-16- 3-35

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.804 V, 5.2% >> pos = 0.382V, neg = -0.422V # Test item 6-16- 3-36

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.1% >> pos = -0.024V, neg = -0.026V # Test item 6-16- 3-37

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 8.5% >> pos = 0.099V, neg = -0.149V # Test item 6-16- 3-38

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = -0.026V, neg = -0.027V # Test item 6-16- 3-39

T AMCA: MESE 2276: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.091 V, 45.8% >> pos = 0.025V, neg = -0.065V # Test item 6-16- 3-40

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.2% >> pos = 0.068V, neg = 0.063V # Test item 7-16- 3- 1

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.178 V, 6.9% >> pos = 1.653V, neg = -1.525V # Test item 7-16- 3- 2

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.005 V, 4.8% >> pos = 0.037V, neg = 0.042V # Test item 7-16- 3- 3

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.585 V, 9.4% >> pos = 0.834V, neg = -0.751V # Test item 7-16- 3- 4

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.030V, neg = 0.029V # Test item 7-16- 3- 5

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.796 V, 5.2% >> pos = 0.427V, neg = -0.369V # Test item 7-16- 3- 6

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.018 V, 18.5% >> pos = 0.019V, neg = 0.038V # Test item 7-16- 3- 7

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 3.9% >> pos = 0.147V, neg = -0.102V # Test item 7-16- 3- 8

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.020V, neg = 0.019V # Test item 7-16- 3- 9

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.0% >> pos = 0.069V, neg = -0.031V # Test item 7-16- 3-10

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.1% >> pos = 0.038V, neg = 0.029V # Test item 7-16- 3-11

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.238 V, 12.0% >> pos = 1.651V, neg = -1.587V # Test item 7-16- 3-12

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = 0.015V, neg = 0.015V # Test item 7-16- 3-13

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.598 V, 1.3% >> pos = 0.806V, neg = -0.792V # Test item 7-16- 3-14

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.4% >> pos = -0.001V, neg = -0.004V # Test item 7-16- 3-15

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.801 V, 1.3% >> pos = 0.398V, neg = -0.403V # Test item 7-16- 3-16

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.0% >> pos = -0.012V, neg = -0.011V # Test item 7-16- 3-17

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 1.1% >> pos = 0.115V, neg = -0.136V # Test item 7-16- 3-18

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.5% >> pos = -0.013V, neg = -0.014V # Test item 7-16- 3-19

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.7% >> pos = 0.037V, neg = -0.063V # Test item 7-16- 3-20

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.063V, neg = 0.064V # Test item 7-16- 3-21

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.179 V, 6.5% >> pos = 1.653V, neg = -1.526V # Test item 7-16- 3-22

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.0% >> pos = 0.045V, neg = 0.043V # Test item 7-16- 3-23

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.587 V, 8.1% >> pos = 0.834V, neg = -0.753V # Test item 7-16- 3-24

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.1% >> pos = 0.031V, neg = 0.027V # Test item 7-16- 3-25

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.797 V, 4.0% >> pos = 0.428V, neg = -0.369V # Test item 7-16- 3-26

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% >> pos = 0.025V, neg = 0.023V # Test item 7-16- 3-27

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 6.8% >> pos = 0.147V, neg = -0.101V # Test item 7-16- 3-28

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.5% >> pos = 0.020V, neg = 0.019V # Test item 7-16- 3-29

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 17.6% >> pos = 0.071V, neg = -0.033V # Test item 7-16- 3-30

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.2% >> pos = 0.033V, neg = 0.028V # Test item 7-16- 3-31

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.246 V, 14.3% >> pos = 1.652V, neg = -1.594V # Test item 7-16- 3-32

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.6% >> pos = 0.004V, neg = 0.006V # Test item 7-16- 3-33

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.605 V, 3.3% >> pos = 0.810V, neg = -0.796V # Test item 7-16- 3-34

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = -0.003V, neg = -0.004V # Test item 7-16- 3-35

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.804 V, 5.0% >> pos = 0.397V, neg = -0.407V # Test item 7-16- 3-36

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = -0.012V, neg = -0.010V # Test item 7-16- 3-37

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 3.8% >> pos = 0.115V, neg = -0.136V # Test item 7-16- 3-38

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.6% >> pos = -0.014V, neg = -0.013V # Test item 7-16- 3-39

T AMCA: MESE 2277: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.101 V, 5.7% >> pos = 0.037V, neg = -0.064V # Test item 7-16- 3-40

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.080V, neg = 0.080V # Test item 8-16- 3- 1

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.244 V, 13.8% >> pos = 1.703V, neg = -1.542V # Test item 8-16- 3- 2

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.0% >> pos = 0.054V, neg = 0.056V # Test item 8-16- 3- 3

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.604 V, 2.3% >> pos = 0.859V, neg = -0.744V # Test item 8-16- 3- 4

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.5% >> pos = 0.047V, neg = 0.046V # Test item 8-16- 3- 5

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.798 V, 2.8% >> pos = 0.446V, neg = -0.351V # Test item 8-16- 3- 6

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = 0.036V, neg = 0.037V # Test item 8-16- 3- 7

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.253 V, 10.2% >> pos = 0.161V, neg = -0.091V # Test item 8-16- 3- 8

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.8% >> pos = 0.035V, neg = 0.038V # Test item 8-16- 3- 9

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 8.8% >> pos = 0.084V, neg = -0.015V # Test item 8-16- 3-10

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.019V, neg = 0.019V # Test item 8-16- 3-11

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.158 V, 13.2% >> pos = 1.599V, neg = -1.559V # Test item 8-16- 3-12

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.5% >> pos = 0.007V, neg = 0.009V # Test item 8-16- 3-13

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.578 V, 13.5% >> pos = 0.783V, neg = -0.796V # Test item 8-16- 3-14

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.4% >> pos = -0.014V, neg = -0.016V # Test item 8-16- 3-15

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.792 V, 9.6% >> pos = 0.380V, neg = -0.412V # Test item 8-16- 3-16

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.007 V, 6.6% >> pos = -0.031V, neg = -0.024V # Test item 8-16- 3-17

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 11.7% >> pos = 0.100V, neg = -0.147V # Test item 8-16- 3-18

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.1% >> pos = -0.027V, neg = -0.025V # Test item 8-16- 3-19

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 8.4% >> pos = 0.023V, neg = -0.075V # Test item 8-16- 3-20

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = 0.081V, neg = 0.081V # Test item 8-16- 3-21

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.239 V, 12.2% >> pos = 1.701V, neg = -1.538V # Test item 8-16- 3-22

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.7% >> pos = 0.055V, neg = 0.047V # Test item 8-16- 3-23

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.606 V, 3.9% >> pos = 0.855V, neg = -0.751V # Test item 8-16- 3-24

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = 0.046V, neg = 0.048V # Test item 8-16- 3-25

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.804 V, 5.2% >> pos = 0.449V, neg = -0.355V # Test item 8-16- 3-26

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.039V, neg = 0.039V # Test item 8-16- 3-27

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 2.1% >> pos = 0.162V, neg = -0.088V # Test item 8-16- 3-28

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% >> pos = 0.035V, neg = 0.035V # Test item 8-16- 3-29

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.107 V, 35.8% >> pos = 0.092V, neg = -0.015V # Test item 8-16- 3-30

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.013 V, 12.9% >> pos = 0.015V, neg = 0.027V # Test item 8-16- 3-31

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.164 V, 11.2% >> pos = 1.605V, neg = -1.559V # Test item 8-16- 3-32

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = -0.004V, neg = -0.004V # Test item 8-16- 3-33

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.584 V, 10.2% >> pos = 0.788V, neg = -0.796V # Test item 8-16- 3-34

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.9% >> pos = -0.015V, neg = -0.014V # Test item 8-16- 3-35

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.795 V, 6.3% >> pos = 0.385V, neg = -0.410V # Test item 8-16- 3-36

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = -0.025V, neg = -0.025V # Test item 8-16- 3-37

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 5.2% >> pos = 0.101V, neg = -0.148V # Test item 8-16- 3-38

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.026V, neg = -0.026V # Test item 8-16- 3-39

T AMCA: MESE 2278: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.103 V, 13.4% >> pos = 0.025V, neg = -0.077V # Test item 8-16- 3-40

T AMCA: MESE 2271: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.786 V, 14.0% >> POS = 0.828V, NEG = 0.042V # Test item 1-16- 4- 1

T AMCA: MESE 2271: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.766 V, 33.5% >> POS = 0.809V, NEG = 0.043V # Test item 1-16- 4- 2

T AMCA: MESE 2271: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.749 V, 50.5% >> POS = 0.781V, NEG = 0.032V # Test item 1-16- 4- 3

T AMCA: MESE 2271: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.798 V, 1.7% >> POS = 0.846V, NEG = 0.047V # Test item 1-16- 4- 4

T AMCA: MESE 2272: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.789 V, 10.6% >> POS = 0.827V, NEG = 0.037V # Test item 2-16- 4- 1

T AMCA: MESE 2272: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.765 V, 35.2% >> POS = 0.803V, NEG = 0.039V # Test item 2-16- 4- 2

T AMCA: MESE 2272: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.750 V, 50.4% >> POS = 0.775V, NEG = 0.026V # Test item 2-16- 4- 3

T AMCA: MESE 2272: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.800 V, 0.5% >> POS = 0.841V, NEG = 0.041V # Test item 2-16- 4- 4

T AMCA: MESE 2273: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.784 V, 16.3% >> POS = 0.830V, NEG = 0.046V # Test item 3-16- 4- 1

T AMCA: MESE 2273: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.766 V, 34.3% >> POS = 0.812V, NEG = 0.046V # Test item 3-16- 4- 2

T AMCA: MESE 2273: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.745 V, 55.0% >> POS = 0.782V, NEG = 0.037V # Test item 3-16- 4- 3

T AMCA: MESE 2273: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.798 V, 2.2% >> POS = 0.849V, NEG = 0.051V # Test item 3-16- 4- 4

T AMCA: MESE 2274: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.791 V, 8.8% >> POS = 0.847V, NEG = 0.056V # Test item 4-16- 4- 1

T AMCA: MESE 2274: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.772 V, 28.4% >> POS = 0.828V, NEG = 0.056V # Test item 4-16- 4- 2

T AMCA: MESE 2274: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.752 V, 48.4% >> POS = 0.796V, NEG = 0.045V # Test item 4-16- 4- 3

T AMCA: MESE 2274: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.805 V, 4.6% >> POS = 0.862V, NEG = 0.057V # Test item 4-16- 4- 4

T AMCA: MESE 2275: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.783 V, 16.7% >> POS = 0.823V, NEG = 0.040V # Test item 5-16- 4- 1

T AMCA: MESE 2275: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.764 V, 36.4% >> POS = 0.804V, NEG = 0.041V # Test item 5-16- 4- 2

T AMCA: MESE 2275: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.747 V, 53.1% >> POS = 0.776V, NEG = 0.029V # Test item 5-16- 4- 3

T AMCA: MESE 2275: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.791 V, 9.4% >> POS = 0.838V, NEG = 0.047V # Test item 5-16- 4- 4

T AMCA: MESE 2276: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.782 V, 17.7% >> POS = 0.819V, NEG = 0.037V # Test item 6-16- 4- 1

T AMCA: MESE 2276: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.766 V, 33.8% >> POS = 0.803V, NEG = 0.037V # Test item 6-16- 4- 2

T AMCA: MESE 2276: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.742 V, 57.8% >> POS = 0.766V, NEG = 0.023V # Test item 6-16- 4- 3

T AMCA: MESE 2276: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.805 V, 5.4% >> POS = 0.837V, NEG = 0.031V # Test item 6-16- 4- 4

T AMCA: MESE 2277: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.777 V, 23.3% >> POS = 0.814V, NEG = 0.037V # Test item 7-16- 4- 1

T AMCA: MESE 2277: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.760 V, 39.8% >> POS = 0.796V, NEG = 0.036V # Test item 7-16- 4- 2

T AMCA: MESE 2277: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.741 V, 58.9% >> POS = 0.767V, NEG = 0.026V # Test item 7-16- 4- 3

T AMCA: MESE 2277: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.796 V, 4.4% >> POS = 0.834V, NEG = 0.038V # Test item 7-16- 4- 4

T AMCA: MESE 2278: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.792 V, 7.6% >> POS = 0.837V, NEG = 0.045V # Test item 8-16- 4- 1

T AMCA: MESE 2278: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.771 V, 28.8% >> POS = 0.823V, NEG = 0.052V # Test item 8-16- 4- 2

T AMCA: MESE 2278: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.747 V, 53.2% >> POS = 0.786V, NEG = 0.039V # Test item 8-16- 4- 3

T AMCA: MESE 2278: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.803 V, 2.9% >> POS = 0.857V, NEG = 0.054V # Test item 8-16- 4- 4

T AMCA: MESE 2271: Signal\_Input/Output\_relay\_test\_Resistance\_A+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5013.497 Ohm, 2.1% >> vOffset = -0.077V, vMeas = 2.430V, iForce = 0.500mA # Test item 1-20- 1- 1

T AMCA: MESE 2271: Signal\_Input/Output\_relay\_test\_Resistance\_A-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5012.237 Ohm, 2.3% >> vOffset = -0.077V, vMeas = 2.429V, iForce = 0.500mA # Test item 1-20- 1- 2

T AMCA: MESE 2271: Signal\_Input/Output\_relay\_test\_Resistance\_AA+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1003.052 Ohm, 3.1% >> vOffset = -0.011V, vMeas = 0.490V, iForce = 0.500mA # Test item 1-20- 1- 3

T AMCA: MESE 2271: Signal\_Input/Output\_relay\_test\_Resistance\_AA-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1004.311 Ohm, 4.3% >> vOffset = -0.011V, vMeas = 0.491V, iForce = 0.500mA # Test item 1-20- 1- 4

T AMCA: MESE 2272: Signal\_Input/Output\_relay\_test\_Resistance\_B+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5020.546 Ohm, 0.7% >> vOffset = -0.113V, vMeas = 2.397V, iForce = 0.500mA # Test item 2-20- 1- 1

T AMCA: MESE 2272: Signal\_Input/Output\_relay\_test\_Resistance\_B-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5021.553 Ohm, 0.5% >> vOffset = -0.113V, vMeas = 2.398V, iForce = 0.500mA # Test item 2-20- 1- 2

T AMCA: MESE 2272: Signal\_Input/Output\_relay\_test\_Resistance\_BB+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1000.282 Ohm, 0.3% >> vOffset = -0.017V, vMeas = 0.483V, iForce = 0.500mA # Test item 2-20- 1- 3

T AMCA: MESE 2272: Signal\_Input/Output\_relay\_test\_Resistance\_BB-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1001.038 Ohm, 1.0% >> vOffset = -0.018V, vMeas = 0.483V, iForce = 0.500mA # Test item 2-20- 1- 4

T AMCA: MESE 2273: Signal\_Input/Output\_relay\_test\_Resistance\_C+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5027.092 Ohm, 0.6% >> vOffset = -0.086V, vMeas = 2.427V, iForce = 0.500mA # Test item 3-20- 1- 1

T AMCA: MESE 2273: Signal\_Input/Output\_relay\_test\_Resistance\_C-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5025.582 Ohm, 0.3% >> vOffset = -0.086V, vMeas = 2.427V, iForce = 0.500mA # Test item 3-20- 1- 2

T AMCA: MESE 2273: Signal\_Input/Output\_relay\_test\_Resistance\_CC+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1004.059 Ohm, 4.1% >> vOffset = -0.014V, vMeas = 0.488V, iForce = 0.500mA # Test item 3-20- 1- 3

T AMCA: MESE 2273: Signal\_Input/Output\_relay\_test\_Resistance\_CC-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1005.066 Ohm, 5.1% >> vOffset = -0.015V, vMeas = 0.488V, iForce = 0.500mA # Test item 3-20- 1- 4

T AMCA: MESE 2274: Signal\_Input/Output\_relay\_test\_Resistance\_D+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5015.762 Ohm, 1.6% >> vOffset = -0.127V, vMeas = 2.380V, iForce = 0.500mA # Test item 4-20- 1- 1

T AMCA: MESE 2274: Signal\_Input/Output\_relay\_test\_Resistance\_D-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5013.497 Ohm, 2.1% >> vOffset = -0.127V, vMeas = 2.380V, iForce = 0.500mA # Test item 4-20- 1- 2

T AMCA: MESE 2274: Signal\_Input/Output\_relay\_test\_Resistance\_DD+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1004.059 Ohm, 4.1% >> vOffset = -0.021V, vMeas = 0.481V, iForce = 0.500mA # Test item 4-20- 1- 3

T AMCA: MESE 2274: Signal\_Input/Output\_relay\_test\_Resistance\_DD-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1001.038 Ohm, 1.0% >> vOffset = -0.021V, vMeas = 0.479V, iForce = 0.500mA # Test item 4-20- 1- 4

T AMCA: MESE 2275: Signal\_Input/Output\_relay\_test\_Resistance\_E+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5019.791 Ohm, 0.8% >> vOffset = -0.078V, vMeas = 2.432V, iForce = 0.500mA # Test item 5-20- 1- 1

T AMCA: MESE 2275: Signal\_Input/Output\_relay\_test\_Resistance\_E-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5016.014 Ohm, 1.6% >> vOffset = -0.077V, vMeas = 2.431V, iForce = 0.500mA # Test item 5-20- 1- 2

T AMCA: MESE 2275: Signal\_Input/Output\_relay\_test\_Resistance\_EE+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5017.021 Ohm, 1.4% >> vOffset = -0.077V, vMeas = 2.432V, iForce = 0.500mA # Test item 5-20- 1- 3

T AMCA: MESE 2275: Signal\_Input/Output\_relay\_test\_Resistance\_EE-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5016.518 Ohm, 1.5% >> vOffset = -0.077V, vMeas = 2.431V, iForce = 0.500mA # Test item 5-20- 1- 4

T AMCA: MESE 2276: Signal\_Input/Output\_relay\_test\_Resistance\_F+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5020.294 Ohm, 0.7% >> vOffset = -0.118V, vMeas = 2.392V, iForce = 0.500mA # Test item 6-20- 1- 1

T AMCA: MESE 2276: Signal\_Input/Output\_relay\_test\_Resistance\_F-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5021.553 Ohm, 0.5% >> vOffset = -0.118V, vMeas = 2.393V, iForce = 0.500mA # Test item 6-20- 1- 2

T AMCA: MESE 2276: Signal\_Input/Output\_relay\_test\_Resistance\_FF+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5021.805 Ohm, 0.4% >> vOffset = -0.118V, vMeas = 2.392V, iForce = 0.500mA # Test item 6-20- 1- 3

T AMCA: MESE 2276: Signal\_Input/Output\_relay\_test\_Resistance\_FF-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5021.049 Ohm, 0.6% >> vOffset = -0.118V, vMeas = 2.393V, iForce = 0.500mA # Test item 6-20- 1- 4

T AMCA: MESE 2277: Signal\_Input/Output\_relay\_test\_Resistance\_G+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5025.078 Ohm, 0.2% >> vOffset = -0.070V, vMeas = 2.442V, iForce = 0.500mA # Test item 7-20- 1- 1

T AMCA: MESE 2277: Signal\_Input/Output\_relay\_test\_Resistance\_G-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5024.826 Ohm, 0.2% >> vOffset = -0.070V, vMeas = 2.442V, iForce = 0.500mA # Test item 7-20- 1- 2

T AMCA: MESE 2277: Signal\_Input/Output\_relay\_test\_Resistance\_GG+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5026.588 Ohm, 0.5% >> vOffset = -0.070V, vMeas = 2.443V, iForce = 0.500mA # Test item 7-20- 1- 3

T AMCA: MESE 2277: Signal\_Input/Output\_relay\_test\_Resistance\_GG-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5026.085 Ohm, 0.4% >> vOffset = -0.070V, vMeas = 2.443V, iForce = 0.500mA # Test item 7-20- 1- 4

T AMCA: MESE 2278: Signal\_Input/Output\_relay\_test\_Resistance\_H+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5021.805 Ohm, 0.4% >> vOffset = -0.128V, vMeas = 2.383V, iForce = 0.500mA # Test item 8-20- 1- 1

T AMCA: MESE 2278: Signal\_Input/Output\_relay\_test\_Resistance\_H-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5021.553 Ohm, 0.5% >> vOffset = -0.128V, vMeas = 2.383V, iForce = 0.500mA # Test item 8-20- 1- 2

T AMCA: MESE 2278: Signal\_Input/Output\_relay\_test\_Resistance\_HH+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5029.358 Ohm, 1.1% >> vOffset = -0.128V, vMeas = 2.386V, iForce = 0.500mA # Test item 8-20- 1- 3

T AMCA: MESE 2278: Signal\_Input/Output\_relay\_test\_Resistance\_HH-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5022.309 Ohm, 0.3% >> vOffset = -0.128V, vMeas = 2.383V, iForce = 0.500mA # Test item 8-20- 1- 4

T AMCA: MCE 2270: Common\_trigger\_input\_relay\_test\_Common\_Trigger\_1: R\_exp = 470.000 Ohm, [369.000 ... 571.000 Ohm] : R\_meas = 467.789 Ohm, 2.2% >> vMeas = 1.399V, vOffset = -0.004V, usedUnit = 1 # Test item 0-21- 1- 1

T AMCA: MCE 2270: Common\_trigger\_input\_relay\_test\_Common\_Trigger\_2: R\_exp = 470.000 Ohm, [369.000 ... 571.000 Ohm] : R\_meas = 468.712 Ohm, 1.3% >> vMeas = 1.403V, vOffset = -0.003V, usedUnit = 1 # Test item 0-21- 1- 2

T AMCA: MCE 2290: Unit\_FPGA\_configuration\_test\_Unit\_1: exp = passed: meas = passed # Test item 1- 1- 1- 1

T AMCA: MCE 2290: Unit\_FPGA\_configuration\_test\_Unit\_2: exp = passed: meas = passed # Test item 2- 1- 1- 1

T AMCA: MCE 2290: Unit\_FPGA\_configuration\_test\_Unit\_3: exp = passed: meas = passed # Test item 3- 1- 1- 1

T AMCA: MCE 2290: Unit\_FPGA\_configuration\_test\_Unit\_4: exp = passed: meas = passed # Test item 4- 1- 1- 1

T AMCA: MCE 2290: Unit\_FPGA\_configuration\_test\_Unit\_5: exp = passed: meas = passed # Test item 5- 1- 1- 1

T AMCA: MCE 2290: Unit\_FPGA\_configuration\_test\_Unit\_6: exp = passed: meas = passed # Test item 6- 1- 1- 1

T AMCA: MCE 2290: Unit\_FPGA\_configuration\_test\_Unit\_7: exp = passed: meas = passed # Test item 7- 1- 1- 1

T AMCA: MCE 2290: Unit\_FPGA\_configuration\_test\_Unit\_8: exp = passed: meas = passed # Test item 8- 1- 1- 1

T AMCA: MCE 2290: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Data\_Bus\_Test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 0- 1- 2- 1

T AMCA: MCE 2290: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Data\_Bus\_Test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 1- 2- 2

T AMCA: MCE 2290: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 1- 2- 3

T AMCA: MESE 2291: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 1- 8- 1

T AMCA: MESE 2292: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 1- 8- 1

T AMCA: MESE 2293: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 1- 8- 1

T AMCA: MESE 2294: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 1- 8- 1

T AMCA: MESE 2295: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 1- 8- 1

T AMCA: MESE 2296: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 1- 8- 1

T AMCA: MESE 2297: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 1- 8- 1

T AMCA: MESE 2298: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 1- 8- 1

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_DGND\_B\_(U10144): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.005 V, 0.9% # Test item 0- 1- 3- 1

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_(U10145): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.007 V, 1.4% # Test item 1- 1- 3- 2

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_IF\_(U10145): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.024 V, 4.8% # Test item 2- 1- 3- 3

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_(U811): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.003 V, 0.5% # Test item 3- 1- 3- 4

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.969 V, 11.0% # Test item 0- 1- 3- 5

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 0.985 V, 24.5% # Test item 1- 1- 3- 6

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_B: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.985 V, 5.4% # Test item 2- 1- 3- 7

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_B: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.017 V, 7.2% # Test item 3- 1- 3- 8

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5VD\_B: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.969 V, 9.6% # Test item 4- 1- 3- 9

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_B: V\_exp = 1.020 V, [0.918 ... 1.122 V] : V\_meas = 1.029 V, 9.1% # Test item 5- 1- 3-10

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_TEMP\_BUSIF: V\_exp = 0.750 V, [0.500 ... 1.000 V] : V\_meas = 0.624 V, 50.3% # Test item 6- 1- 3-11

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5.5VA: V\_exp = 1.050 V, [0.945 ... 1.155 V] : V\_meas = 1.049 V, 0.7% # Test item 7- 1- 3-12

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5.5VA: V\_exp = -1.140 V, [-1.254 ... -1.026 V] : V\_meas = -1.130 V, 9.0% # Test item 8- 1- 3-13

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+10VA: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 1.001 V, 1.3% # Test item 9- 1- 3-14

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.2VA\_IF: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.998 V, 1.7% # Test item 10- 1- 3-15

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U13: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.967 V, 23.0% # Test item 11- 1- 3-16

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U24: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.970 V, 19.9% # Test item 12- 1- 3-17

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U57: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.972 V, 17.9% # Test item 13- 1- 3-18

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U68: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.968 V, 22.0% # Test item 14- 1- 3-19

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U13: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.003 V, 13.4% # Test item 15- 1- 3-20

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U24: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.996 V, 6.3% # Test item 16- 1- 3-21

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U57: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.999 V, 9.3% # Test item 17- 1- 3-22

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U68: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.996 V, 6.3% # Test item 18- 1- 3-23

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_F\_TG: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.001 V, 37.5% # Test item 19- 1- 3-24

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.5VD\_F\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.972 V, 28.0% # Test item 20- 1- 3-25

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_F: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.008 V, 30.8% # Test item 21- 1- 3-26

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.5VD\_F: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.979 V, 21.0% # Test item 22- 1- 3-27

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5VD\_F: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.955 V, 5.2% # Test item 23- 1- 3-28

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5VA\_F: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.957 V, 2.9% # Test item 24- 1- 3-29

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5VA\_F: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.957 V, 3.4% # Test item 25- 1- 3-30

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VA\_F: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.007 V, 31.5% # Test item 26- 1- 3-31

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.968 V, 12.2% # Test item 27- 1- 3-32

T AMCA: MCE 2290: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_IF\_IF2: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.963 V, 37.0% # Test item 28- 1- 3-33

T AMCA: MCE 2290: Unit\_bus\_data\_line\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 0- 1- 4- 1

T AMCA: MCE 2290: Unit\_bus\_data\_line\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 1- 4- 2

T AMCA: MCE 2290: Unit\_bus\_address/control\_line\_test\_WE\_line\_and\_bit\_0\_of\_addr.\_bus: Reg\_exp = 0x00030002: Reg\_meas = 0x00030002 # Test item 0- 1- 5- 1

T AMCA: MCE 2290: Unit\_bus\_address/control\_line\_test\_OE\_line\_\_unit\_select\_line\_for\_unit\_1--4\_and\_bit\_1\_of\_addr.\_bus: Reg\_exp = 0x0002F004: Reg\_meas = 0x0002F004 # Test item 1- 1- 5- 2

T AMCA: MCE 2290: Unit\_bus\_address/control\_line\_test\_Unit\_select\_line\_for\_unit\_5--8\_and\_bit\_2\_of\_addr.\_bus: Reg\_exp = 0x00020F08: Reg\_meas = 0x00020F08 # Test item 2- 1- 5- 3

T AMCA: MCE 2290: Unit\_bus\_address/control\_line\_test\_Reset: Reg\_exp = 0x00060000: Reg\_meas = 0x00060000 # Test item 3- 1- 5- 4

T AMCA: MCE 2290: Analog\_interface\_module\_EEPROM\_access\_test\_Analog\_Interface\_Module: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 0- 1- 6- 1

T AMCA: MCE 2290: Front\_module\_serial\_bus\_test\_1k\_Ohm\_Rref\_connect\_relay\_on\_test: Reg\_exp = 0x00000003: Reg\_meas = 0x00000003 # Test item 0- 1- 7- 1

T AMCA: MCE 2290: Temperature\_sensor\_test\_START: V\_exp = 0.912 V, [0.450 ... 1.375 V] : V\_meas = 0.794 V, 25.6% >> degree = 33.760degree # Test item 0- 2- 3- 1

T AMCA: MCE 2290: MCLK\_test\_START: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.017 V, 17.0% >> D\_MCLK\_DC = 0.921V, D\_MCLK\_DC\* = 0.938V # Test item 0- 2- 4- 1

T AMCA: MCE 2290: MCLK\_test\_STOP: V\_exp = 0.300 V, [0.200 ... 19.980 V] : V\_meas = 0.341 V, 0.2% >> D\_MCLK\_DC = 0.753V, D\_MCLK\_DC\* = 1.094V # Test item 0- 2- 4- 2

T AMCA: MCE 2290: DC\_rail\_path\_test: R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1001.167 Ohm, 1.2% # Test item 0- 2- 8- 1

T AMCA: MCE 2290: DC\_rail\_path\_test\_GND\_connection\_switch\_On: R\_exp = 5.000 Ohm, [0.000 ... 10.000 Ohm] : R\_meas = 0.833 Ohm, 83.3% # Test item 0- 2- 8- 2

T AMCA: MESE 2291: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 3- 1- 1

T AMCA: MESE 2292: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 3- 1- 1

T AMCA: MESE 2293: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 3- 1- 1

T AMCA: MESE 2294: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 3- 1- 1

T AMCA: MESE 2295: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 3- 1- 1

T AMCA: MESE 2296: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 3- 1- 1

T AMCA: MESE 2297: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 3- 1- 1

T AMCA: MESE 2298: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 3- 1- 1

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.982 V, 2.3% # Test item 1- 3- 2- 1

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.972 V, 7.9% # Test item 1- 3- 2- 2

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.024 V, 14.1% # Test item 1- 3- 2- 3

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.012 V, 2.0% # Test item 1- 3- 2- 4

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.972 V, 8.2% # Test item 1- 3- 2- 5

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.975 V, 25.0% # Test item 1- 3- 2- 6

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.960 V, 0.5% # Test item 1- 3- 2- 7

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.983 V, 23.4% # Test item 1- 3- 2- 8

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.011 V, 31.9% # Test item 2- 3- 2- 1

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.014 V, 34.9% # Test item 2- 3- 2- 2

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.043 V, 32.9% # Test item 2- 3- 2- 3

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.006 V, 4.0% # Test item 2- 3- 2- 4

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.974 V, 6.1% # Test item 2- 3- 2- 5

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.981 V, 19.0% # Test item 2- 3- 2- 6

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.964 V, 4.7% # Test item 2- 3- 2- 7

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.974 V, 14.1% # Test item 2- 3- 2- 8

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.008 V, 28.8% # Test item 3- 3- 2- 1

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.005 V, 25.8% # Test item 3- 3- 2- 2

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.041 V, 30.9% # Test item 3- 3- 2- 3

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.013 V, 3.0% # Test item 3- 3- 2- 4

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.966 V, 14.3% # Test item 3- 3- 2- 5

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.977 V, 23.0% # Test item 3- 3- 2- 6

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.957 V, 3.6% # Test item 3- 3- 2- 7

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.976 V, 16.1% # Test item 3- 3- 2- 8

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.979 V, 0.8% # Test item 4- 3- 2- 1

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.976 V, 3.8% # Test item 4- 3- 2- 2

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.021 V, 11.1% # Test item 4- 3- 2- 3

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.010 V, 0.0% # Test item 4- 3- 2- 4

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.981 V, 1.0% # Test item 4- 3- 2- 5

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.981 V, 19.0% # Test item 4- 3- 2- 6

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.951 V, 9.9% # Test item 4- 3- 2- 7

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.970 V, 9.9% # Test item 4- 3- 2- 8

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.011 V, 31.9% # Test item 5- 3- 2- 1

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.010 V, 30.9% # Test item 5- 3- 2- 2

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.052 V, 41.8% # Test item 5- 3- 2- 3

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.013 V, 3.0% # Test item 5- 3- 2- 4

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.972 V, 8.2% # Test item 5- 3- 2- 5

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.977 V, 23.0% # Test item 5- 3- 2- 6

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.954 V, 6.8% # Test item 5- 3- 2- 7

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.962 V, 1.6% # Test item 5- 3- 2- 8

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.998 V, 18.6% # Test item 6- 3- 2- 1

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.013 V, 33.9% # Test item 6- 3- 2- 2

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.045 V, 34.9% # Test item 6- 3- 2- 3

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.007 V, 3.0% # Test item 6- 3- 2- 4

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.979 V, 1.0% # Test item 6- 3- 2- 5

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.984 V, 16.0% # Test item 6- 3- 2- 6

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.961 V, 0.5% # Test item 6- 3- 2- 7

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -1.003 V, 44.3% # Test item 6- 3- 2- 8

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.013 V, 33.9% # Test item 7- 3- 2- 1

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.010 V, 30.9% # Test item 7- 3- 2- 2

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.054 V, 43.8% # Test item 7- 3- 2- 3

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.012 V, 2.0% # Test item 7- 3- 2- 4

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.982 V, 2.0% # Test item 7- 3- 2- 5

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.978 V, 22.0% # Test item 7- 3- 2- 6

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.957 V, 2.6% # Test item 7- 3- 2- 7

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.958 V, 2.6% # Test item 7- 3- 2- 8

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.009 V, 29.8% # Test item 8- 3- 2- 1

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.008 V, 28.8% # Test item 8- 3- 2- 2

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.049 V, 38.9% # Test item 8- 3- 2- 3

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.008 V, 2.0% # Test item 8- 3- 2- 4

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.979 V, 1.0% # Test item 8- 3- 2- 5

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.975 V, 25.0% # Test item 8- 3- 2- 6

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.958 V, 1.6% # Test item 8- 3- 2- 7

T AMCA: MCE 2290: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.961 V, 0.5% # Test item 8- 3- 2- 8

T AMCA: MESE 2291: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 3- 3- 1

T AMCA: MESE 2292: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 3- 3- 1

T AMCA: MESE 2293: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 3- 3- 1

T AMCA: MESE 2294: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 3- 3- 1

T AMCA: MESE 2295: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 3- 3- 1

T AMCA: MESE 2296: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 3- 3- 1

T AMCA: MESE 2297: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 3- 3- 1

T AMCA: MESE 2298: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 3- 3- 1

T AMCA: MESE 2291: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 1- 3- 4- 1

T AMCA: MESE 2291: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 3- 4- 2

T AMCA: MESE 2292: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 2- 3- 4- 1

T AMCA: MESE 2292: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 2- 3- 4- 2

T AMCA: MESE 2293: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 3- 3- 4- 1

T AMCA: MESE 2293: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 3- 3- 4- 2

T AMCA: MESE 2294: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 4- 3- 4- 1

T AMCA: MESE 2294: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 4- 3- 4- 2

T AMCA: MESE 2295: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 5- 3- 4- 1

T AMCA: MESE 2295: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 5- 3- 4- 2

T AMCA: MESE 2296: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 6- 3- 4- 1

T AMCA: MESE 2296: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 6- 3- 4- 2

T AMCA: MESE 2297: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 7- 3- 4- 1

T AMCA: MESE 2297: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 7- 3- 4- 2

T AMCA: MESE 2298: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 8- 3- 4- 1

T AMCA: MESE 2298: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 8- 3- 4- 2

T AMCA: MESE 2291: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 3- 5- 1

T AMCA: MESE 2292: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 3- 5- 1

T AMCA: MESE 2293: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 3- 5- 1

T AMCA: MESE 2294: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 3- 5- 1

T AMCA: MESE 2295: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 3- 5- 1

T AMCA: MESE 2296: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 3- 5- 1

T AMCA: MESE 2297: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 3- 5- 1

T AMCA: MESE 2298: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 3- 5- 1

T AMCA: MESE 2291: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 3- 5- 2

T AMCA: MESE 2292: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 3- 5- 2

T AMCA: MESE 2293: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 3- 5- 2

T AMCA: MESE 2294: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 3- 5- 2

T AMCA: MESE 2295: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 3- 5- 2

T AMCA: MESE 2296: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 3- 5- 2

T AMCA: MESE 2297: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 3- 5- 2

T AMCA: MESE 2298: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 3- 5- 2

T AMCA: MESE 2291: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 3- 5- 3

T AMCA: MESE 2292: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 3- 5- 3

T AMCA: MESE 2293: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 3- 5- 3

T AMCA: MESE 2294: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 3- 5- 3

T AMCA: MESE 2295: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 3- 5- 3

T AMCA: MESE 2296: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 3- 5- 3

T AMCA: MESE 2297: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 3- 5- 3

T AMCA: MESE 2298: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 3- 5- 3

T AMCA: MESE 2291: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 3- 5- 4

T AMCA: MESE 2292: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 3- 5- 4

T AMCA: MESE 2293: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 3- 5- 4

T AMCA: MESE 2294: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 3- 5- 4

T AMCA: MESE 2295: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 3- 5- 4

T AMCA: MESE 2296: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 3- 5- 4

T AMCA: MESE 2297: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 3- 5- 4

T AMCA: MESE 2298: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 3- 5- 4

T AMCA: MESE 2291: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 1- 3- 6- 1

T AMCA: MESE 2292: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 2- 3- 6- 1

T AMCA: MESE 2293: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 3- 3- 6- 1

T AMCA: MESE 2294: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 4- 3- 6- 1

T AMCA: MESE 2295: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 5- 3- 6- 1

T AMCA: MESE 2296: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 6- 3- 6- 1

T AMCA: MESE 2297: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 7- 3- 6- 1

T AMCA: MESE 2298: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 8- 3- 6- 1

T AMCA: MESE 2291: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 1- 3- 6- 2

T AMCA: MESE 2292: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 2- 3- 6- 2

T AMCA: MESE 2293: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 3- 3- 6- 2

T AMCA: MESE 2294: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 4- 3- 6- 2

T AMCA: MESE 2295: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 5- 3- 6- 2

T AMCA: MESE 2296: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 6- 3- 6- 2

T AMCA: MESE 2297: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 7- 3- 6- 2

T AMCA: MESE 2298: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 8- 3- 6- 2

T AMCA: MESE 2291: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 3- 7- 1

T AMCA: MESE 2292: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 3- 7- 1

T AMCA: MESE 2293: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 3- 7- 1

T AMCA: MESE 2294: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 3- 7- 1

T AMCA: MESE 2295: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 3- 7- 1

T AMCA: MESE 2296: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 3- 7- 1

T AMCA: MESE 2297: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 3- 7- 1

T AMCA: MESE 2298: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 3- 7- 1

T AMCA: MESE 2291: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 1-22- 1- 1

T AMCA: MESE 2291: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 1-22- 1- 2

T AMCA: MESE 2292: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 2-22- 1- 1

T AMCA: MESE 2292: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 2-22- 1- 2

T AMCA: MESE 2293: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 3-22- 1- 1

T AMCA: MESE 2293: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 3-22- 1- 2

T AMCA: MESE 2294: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 4-22- 1- 1

T AMCA: MESE 2294: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 4-22- 1- 2

T AMCA: MESE 2295: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 5-22- 1- 1

T AMCA: MESE 2295: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 5-22- 1- 2

T AMCA: MESE 2296: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 6-22- 1- 1

T AMCA: MESE 2296: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 6-22- 1- 2

T AMCA: MESE 2297: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 7-22- 1- 1

T AMCA: MESE 2297: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 7-22- 1- 2

T AMCA: MESE 2298: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 8-22- 1- 1

T AMCA: MESE 2298: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 8-22- 1- 2

T AMCA: MESE 2291: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.887 V, 5.9% # Test item 1- 4- 1- 1

T AMCA: MESE 2291: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.851 V, 6.5% # Test item 1- 4- 1- 2

T AMCA: MESE 2292: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.904 V, 11.8% # Test item 2- 4- 1- 1

T AMCA: MESE 2292: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.838 V, 11.1% # Test item 2- 4- 1- 2

T AMCA: MESE 2293: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.889 V, 6.6% # Test item 3- 4- 1- 1

T AMCA: MESE 2293: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.849 V, 7.2% # Test item 3- 4- 1- 2

T AMCA: MESE 2294: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.905 V, 12.1% # Test item 4- 4- 1- 1

T AMCA: MESE 2294: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.838 V, 11.1% # Test item 4- 4- 1- 2

T AMCA: MESE 2295: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.891 V, 7.3% # Test item 5- 4- 1- 1

T AMCA: MESE 2295: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.847 V, 8.0% # Test item 5- 4- 1- 2

T AMCA: MESE 2296: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.911 V, 14.1% # Test item 6- 4- 1- 1

T AMCA: MESE 2296: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.830 V, 14.0% # Test item 6- 4- 1- 2

T AMCA: MESE 2297: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.889 V, 6.7% # Test item 7- 4- 1- 1

T AMCA: MESE 2297: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.849 V, 7.4% # Test item 7- 4- 1- 2

T AMCA: MESE 2298: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.904 V, 11.8% # Test item 8- 4- 1- 1

T AMCA: MESE 2298: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.839 V, 11.0% # Test item 8- 4- 1- 2

T AMCA: MESE 2291: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.8% # Test item 1- 4- 2- 1

T AMCA: MESE 2291: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.021 V, 20.9% # Test item 1- 4- 2- 2

T AMCA: MESE 2291: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.973 V, 9.2% # Test item 1- 4- 2- 3

T AMCA: MESE 2292: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.029 V, 9.6% # Test item 2- 4- 2- 1

T AMCA: MESE 2292: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.034 V, 34.2% # Test item 2- 4- 2- 2

T AMCA: MESE 2292: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.960 V, 13.3% # Test item 2- 4- 2- 3

T AMCA: MESE 2293: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 4.9% # Test item 3- 4- 2- 1

T AMCA: MESE 2293: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.020 V, 19.9% # Test item 3- 4- 2- 2

T AMCA: MESE 2293: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.975 V, 8.3% # Test item 3- 4- 2- 3

T AMCA: MESE 2294: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.030 V, 10.1% # Test item 4- 4- 2- 1

T AMCA: MESE 2294: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.036 V, 35.8% # Test item 4- 4- 2- 2

T AMCA: MESE 2294: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.959 V, 13.8% # Test item 4- 4- 2- 3

T AMCA: MESE 2295: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.7% # Test item 5- 4- 2- 1

T AMCA: MESE 2295: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.023 V, 23.0% # Test item 5- 4- 2- 2

T AMCA: MESE 2295: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.971 V, 9.5% # Test item 5- 4- 2- 3

T AMCA: MESE 2296: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.036 V, 11.9% # Test item 6- 4- 2- 1

T AMCA: MESE 2296: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.040 V, 40.3% # Test item 6- 4- 2- 2

T AMCA: MESE 2296: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.955 V, 14.9% # Test item 6- 4- 2- 3

T AMCA: MESE 2297: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.3% # Test item 7- 4- 2- 1

T AMCA: MESE 2297: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.020 V, 20.4% # Test item 7- 4- 2- 2

T AMCA: MESE 2297: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.972 V, 9.2% # Test item 7- 4- 2- 3

T AMCA: MESE 2291: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.876 mA, 41.3% # Test item 1- 4- 3- 1

T AMCA: MESE 2291: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.010 mA, 6.6% # Test item 1- 4- 3- 2

T AMCA: MESE 2291: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.841 mA, 35.2% # Test item 1- 4- 3- 3

T AMCA: MESE 2291: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.009 mA, 5.9% # Test item 1- 4- 3- 4

T AMCA: MESE 2292: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.892 mA, 36.1% # Test item 2- 4- 3- 1

T AMCA: MESE 2292: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.011 mA, 7.5% # Test item 2- 4- 3- 2

T AMCA: MESE 2292: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.827 mA, 38.4% # Test item 2- 4- 3- 3

T AMCA: MESE 2292: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.008 mA, 5.1% # Test item 2- 4- 3- 4

T AMCA: MESE 2293: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.877 mA, 41.1% # Test item 3- 4- 3- 1

T AMCA: MESE 2293: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.009 mA, 6.0% # Test item 3- 4- 3- 2

T AMCA: MESE 2293: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.841 mA, 35.2% # Test item 3- 4- 3- 3

T AMCA: MESE 2293: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.009 mA, 6.3% # Test item 3- 4- 3- 4

T AMCA: MESE 2294: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.893 mA, 35.5% # Test item 4- 4- 3- 1

T AMCA: MESE 2294: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.011 mA, 7.3% # Test item 4- 4- 3- 2

T AMCA: MESE 2294: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.826 mA, 38.6% # Test item 4- 4- 3- 3

T AMCA: MESE 2294: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.007 mA, 4.4% # Test item 4- 4- 3- 4

T AMCA: MESE 2295: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.879 mA, 40.3% # Test item 5- 4- 3- 1

T AMCA: MESE 2295: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.010 mA, 6.9% # Test item 5- 4- 3- 2

T AMCA: MESE 2295: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.835 mA, 36.6% # Test item 5- 4- 3- 3

T AMCA: MESE 2295: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.007 mA, 4.4% # Test item 5- 4- 3- 4

T AMCA: MESE 2296: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.896 mA, 34.8% # Test item 6- 4- 3- 1

T AMCA: MESE 2296: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.007 mA, 4.9% # Test item 6- 4- 3- 2

T AMCA: MESE 2296: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.824 mA, 39.2% # Test item 6- 4- 3- 3

T AMCA: MESE 2296: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.011 mA, 7.1% # Test item 6- 4- 3- 4

T AMCA: MESE 2297: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.879 mA, 40.4% # Test item 7- 4- 3- 1

T AMCA: MESE 2297: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.010 mA, 6.7% # Test item 7- 4- 3- 2

T AMCA: MESE 2297: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.838 mA, 36.0% # Test item 7- 4- 3- 3

T AMCA: MESE 2297: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.008 mA, 5.7% # Test item 7- 4- 3- 4

T AMCA: MESE 2298: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.889 mA, 36.8% # Test item 8- 4- 3- 1

T AMCA: MESE 2298: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.010 mA, 6.9% # Test item 8- 4- 3- 2

T AMCA: MESE 2298: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.825 mA, 38.9% # Test item 8- 4- 3- 3

T AMCA: MESE 2298: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.008 mA, 5.1% # Test item 8- 4- 3- 4

T AMCA: MESE 2291: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.987 V, 4.2% # Test item 1- 4- 4- 1

T AMCA: MESE 2291: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.274 V, 8.9% # Test item 1- 4- 4- 2

T AMCA: MESE 2291: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.010 V, 2.3% # Test item 1- 4- 4- 3

T AMCA: MESE 2291: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.272 V, 1.1% # Test item 1- 4- 4- 4

T AMCA: MESE 2292: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.975 V, 8.4% # Test item 2- 4- 4- 1

T AMCA: MESE 2292: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.272 V, 0.3% # Test item 2- 4- 4- 2

T AMCA: MESE 2292: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.026 V, 5.8% # Test item 2- 4- 4- 3

T AMCA: MESE 2292: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.274 V, 8.5% # Test item 2- 4- 4- 4

T AMCA: MESE 2293: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.983 V, 5.6% # Test item 3- 4- 4- 1

T AMCA: MESE 2293: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.273 V, 2.9% # Test item 3- 4- 4- 2

T AMCA: MESE 2293: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.014 V, 3.1% # Test item 3- 4- 4- 3

T AMCA: MESE 2293: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.275 V, 10.8% # Test item 3- 4- 4- 4

T AMCA: MESE 2294: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.974 V, 8.7% # Test item 4- 4- 4- 1

T AMCA: MESE 2294: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.272 V, 1.1% # Test item 4- 4- 4- 2

T AMCA: MESE 2294: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.025 V, 5.5% # Test item 4- 4- 4- 3

T AMCA: MESE 2294: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.275 V, 9.9% # Test item 4- 4- 4- 4

T AMCA: MESE 2295: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.986 V, 4.5% # Test item 5- 4- 4- 1

T AMCA: MESE 2295: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.274 V, 5.7% # Test item 5- 4- 4- 2

T AMCA: MESE 2295: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.018 V, 4.0% # Test item 5- 4- 4- 3

T AMCA: MESE 2295: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.273 V, 4.8% # Test item 5- 4- 4- 4

T AMCA: MESE 2296: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.970 V, 10.1% # Test item 6- 4- 4- 1

T AMCA: MESE 2296: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.271 V, 5.4% # Test item 6- 4- 4- 2

T AMCA: MESE 2296: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.036 V, 8.0% # Test item 6- 4- 4- 3

T AMCA: MESE 2296: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.277 V, 19.1% # Test item 6- 4- 4- 4

T AMCA: MESE 2297: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.983 V, 5.6% # Test item 7- 4- 4- 1

T AMCA: MESE 2297: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.274 V, 6.6% # Test item 7- 4- 4- 2

T AMCA: MESE 2297: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.013 V, 2.9% # Test item 7- 4- 4- 3

T AMCA: MESE 2297: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.274 V, 7.1% # Test item 7- 4- 4- 4

T AMCA: MESE 2298: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.978 V, 7.5% # Test item 8- 4- 4- 1

T AMCA: MESE 2298: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.274 V, 8.9% # Test item 8- 4- 4- 2

T AMCA: MESE 2298: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.023 V, 5.0% # Test item 8- 4- 4- 3

T AMCA: MESE 2298: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.271 V, 4.5% # Test item 8- 4- 4- 4

T AMCA: MESE 2291: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.995 kOhm, 5.3% # Test item 1- 4- 5- 1

T AMCA: MESE 2292: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.991 kOhm, 9.2% # Test item 2- 4- 5- 1

T AMCA: MESE 2293: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.993 kOhm, 6.6% # Test item 3- 4- 5- 1

T AMCA: MESE 2294: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.990 kOhm, 9.7% # Test item 4- 4- 5- 1

T AMCA: MESE 2295: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.995 kOhm, 5.5% # Test item 5- 4- 5- 1

T AMCA: MESE 2296: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.989 kOhm, 10.8% # Test item 6- 4- 5- 1

T AMCA: MESE 2297: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.994 kOhm, 6.2% # Test item 7- 4- 5- 1

T AMCA: MESE 2298: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.992 kOhm, 8.2% # Test item 8- 4- 5- 1

T AMCA: MCE 2290: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_1: R\_exp = 10220.000 Ohm, [9198.000 ... 11242.000 Ohm] : R\_meas = 10000.305 Ohm, 21.5% >> vMeas = 2.851V, vOffset = -0.149V, usedUnit = 1 # Test item 0- 5- 1- 1

T AMCA: MCE 2290: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_1\_(with\_GND): R\_exp = 59.000 Ohm, [49.000 ... 69.000 Ohm] : R\_meas = 58.243 Ohm, 7.6% >> vMeas = 0.179V, vOffset = 0.004V, usedUnit = 1 # Test item 0- 5- 1- 2

T AMCA: MCE 2290: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_2: R\_exp = 10220.000 Ohm, [9198.000 ... 11242.000 Ohm] : R\_meas = 10007.019 Ohm, 20.8% >> vMeas = 2.852V, vOffset = -0.150V, usedUnit = 1 # Test item 0- 5- 1- 3

T AMCA: MCE 2290: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_2\_(with\_GND): R\_exp = 59.000 Ohm, [49.000 ... 69.000 Ohm] : R\_meas = 58.704 Ohm, 3.0% >> vMeas = 0.180V, vOffset = 0.004V, usedUnit = 1 # Test item 0- 5- 1- 4

T AMCA: MCE 2290: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_\_0.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 1

T AMCA: MCE 2290: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_\_1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 2

T AMCA: MCE 2290: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_-1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 3

T AMCA: MCE 2290: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_\_0.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 4

T AMCA: MCE 2290: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_\_1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 5

T AMCA: MCE 2290: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_-1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 6

T AMCA: MESE 2291: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.498 V, 1.5% >> vOffset = -0.011V # Test item 1- 2- 9- 1

T AMCA: MESE 2291: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 18.9% >> vOffset = 0.003V # Test item 1- 2- 9- 2

T AMCA: MESE 2292: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.498 V, 1.1% >> vOffset = -0.024V # Test item 2- 2- 9- 1

T AMCA: MESE 2292: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 20.1% >> vOffset = 0.001V # Test item 2- 2- 9- 2

T AMCA: MESE 2293: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.497 V, 2.1% >> vOffset = -0.014V # Test item 3- 2- 9- 1

T AMCA: MESE 2293: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 18.9% >> vOffset = 0.000V # Test item 3- 2- 9- 2

T AMCA: MESE 2294: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.498 V, 1.4% >> vOffset = -0.025V # Test item 4- 2- 9- 1

T AMCA: MESE 2294: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 23.9% >> vOffset = 0.003V # Test item 4- 2- 9- 2

T AMCA: MESE 2295: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 0.8% >> vOffset = -0.014V # Test item 5- 2- 9- 1

T AMCA: MESE 2295: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 20.1% >> vOffset = 0.005V # Test item 5- 2- 9- 2

T AMCA: MESE 2296: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.500 V, 0.2% >> vOffset = -0.031V # Test item 6- 2- 9- 1

T AMCA: MESE 2296: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 20.1% >> vOffset = 0.003V # Test item 6- 2- 9- 2

T AMCA: MESE 2297: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.497 V, 2.1% >> vOffset = -0.012V # Test item 7- 2- 9- 1

T AMCA: MESE 2297: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 20.1% >> vOffset = 0.004V # Test item 7- 2- 9- 2

T AMCA: MESE 2298: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.498 V, 1.2% >> vOffset = -0.020V # Test item 8- 2- 9- 1

T AMCA: MESE 2298: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 17.6% >> vOffset = 0.007V # Test item 8- 2- 9- 2

T AMCA: MESE 2291: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 1- 7- 1- 1

T AMCA: MESE 2292: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 2- 7- 1- 1

T AMCA: MESE 2293: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 3- 7- 1- 1

T AMCA: MESE 2294: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 4- 7- 1- 1

T AMCA: MESE 2295: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 5- 7- 1- 1

T AMCA: MESE 2296: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 6- 7- 1- 1

T AMCA: MESE 2297: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 7- 7- 1- 1

T AMCA: MESE 2298: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 8- 7- 1- 1

T AMCA: MESE 2291: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 1- 7- 1- 2

T AMCA: MESE 2292: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 2- 7- 1- 2

T AMCA: MESE 2293: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 3- 7- 1- 2

T AMCA: MESE 2294: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 4- 7- 1- 2

T AMCA: MESE 2295: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 5- 7- 1- 2

T AMCA: MESE 2296: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 6- 7- 1- 2

T AMCA: MESE 2297: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 7- 7- 1- 2

T AMCA: MESE 2298: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 8- 7- 1- 2

T AMCA: MESE 2291: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 1- 7- 2- 1

T AMCA: MESE 2292: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 2- 7- 2- 1

T AMCA: MESE 2293: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 3- 7- 2- 1

T AMCA: MESE 2294: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 4- 7- 2- 1

T AMCA: MESE 2295: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 5- 7- 2- 1

T AMCA: MESE 2296: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 6- 7- 2- 1

T AMCA: MESE 2297: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 7- 7- 2- 1

T AMCA: MESE 2298: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 8- 7- 2- 1

T AMCA: MESE 2291: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 7- 3- 1

T AMCA: MESE 2291: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 7- 3- 2

T AMCA: MESE 2292: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 7- 3- 1

T AMCA: MESE 2292: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 7- 3- 2

T AMCA: MESE 2293: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 7- 3- 1

T AMCA: MESE 2293: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 7- 3- 2

T AMCA: MESE 2294: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 7- 3- 1

T AMCA: MESE 2294: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 7- 3- 2

T AMCA: MESE 2295: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 7- 3- 1

T AMCA: MESE 2295: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 7- 3- 2

T AMCA: MESE 2296: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 7- 3- 1

T AMCA: MESE 2296: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 7- 3- 2

T AMCA: MESE 2297: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 7- 3- 1

T AMCA: MESE 2297: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 7- 3- 2

T AMCA: MESE 2298: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 7- 3- 1

T AMCA: MESE 2298: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 7- 3- 2

T AMCA: MESE 2291: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 1- 8- 1- 1

T AMCA: MESE 2291: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 1- 8- 1- 2

T AMCA: MESE 2291: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 1- 8- 1- 3

T AMCA: MESE 2291: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 4

T AMCA: MESE 2291: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 1- 8- 1- 5

T AMCA: MESE 2291: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 6

T AMCA: MESE 2291: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 1- 8- 1- 7

T AMCA: MESE 2291: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 8

T AMCA: MESE 2291: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 9

T AMCA: MESE 2292: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 2- 8- 1- 1

T AMCA: MESE 2292: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 2- 8- 1- 2

T AMCA: MESE 2292: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 2- 8- 1- 3

T AMCA: MESE 2292: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 4

T AMCA: MESE 2292: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 2- 8- 1- 5

T AMCA: MESE 2292: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 6

T AMCA: MESE 2292: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 2- 8- 1- 7

T AMCA: MESE 2292: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 8

T AMCA: MESE 2292: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 9

T AMCA: MESE 2293: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 3- 8- 1- 1

T AMCA: MESE 2293: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 3- 8- 1- 2

T AMCA: MESE 2293: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 3- 8- 1- 3

T AMCA: MESE 2293: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 4

T AMCA: MESE 2293: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 3- 8- 1- 5

T AMCA: MESE 2293: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 6

T AMCA: MESE 2293: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 3- 8- 1- 7

T AMCA: MESE 2293: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 8

T AMCA: MESE 2293: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 9

T AMCA: MESE 2294: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 4- 8- 1- 1

T AMCA: MESE 2294: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 4- 8- 1- 2

T AMCA: MESE 2294: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 4- 8- 1- 3

T AMCA: MESE 2294: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 4

T AMCA: MESE 2294: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 4- 8- 1- 5

T AMCA: MESE 2294: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 6

T AMCA: MESE 2294: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 4- 8- 1- 7

T AMCA: MESE 2294: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 8

T AMCA: MESE 2294: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 9

T AMCA: MESE 2295: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 5- 8- 1- 1

T AMCA: MESE 2295: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 5- 8- 1- 2

T AMCA: MESE 2295: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 5- 8- 1- 3

T AMCA: MESE 2295: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 4

T AMCA: MESE 2295: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 5- 8- 1- 5

T AMCA: MESE 2295: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 6

T AMCA: MESE 2295: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 5- 8- 1- 7

T AMCA: MESE 2295: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 8

T AMCA: MESE 2295: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 9

T AMCA: MESE 2296: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 6- 8- 1- 1

T AMCA: MESE 2296: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 6- 8- 1- 2

T AMCA: MESE 2296: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 6- 8- 1- 3

T AMCA: MESE 2296: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 4

T AMCA: MESE 2296: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 6- 8- 1- 5

T AMCA: MESE 2296: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 6

T AMCA: MESE 2296: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 6- 8- 1- 7

T AMCA: MESE 2296: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 8

T AMCA: MESE 2296: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 9

T AMCA: MESE 2297: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 7- 8- 1- 1

T AMCA: MESE 2297: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 7- 8- 1- 2

T AMCA: MESE 2297: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 7- 8- 1- 3

T AMCA: MESE 2297: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 4

T AMCA: MESE 2297: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 7- 8- 1- 5

T AMCA: MESE 2297: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 6

T AMCA: MESE 2297: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 7- 8- 1- 7

T AMCA: MESE 2297: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 8

T AMCA: MESE 2297: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 9

T AMCA: MESE 2298: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 8- 8- 1- 1

T AMCA: MESE 2298: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 8- 8- 1- 2

T AMCA: MESE 2298: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 8- 8- 1- 3

T AMCA: MESE 2298: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 4

T AMCA: MESE 2298: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 8- 8- 1- 5

T AMCA: MESE 2298: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 6

T AMCA: MESE 2298: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 8- 8- 1- 7

T AMCA: MESE 2298: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 8

T AMCA: MESE 2298: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 9

T AMCA: MESE 2291: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 1, masterModule = 1, slaveUnit = 3, slaveModule = 1 # Test item 1- 8- 3- 1

T AMCA: MESE 2291: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 1, masterModule = 1, slaveUnit = 3, slaveModule = 1 # Test item 1- 8- 3- 2

T AMCA: MESE 2293: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 3, masterModule = 1, slaveUnit = 2, slaveModule = 2 # Test item 3- 8- 3- 3

T AMCA: MESE 2293: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 3, masterModule = 1, slaveUnit = 2, slaveModule = 2 # Test item 3- 8- 3- 4

T AMCA: MESE 2292: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 2, masterModule = 2, slaveUnit = 4, slaveModule = 2 # Test item 2- 8- 3- 5

T AMCA: MESE 2292: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 2, masterModule = 2, slaveUnit = 4, slaveModule = 2 # Test item 2- 8- 3- 6

T AMCA: MESE 2294: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 4, masterModule = 2, slaveUnit = 5, slaveModule = 3 # Test item 4- 8- 3- 7

T AMCA: MESE 2294: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 4, masterModule = 2, slaveUnit = 5, slaveModule = 3 # Test item 4- 8- 3- 8

T AMCA: MESE 2295: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 5, masterModule = 3, slaveUnit = 7, slaveModule = 3 # Test item 5- 8- 3- 9

T AMCA: MESE 2295: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 5, masterModule = 3, slaveUnit = 7, slaveModule = 3 # Test item 5- 8- 3-10

T AMCA: MESE 2297: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 7, masterModule = 3, slaveUnit = 8, slaveModule = 4 # Test item 7- 8- 3-11

T AMCA: MESE 2297: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 7, masterModule = 3, slaveUnit = 8, slaveModule = 4 # Test item 7- 8- 3-12

T AMCA: MESE 2298: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 8, masterModule = 4, slaveUnit = 6, slaveModule = 4 # Test item 8- 8- 3-13

T AMCA: MESE 2298: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 8, masterModule = 4, slaveUnit = 6, slaveModule = 4 # Test item 8- 8- 3-14

T AMCA: MESE 2296: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 6, masterModule = 4, slaveUnit = 1, slaveModule = 1 # Test item 6- 8- 3-15

T AMCA: MESE 2296: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 6, masterModule = 4, slaveUnit = 1, slaveModule = 1 # Test item 6- 8- 3-16

T AMCA: MESE 2291: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.640 ns, 18.0% >> short = 59256, long = 30356 # Test item 1- 8- 6- 1

T AMCA: MESE 2292: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.324 ns, 33.8% >> short = 60690, long = 31234 # Test item 2- 8- 6- 1

T AMCA: MESE 2293: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.508 ns, 24.6% >> short = 59782, long = 30699 # Test item 3- 8- 6- 1

T AMCA: MESE 2294: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.917 ns, 4.1% >> short = 60164, long = 30166 # Test item 4- 8- 6- 1

T AMCA: MESE 2295: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.915 ns, 4.3% >> short = 59744, long = 30064 # Test item 5- 8- 6- 1

T AMCA: MESE 2296: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.806 ns, 9.7% >> short = 59909, long = 30271 # Test item 6- 8- 6- 1

T AMCA: MESE 2297: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.930 ns, 3.5% >> short = 60430, long = 30213 # Test item 7- 8- 6- 1

T AMCA: MESE 2298: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.628 ns, 18.6% >> short = 59336, long = 30396 # Test item 8- 8- 6- 1

T AMCA: MESE 2291: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 1- 8- 7- 1

T AMCA: MESE 2291: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18267, 43.3% # Test item 1- 8- 7- 2

T AMCA: MESE 2291: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000475B: Reg\_meas = 0x0000475B # Test item 1- 8- 7- 3

T AMCA: MESE 2291: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 8- 7- 4

T AMCA: MESE 2292: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 2- 8- 7- 1

T AMCA: MESE 2292: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18661, 33.5% # Test item 2- 8- 7- 2

T AMCA: MESE 2292: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x000048E5: Reg\_meas = 0x000048E5 # Test item 2- 8- 7- 3

T AMCA: MESE 2292: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 8- 7- 4

T AMCA: MESE 2293: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 3- 8- 7- 1

T AMCA: MESE 2293: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17566, 60.9% # Test item 3- 8- 7- 2

T AMCA: MESE 2293: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000449E: Reg\_meas = 0x0000449E # Test item 3- 8- 7- 3

T AMCA: MESE 2293: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 8- 7- 4

T AMCA: MESE 2294: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 4- 8- 7- 1

T AMCA: MESE 2294: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17905, 52.4% # Test item 4- 8- 7- 2

T AMCA: MESE 2294: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x000045F1: Reg\_meas = 0x000045F1 # Test item 4- 8- 7- 3

T AMCA: MESE 2294: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 8- 7- 4

T AMCA: MESE 2295: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 5- 8- 7- 1

T AMCA: MESE 2295: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18442, 39.0% # Test item 5- 8- 7- 2

T AMCA: MESE 2295: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000480A: Reg\_meas = 0x0000480A # Test item 5- 8- 7- 3

T AMCA: MESE 2295: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 8- 7- 4

T AMCA: MESE 2296: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 6- 8- 7- 1

T AMCA: MESE 2296: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17654, 58.7% # Test item 6- 8- 7- 2

T AMCA: MESE 2296: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x000044F6: Reg\_meas = 0x000044F6 # Test item 6- 8- 7- 3

T AMCA: MESE 2296: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 8- 7- 4

T AMCA: MESE 2297: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 7- 8- 7- 1

T AMCA: MESE 2297: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17949, 51.3% # Test item 7- 8- 7- 2

T AMCA: MESE 2297: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000461D: Reg\_meas = 0x0000461D # Test item 7- 8- 7- 3

T AMCA: MESE 2297: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 8- 7- 4

T AMCA: MESE 2298: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 8- 8- 7- 1

T AMCA: MESE 2298: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18219, 44.5% # Test item 8- 8- 7- 2

T AMCA: MESE 2298: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000472B: Reg\_meas = 0x0000472B # Test item 8- 8- 7- 3

T AMCA: MESE 2298: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 8- 7- 4

T AMCA: MESE 2291: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 9- 1- 1

T AMCA: MESE 2291: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 9- 1- 2

T AMCA: MESE 2291: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 3

T AMCA: MESE 2291: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 4

T AMCA: MESE 2291: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 5

T AMCA: MESE 2291: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 6

T AMCA: MESE 2291: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 7

T AMCA: MESE 2291: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 1- 9- 1- 8

T AMCA: MESE 2291: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 9

T AMCA: MESE 2291: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1-10

T AMCA: MESE 2291: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 1- 9- 1-11

T AMCA: MESE 2291: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 1- 9- 1-12

T AMCA: MESE 2291: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 1- 9- 1-13

T AMCA: MESE 2291: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 9- 1-14

T AMCA: MESE 2292: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 9- 1- 1

T AMCA: MESE 2292: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 9- 1- 2

T AMCA: MESE 2292: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 3

T AMCA: MESE 2292: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 4

T AMCA: MESE 2292: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 5

T AMCA: MESE 2292: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 6

T AMCA: MESE 2292: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 7

T AMCA: MESE 2292: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 2- 9- 1- 8

T AMCA: MESE 2292: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 9

T AMCA: MESE 2292: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1-10

T AMCA: MESE 2292: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 2- 9- 1-11

T AMCA: MESE 2292: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 2- 9- 1-12

T AMCA: MESE 2292: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 2- 9- 1-13

T AMCA: MESE 2292: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 9- 1-14

T AMCA: MESE 2293: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 9- 1- 1

T AMCA: MESE 2293: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 9- 1- 2

T AMCA: MESE 2293: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 3

T AMCA: MESE 2293: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 4

T AMCA: MESE 2293: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 5

T AMCA: MESE 2293: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 6

T AMCA: MESE 2293: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 7

T AMCA: MESE 2293: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 3- 9- 1- 8

T AMCA: MESE 2293: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 9

T AMCA: MESE 2293: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1-10

T AMCA: MESE 2293: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 3- 9- 1-11

T AMCA: MESE 2293: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 3- 9- 1-12

T AMCA: MESE 2293: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 3- 9- 1-13

T AMCA: MESE 2293: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 9- 1-14

T AMCA: MESE 2294: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 9- 1- 1

T AMCA: MESE 2294: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 9- 1- 2

T AMCA: MESE 2294: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 3

T AMCA: MESE 2294: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 4

T AMCA: MESE 2294: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 5

T AMCA: MESE 2294: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 6

T AMCA: MESE 2294: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 7

T AMCA: MESE 2294: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 4- 9- 1- 8

T AMCA: MESE 2294: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 9

T AMCA: MESE 2294: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1-10

T AMCA: MESE 2294: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 4- 9- 1-11

T AMCA: MESE 2294: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 4- 9- 1-12

T AMCA: MESE 2294: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 4- 9- 1-13

T AMCA: MESE 2294: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 9- 1-14

T AMCA: MESE 2295: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 9- 1- 1

T AMCA: MESE 2295: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 9- 1- 2

T AMCA: MESE 2295: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 3

T AMCA: MESE 2295: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 4

T AMCA: MESE 2295: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 5

T AMCA: MESE 2295: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 6

T AMCA: MESE 2295: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 7

T AMCA: MESE 2295: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 5- 9- 1- 8

T AMCA: MESE 2295: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 9

T AMCA: MESE 2295: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1-10

T AMCA: MESE 2295: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 5- 9- 1-11

T AMCA: MESE 2295: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 5- 9- 1-12

T AMCA: MESE 2295: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 5- 9- 1-13

T AMCA: MESE 2295: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 9- 1-14

T AMCA: MESE 2296: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 9- 1- 1

T AMCA: MESE 2296: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 9- 1- 2

T AMCA: MESE 2296: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 3

T AMCA: MESE 2296: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 4

T AMCA: MESE 2296: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 5

T AMCA: MESE 2296: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 6

T AMCA: MESE 2296: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 7

T AMCA: MESE 2296: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 6- 9- 1- 8

T AMCA: MESE 2296: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 9

T AMCA: MESE 2296: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1-10

T AMCA: MESE 2296: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 6- 9- 1-11

T AMCA: MESE 2296: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 6- 9- 1-12

T AMCA: MESE 2296: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 6- 9- 1-13

T AMCA: MESE 2296: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 9- 1-14

T AMCA: MESE 2297: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 9- 1- 1

T AMCA: MESE 2297: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 9- 1- 2

T AMCA: MESE 2297: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 3

T AMCA: MESE 2297: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 4

T AMCA: MESE 2297: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 5

T AMCA: MESE 2297: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 6

T AMCA: MESE 2297: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 7

T AMCA: MESE 2297: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 7- 9- 1- 8

T AMCA: MESE 2297: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 9

T AMCA: MESE 2297: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1-10

T AMCA: MESE 2297: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 7- 9- 1-11

T AMCA: MESE 2297: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 7- 9- 1-12

T AMCA: MESE 2297: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 7- 9- 1-13

T AMCA: MESE 2297: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 9- 1-14

T AMCA: MESE 2298: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 9- 1- 1

T AMCA: MESE 2298: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 9- 1- 2

T AMCA: MESE 2298: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 3

T AMCA: MESE 2298: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 4

T AMCA: MESE 2298: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 5

T AMCA: MESE 2298: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 6

T AMCA: MESE 2298: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 7

T AMCA: MESE 2298: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 8- 9- 1- 8

T AMCA: MESE 2298: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 9

T AMCA: MESE 2298: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1-10

T AMCA: MESE 2298: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 8- 9- 1-11

T AMCA: MESE 2298: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 8- 9- 1-12

T AMCA: MESE 2298: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 8- 9- 1-13

T AMCA: MESE 2298: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 9- 1-14

T AMCA: MESE 2291: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.955 V, 2.7% # Test item 1-17- 1- 1

T AMCA: MESE 2291: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.948 V, 9.1% # Test item 1-17- 1- 2

T AMCA: MESE 2291: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.957 V, 0.9% # Test item 1-17- 1- 3

T AMCA: MESE 2291: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.954 V, 3.6% # Test item 1-17- 1- 4

T AMCA: MESE 2291: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.069 V, 1.1% # Test item 1-17- 1- 5

T AMCA: MESE 2291: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.983 V, 3.6% # Test item 1-17- 1- 6

T AMCA: MESE 2291: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.969 V, 3.6% # Test item 1-17- 1- 7

T AMCA: MESE 2291: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.901 V, 1.4% # Test item 1-17- 1- 8

T AMCA: MESE 2291: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.975 V, 3.6% # Test item 1-17- 1- 9

T AMCA: MESE 2291: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.984 V, 8.2% # Test item 1-17- 1-10

T AMCA: MESE 2291: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.970 V, 5.5% # Test item 1-17- 1-11

T AMCA: MESE 2292: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.943 V, 13.6% # Test item 2-17- 1- 1

T AMCA: MESE 2292: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.951 V, 6.4% # Test item 2-17- 1- 2

T AMCA: MESE 2292: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.959 V, 0.9% # Test item 2-17- 1- 3

T AMCA: MESE 2292: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.947 V, 10.0% # Test item 2-17- 1- 4

T AMCA: MESE 2292: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.062 V, 6.7% # Test item 2-17- 1- 5

T AMCA: MESE 2292: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.983 V, 3.6% # Test item 2-17- 1- 6

T AMCA: MESE 2292: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.966 V, 6.4% # Test item 2-17- 1- 7

T AMCA: MESE 2292: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.900 V, 0.9% # Test item 2-17- 1- 8

T AMCA: MESE 2292: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.974 V, 4.5% # Test item 2-17- 1- 9

T AMCA: MESE 2292: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.987 V, 5.5% # Test item 2-17- 1-10

T AMCA: MESE 2292: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.973 V, 2.7% # Test item 2-17- 1-11

T AMCA: MESE 2293: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.949 V, 8.2% # Test item 3-17- 1- 1

T AMCA: MESE 2293: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.957 V, 0.9% # Test item 3-17- 1- 2

T AMCA: MESE 2293: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.946 V, 10.9% # Test item 3-17- 1- 3

T AMCA: MESE 2293: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.960 V, 1.8% # Test item 3-17- 1- 4

T AMCA: MESE 2293: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.062 V, 5.5% # Test item 3-17- 1- 5

T AMCA: MESE 2293: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.976 V, 2.7% # Test item 3-17- 1- 6

T AMCA: MESE 2293: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.972 V, 0.9% # Test item 3-17- 1- 7

T AMCA: MESE 2293: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.635 V, 1.7% # Test item 3-17- 1- 8

T AMCA: MESE 2293: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.974 V, 4.5% # Test item 3-17- 1- 9

T AMCA: MESE 2293: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.716 V, 10.9% # Test item 3-17- 1-10

T AMCA: MESE 2294: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.952 V, 5.5% # Test item 4-17- 1- 1

T AMCA: MESE 2294: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.949 V, 8.2% # Test item 4-17- 1- 2

T AMCA: MESE 2294: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.957 V, 0.9% # Test item 4-17- 1- 3

T AMCA: MESE 2294: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.960 V, 1.8% # Test item 4-17- 1- 4

T AMCA: MESE 2294: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.069 V, 0.9% # Test item 4-17- 1- 5

T AMCA: MESE 2294: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.979 V, 0.0% # Test item 4-17- 1- 6

T AMCA: MESE 2294: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.959 V, 12.7% # Test item 4-17- 1- 7

T AMCA: MESE 2294: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.640 V, 6.2% # Test item 4-17- 1- 8

T AMCA: MESE 2294: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.970 V, 8.2% # Test item 4-17- 1- 9

T AMCA: MESE 2294: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.714 V, 12.7% # Test item 4-17- 1-10

T AMCA: MESE 2295: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.954 V, 3.6% # Test item 5-17- 1- 1

T AMCA: MESE 2295: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.940 V, 16.4% # Test item 5-17- 1- 2

T AMCA: MESE 2295: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.957 V, 0.9% # Test item 5-17- 1- 3

T AMCA: MESE 2295: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.954 V, 3.6% # Test item 5-17- 1- 4

T AMCA: MESE 2295: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.068 V, 0.0% # Test item 5-17- 1- 5

T AMCA: MESE 2295: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.980 V, 0.9% # Test item 5-17- 1- 6

T AMCA: MESE 2295: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.975 V, 1.8% # Test item 5-17- 1- 7

T AMCA: MESE 2295: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.904 V, 2.8% # Test item 5-17- 1- 8

T AMCA: MESE 2295: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.979 V, 0.0% # Test item 5-17- 1- 9

T AMCA: MESE 2295: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.993 V, 0.0% # Test item 5-17- 1-10

T AMCA: MESE 2295: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.972 V, 3.6% # Test item 5-17- 1-11

T AMCA: MESE 2296: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.951 V, 6.4% # Test item 6-17- 1- 1

T AMCA: MESE 2296: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.960 V, 1.8% # Test item 6-17- 1- 2

T AMCA: MESE 2296: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.958 V, 0.0% # Test item 6-17- 1- 3

T AMCA: MESE 2296: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.954 V, 3.6% # Test item 6-17- 1- 4

T AMCA: MESE 2296: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.060 V, 7.3% # Test item 6-17- 1- 5

T AMCA: MESE 2296: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.976 V, 2.7% # Test item 6-17- 1- 6

T AMCA: MESE 2296: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.966 V, 6.4% # Test item 6-17- 1- 7

T AMCA: MESE 2296: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.636 V, 2.6% # Test item 6-17- 1- 8

T AMCA: MESE 2296: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.983 V, 3.6% # Test item 6-17- 1- 9

T AMCA: MESE 2296: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.710 V, 16.4% # Test item 6-17- 1-10

T AMCA: MESE 2297: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.948 V, 9.1% # Test item 7-17- 1- 1

T AMCA: MESE 2297: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.954 V, 3.6% # Test item 7-17- 1- 2

T AMCA: MESE 2297: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.952 V, 5.5% # Test item 7-17- 1- 3

T AMCA: MESE 2297: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.950 V, 7.3% # Test item 7-17- 1- 4

T AMCA: MESE 2297: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.067 V, 0.9% # Test item 7-17- 1- 5

T AMCA: MESE 2297: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.981 V, 1.8% # Test item 7-17- 1- 6

T AMCA: MESE 2297: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.971 V, 1.8% # Test item 7-17- 1- 7

T AMCA: MESE 2297: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.633 V, 0.1% # Test item 7-17- 1- 8

T AMCA: MESE 2297: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.974 V, 4.5% # Test item 7-17- 1- 9

T AMCA: MESE 2297: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.718 V, 9.1% # Test item 7-17- 1-10

T AMCA: MESE 2298: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.950 V, 7.3% # Test item 8-17- 1- 1

T AMCA: MESE 2298: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.949 V, 8.2% # Test item 8-17- 1- 2

T AMCA: MESE 2298: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.955 V, 2.7% # Test item 8-17- 1- 3

T AMCA: MESE 2298: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.949 V, 8.2% # Test item 8-17- 1- 4

T AMCA: MESE 2298: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.060 V, 8.9% # Test item 8-17- 1- 5

T AMCA: MESE 2298: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.976 V, 2.7% # Test item 8-17- 1- 6

T AMCA: MESE 2298: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.977 V, 3.6% # Test item 8-17- 1- 7

T AMCA: MESE 2298: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.900 V, 0.9% # Test item 8-17- 1- 8

T AMCA: MESE 2298: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.975 V, 3.6% # Test item 8-17- 1- 9

T AMCA: MESE 2298: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.996 V, 2.7% # Test item 8-17- 1-10

T AMCA: MESE 2298: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.972 V, 3.6% # Test item 8-17- 1-11

T AMCA: MESE 2291: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.2% # Test item 1-15- 1- 1

T AMCA: MESE 2291: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% # Test item 1-15- 1- 2

T AMCA: MESE 2291: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.011 V, 3.6% # Test item 1-15- 1- 3

T AMCA: MESE 2291: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.1% # Test item 1-15- 1- 4

T AMCA: MESE 2291: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% # Test item 1-15- 1- 5

T AMCA: MESE 2291: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.011 V, 3.6% # Test item 1-15- 1- 6

T AMCA: MESE 2291: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.2% # Test item 1-15- 1- 7

T AMCA: MESE 2291: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.3% # Test item 1-15- 1- 8

T AMCA: MESE 2291: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.4% # Test item 1-15- 1- 9

T AMCA: MESE 2291: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.1% # Test item 1-15- 1-10

T AMCA: MESE 2291: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.4% # Test item 1-15- 1-11

T AMCA: MESE 2291: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.4% # Test item 1-15- 1-12

T AMCA: MESE 2292: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.998 V, 0.5% # Test item 2-15- 1- 1

T AMCA: MESE 2292: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.007 V, 6.7% # Test item 2-15- 1- 2

T AMCA: MESE 2292: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.016 V, 5.5% # Test item 2-15- 1- 3

T AMCA: MESE 2292: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.998 V, 0.6% # Test item 2-15- 1- 4

T AMCA: MESE 2292: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.007 V, 6.8% # Test item 2-15- 1- 5

T AMCA: MESE 2292: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.017 V, 5.6% # Test item 2-15- 1- 6

T AMCA: MESE 2292: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.000 V, 0.1% # Test item 2-15- 1- 7

T AMCA: MESE 2292: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.006 V, 6.0% # Test item 2-15- 1- 8

T AMCA: MESE 2292: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.017 V, 5.7% # Test item 2-15- 1- 9

T AMCA: MESE 2292: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.000 V, 0.1% # Test item 2-15- 1-10

T AMCA: MESE 2292: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.006 V, 6.0% # Test item 2-15- 1-11

T AMCA: MESE 2292: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.017 V, 5.7% # Test item 2-15- 1-12

T AMCA: MESE 2293: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.011 V, 3.7% # Test item 3-15- 1- 1

T AMCA: MESE 2293: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.5% # Test item 3-15- 1- 2

T AMCA: MESE 2293: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.3% # Test item 3-15- 1- 3

T AMCA: MESE 2293: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.011 V, 3.6% # Test item 3-15- 1- 4

T AMCA: MESE 2293: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.5% # Test item 3-15- 1- 5

T AMCA: MESE 2293: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.2% # Test item 3-15- 1- 6

T AMCA: MESE 2293: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 2.8% # Test item 3-15- 1- 7

T AMCA: MESE 2293: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% # Test item 3-15- 1- 8

T AMCA: MESE 2293: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.014 V, 4.5% # Test item 3-15- 1- 9

T AMCA: MESE 2293: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.8% # Test item 3-15- 1-10

T AMCA: MESE 2293: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% # Test item 3-15- 1-11

T AMCA: MESE 2293: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.014 V, 4.5% # Test item 3-15- 1-12

T AMCA: MESE 2294: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.999 V, 0.3% # Test item 4-15- 1- 1

T AMCA: MESE 2294: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.007 V, 6.9% # Test item 4-15- 1- 2

T AMCA: MESE 2294: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.018 V, 6.1% # Test item 4-15- 1- 3

T AMCA: MESE 2294: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.999 V, 0.4% # Test item 4-15- 1- 4

T AMCA: MESE 2294: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.007 V, 7.2% # Test item 4-15- 1- 5

T AMCA: MESE 2294: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.018 V, 6.0% # Test item 4-15- 1- 6

T AMCA: MESE 2294: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.001 V, 0.3% # Test item 4-15- 1- 7

T AMCA: MESE 2294: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.005 V, 5.3% # Test item 4-15- 1- 8

T AMCA: MESE 2294: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.016 V, 5.5% # Test item 4-15- 1- 9

T AMCA: MESE 2294: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.001 V, 0.2% # Test item 4-15- 1-10

T AMCA: MESE 2294: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.005 V, 5.3% # Test item 4-15- 1-11

T AMCA: MESE 2294: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.017 V, 5.6% # Test item 4-15- 1-12

T AMCA: MESE 2295: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.2% # Test item 5-15- 1- 1

T AMCA: MESE 2295: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% # Test item 5-15- 1- 2

T AMCA: MESE 2295: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.012 V, 4.1% # Test item 5-15- 1- 3

T AMCA: MESE 2295: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.3% # Test item 5-15- 1- 4

T AMCA: MESE 2295: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.3% # Test item 5-15- 1- 5

T AMCA: MESE 2295: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.012 V, 4.1% # Test item 5-15- 1- 6

T AMCA: MESE 2295: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.2% # Test item 5-15- 1- 7

T AMCA: MESE 2295: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% # Test item 5-15- 1- 8

T AMCA: MESE 2295: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.3% # Test item 5-15- 1- 9

T AMCA: MESE 2295: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.1% # Test item 5-15- 1-10

T AMCA: MESE 2295: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.5% # Test item 5-15- 1-11

T AMCA: MESE 2295: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.2% # Test item 5-15- 1-12

T AMCA: MESE 2296: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.005 V, 1.6% # Test item 6-15- 1- 1

T AMCA: MESE 2296: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.8% # Test item 6-15- 1- 2

T AMCA: MESE 2296: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.018 V, 5.9% # Test item 6-15- 1- 3

T AMCA: MESE 2296: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.004 V, 1.5% # Test item 6-15- 1- 4

T AMCA: MESE 2296: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.7% # Test item 6-15- 1- 5

T AMCA: MESE 2296: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.018 V, 5.9% # Test item 6-15- 1- 6

T AMCA: MESE 2296: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.003 V, 1.1% # Test item 6-15- 1- 7

T AMCA: MESE 2296: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.7% # Test item 6-15- 1- 8

T AMCA: MESE 2296: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.016 V, 5.5% # Test item 6-15- 1- 9

T AMCA: MESE 2296: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.003 V, 1.1% # Test item 6-15- 1-10

T AMCA: MESE 2296: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.7% # Test item 6-15- 1-11

T AMCA: MESE 2296: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.016 V, 5.4% # Test item 6-15- 1-12

T AMCA: MESE 2297: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.3% # Test item 7-15- 1- 1

T AMCA: MESE 2297: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% # Test item 7-15- 1- 2

T AMCA: MESE 2297: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.4% # Test item 7-15- 1- 3

T AMCA: MESE 2297: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.3% # Test item 7-15- 1- 4

T AMCA: MESE 2297: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% # Test item 7-15- 1- 5

T AMCA: MESE 2297: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.5% # Test item 7-15- 1- 6

T AMCA: MESE 2297: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.1% # Test item 7-15- 1- 7

T AMCA: MESE 2297: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.3% # Test item 7-15- 1- 8

T AMCA: MESE 2297: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.4% # Test item 7-15- 1- 9

T AMCA: MESE 2297: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.1% # Test item 7-15- 1-10

T AMCA: MESE 2297: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% # Test item 7-15- 1-11

T AMCA: MESE 2297: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.3% # Test item 7-15- 1-12

T AMCA: MESE 2298: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.6% # Test item 8-15- 1- 1

T AMCA: MESE 2298: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.8% # Test item 8-15- 1- 2

T AMCA: MESE 2298: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.004 V, 1.2% # Test item 8-15- 1- 3

T AMCA: MESE 2298: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.6% # Test item 8-15- 1- 4

T AMCA: MESE 2298: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.7% # Test item 8-15- 1- 5

T AMCA: MESE 2298: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.004 V, 1.2% # Test item 8-15- 1- 6

T AMCA: MESE 2298: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.8% # Test item 8-15- 1- 7

T AMCA: MESE 2298: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.3% # Test item 8-15- 1- 8

T AMCA: MESE 2298: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.003 V, 1.1% # Test item 8-15- 1- 9

T AMCA: MESE 2298: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.8% # Test item 8-15- 1-10

T AMCA: MESE 2298: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.4% # Test item 8-15- 1-11

T AMCA: MESE 2298: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.003 V, 1.0% # Test item 8-15- 1-12

T AMCA: MESE 2291: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.894 MOhm, 7.1% # Test item 1-15- 2- 1

T AMCA: MESE 2291: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.779 MOhm, 14.7% # Test item 1-15- 2- 2

T AMCA: MESE 2291: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.991 MOhm, 0.6% # Test item 1-15- 2- 3

T AMCA: MESE 2291: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.832 MOhm, 11.2% # Test item 1-15- 2- 4

T AMCA: MESE 2292: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.864 MOhm, 9.1% # Test item 2-15- 2- 1

T AMCA: MESE 2292: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.716 MOhm, 18.9% # Test item 2-15- 2- 2

T AMCA: MESE 2292: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.852 MOhm, 9.8% # Test item 2-15- 2- 3

T AMCA: MESE 2292: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.864 MOhm, 9.1% # Test item 2-15- 2- 4

T AMCA: MESE 2293: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.764 MOhm, 15.7% # Test item 3-15- 2- 1

T AMCA: MESE 2293: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.902 MOhm, 6.5% # Test item 3-15- 2- 2

T AMCA: MESE 2293: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.791 MOhm, 13.9% # Test item 3-15- 2- 3

T AMCA: MESE 2293: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.826 MOhm, 11.6% # Test item 3-15- 2- 4

T AMCA: MESE 2294: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.728 MOhm, 18.1% # Test item 4-15- 2- 1

T AMCA: MESE 2294: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.777 MOhm, 14.8% # Test item 4-15- 2- 2

T AMCA: MESE 2294: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.790 MOhm, 14.0% # Test item 4-15- 2- 3

T AMCA: MESE 2294: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.912 MOhm, 5.8% # Test item 4-15- 2- 4

T AMCA: MESE 2295: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.694 MOhm, 20.4% # Test item 5-15- 2- 1

T AMCA: MESE 2295: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.860 MOhm, 9.3% # Test item 5-15- 2- 2

T AMCA: MESE 2295: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.797 MOhm, 13.5% # Test item 5-15- 2- 3

T AMCA: MESE 2295: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.870 MOhm, 8.7% # Test item 5-15- 2- 4

T AMCA: MESE 2296: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.876 MOhm, 8.3% # Test item 6-15- 2- 1

T AMCA: MESE 2296: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.815 MOhm, 12.3% # Test item 6-15- 2- 2

T AMCA: MESE 2296: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.848 MOhm, 10.2% # Test item 6-15- 2- 3

T AMCA: MESE 2296: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.677 MOhm, 21.6% # Test item 6-15- 2- 4

T AMCA: MESE 2297: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.741 MOhm, 17.3% # Test item 7-15- 2- 1

T AMCA: MESE 2297: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.768 MOhm, 15.4% # Test item 7-15- 2- 2

T AMCA: MESE 2297: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.812 MOhm, 12.6% # Test item 7-15- 2- 3

T AMCA: MESE 2297: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.842 MOhm, 10.5% # Test item 7-15- 2- 4

T AMCA: MESE 2298: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.752 MOhm, 16.6% # Test item 8-15- 2- 1

T AMCA: MESE 2298: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.777 MOhm, 14.8% # Test item 8-15- 2- 2

T AMCA: MESE 2298: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.683 MOhm, 21.1% # Test item 8-15- 2- 3

T AMCA: MESE 2298: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.707 MOhm, 19.6% # Test item 8-15- 2- 4

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.019V, neg = 0.019V # Test item 1-15- 3- 1

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.716 V, 17.4% >> pos = 2.377V, neg = -2.339V # Test item 1-15- 3- 2

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.019V, neg = 0.019V # Test item 1-15- 3- 3

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.569 V, 19.4% >> pos = 0.804V, neg = -0.765V # Test item 1-15- 3- 4

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.019V, neg = 0.019V # Test item 1-15- 3- 5

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.1% >> pos = 0.117V, neg = -0.079V # Test item 1-15- 3- 6

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.019V, neg = -0.019V # Test item 1-15- 3- 7

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.717 V, 17.3% >> pos = 2.339V, neg = -2.378V # Test item 1-15- 3- 8

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.019V, neg = -0.019V # Test item 1-15- 3- 9

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.569 V, 19.3% >> pos = 0.765V, neg = -0.804V # Test item 1-15- 3-10

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.019V, neg = -0.019V # Test item 1-15- 3-11

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.0% >> pos = 0.079V, neg = -0.117V # Test item 1-15- 3-12

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.018V, neg = 0.018V # Test item 1-15- 3-13

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.717 V, 17.2% >> pos = 2.376V, neg = -2.341V # Test item 1-15- 3-14

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.018V, neg = 0.018V # Test item 1-15- 3-15

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.569 V, 19.1% >> pos = 0.802V, neg = -0.767V # Test item 1-15- 3-16

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.018V, neg = 0.018V # Test item 1-15- 3-17

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 17.8% >> pos = 0.116V, neg = -0.081V # Test item 1-15- 3-18

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.018V, neg = -0.018V # Test item 1-15- 3-19

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.717 V, 17.3% >> pos = 2.341V, neg = -2.376V # Test item 1-15- 3-20

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.018V, neg = -0.018V # Test item 1-15- 3-21

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.569 V, 19.1% >> pos = 0.767V, neg = -0.802V # Test item 1-15- 3-22

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.018V, neg = -0.018V # Test item 1-15- 3-23

T AMCA: MESE 2291: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 17.8% >> pos = 0.080V, neg = -0.116V # Test item 1-15- 3-24

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.040V, neg = 0.040V # Test item 2-15- 3- 1

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.707 V, 19.4% >> pos = 2.394V, neg = -2.313V # Test item 2-15- 3- 2

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.040V, neg = 0.040V # Test item 2-15- 3- 3

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.3% >> pos = 0.823V, neg = -0.743V # Test item 2-15- 3- 4

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.040V, neg = 0.040V # Test item 2-15- 3- 5

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.0% >> pos = 0.138V, neg = -0.058V # Test item 2-15- 3- 6

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.040V, neg = -0.040V # Test item 2-15- 3- 7

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.707 V, 19.5% >> pos = 2.313V, neg = -2.394V # Test item 2-15- 3- 8

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.040V, neg = -0.040V # Test item 2-15- 3- 9

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.3% >> pos = 0.743V, neg = -0.823V # Test item 2-15- 3-10

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.040V, neg = -0.040V # Test item 2-15- 3-11

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.9% >> pos = 0.058V, neg = -0.138V # Test item 2-15- 3-12

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.040V, neg = 0.040V # Test item 2-15- 3-13

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.707 V, 19.3% >> pos = 2.394V, neg = -2.314V # Test item 2-15- 3-14

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.040V, neg = 0.040V # Test item 2-15- 3-15

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.2% >> pos = 0.823V, neg = -0.743V # Test item 2-15- 3-16

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.040V, neg = 0.040V # Test item 2-15- 3-17

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.9% >> pos = 0.138V, neg = -0.058V # Test item 2-15- 3-18

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.040V, neg = -0.040V # Test item 2-15- 3-19

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.707 V, 19.3% >> pos = 2.314V, neg = -2.394V # Test item 2-15- 3-20

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.040V, neg = -0.040V # Test item 2-15- 3-21

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.566 V, 21.2% >> pos = 0.743V, neg = -0.823V # Test item 2-15- 3-22

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.040V, neg = -0.040V # Test item 2-15- 3-23

T AMCA: MESE 2292: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.8% >> pos = 0.058V, neg = -0.138V # Test item 2-15- 3-24

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.018V, neg = 0.018V # Test item 3-15- 3- 1

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.719 V, 16.8% >> pos = 2.378V, neg = -2.341V # Test item 3-15- 3- 2

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.018V, neg = 0.018V # Test item 3-15- 3- 3

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.570 V, 18.7% >> pos = 0.804V, neg = -0.767V # Test item 3-15- 3- 4

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.018V, neg = 0.018V # Test item 3-15- 3- 5

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.197 V, 17.3% >> pos = 0.117V, neg = -0.080V # Test item 3-15- 3- 6

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.018V, neg = -0.018V # Test item 3-15- 3- 7

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.720 V, 16.7% >> pos = 2.341V, neg = -2.378V # Test item 3-15- 3- 8

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.018V, neg = -0.018V # Test item 3-15- 3- 9

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.570 V, 18.6% >> pos = 0.767V, neg = -0.804V # Test item 3-15- 3-10

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.018V, neg = -0.018V # Test item 3-15- 3-11

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.197 V, 17.2% >> pos = 0.080V, neg = -0.117V # Test item 3-15- 3-12

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.020V, neg = 0.020V # Test item 3-15- 3-13

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.719 V, 16.9% >> pos = 2.380V, neg = -2.339V # Test item 3-15- 3-14

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.020V, neg = 0.020V # Test item 3-15- 3-15

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.570 V, 19.0% >> pos = 0.805V, neg = -0.765V # Test item 3-15- 3-16

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.020V, neg = 0.020V # Test item 3-15- 3-17

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 17.7% >> pos = 0.118V, neg = -0.078V # Test item 3-15- 3-18

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.020V, neg = -0.020V # Test item 3-15- 3-19

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.719 V, 17.0% >> pos = 2.339V, neg = -2.379V # Test item 3-15- 3-20

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.020V, neg = -0.020V # Test item 3-15- 3-21

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.570 V, 19.0% >> pos = 0.765V, neg = -0.805V # Test item 3-15- 3-22

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.020V, neg = -0.020V # Test item 3-15- 3-23

T AMCA: MESE 2293: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 17.6% >> pos = 0.078V, neg = -0.118V # Test item 3-15- 3-24

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.042V, neg = 0.042V # Test item 4-15- 3- 1

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.715 V, 17.8% >> pos = 2.400V, neg = -2.315V # Test item 4-15- 3- 2

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.042V, neg = 0.042V # Test item 4-15- 3- 3

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 19.8% >> pos = 0.826V, neg = -0.742V # Test item 4-15- 3- 4

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.042V, neg = 0.042V # Test item 4-15- 3- 5

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.7% >> pos = 0.140V, neg = -0.056V # Test item 4-15- 3- 6

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.042V, neg = -0.042V # Test item 4-15- 3- 7

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.714 V, 17.9% >> pos = 2.315V, neg = -2.399V # Test item 4-15- 3- 8

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.042V, neg = -0.042V # Test item 4-15- 3- 9

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 19.8% >> pos = 0.742V, neg = -0.826V # Test item 4-15- 3-10

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.042V, neg = -0.042V # Test item 4-15- 3-11

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.7% >> pos = 0.056V, neg = -0.141V # Test item 4-15- 3-12

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.041V, neg = 0.041V # Test item 4-15- 3-13

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.714 V, 17.8% >> pos = 2.398V, neg = -2.316V # Test item 4-15- 3-14

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.041V, neg = 0.041V # Test item 4-15- 3-15

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 19.8% >> pos = 0.825V, neg = -0.743V # Test item 4-15- 3-16

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.041V, neg = 0.041V # Test item 4-15- 3-17

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.7% >> pos = 0.139V, neg = -0.057V # Test item 4-15- 3-18

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.041V, neg = -0.041V # Test item 4-15- 3-19

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.713 V, 18.1% >> pos = 2.316V, neg = -2.398V # Test item 4-15- 3-20

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.041V, neg = -0.041V # Test item 4-15- 3-21

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 19.9% >> pos = 0.743V, neg = -0.825V # Test item 4-15- 3-22

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.041V, neg = -0.041V # Test item 4-15- 3-23

T AMCA: MESE 2294: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.7% >> pos = 0.057V, neg = -0.139V # Test item 4-15- 3-24

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.023V, neg = 0.023V # Test item 5-15- 3- 1

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.702 V, 20.4% >> pos = 2.374V, neg = -2.328V # Test item 5-15- 3- 2

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.023V, neg = 0.023V # Test item 5-15- 3- 3

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.6% >> pos = 0.805V, neg = -0.759V # Test item 5-15- 3- 4

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.023V, neg = 0.023V # Test item 5-15- 3- 5

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.1% >> pos = 0.121V, neg = -0.075V # Test item 5-15- 3- 6

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.023V, neg = -0.023V # Test item 5-15- 3- 7

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.701 V, 20.5% >> pos = 2.328V, neg = -2.374V # Test item 5-15- 3- 8

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.023V, neg = -0.023V # Test item 5-15- 3- 9

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.7% >> pos = 0.759V, neg = -0.805V # Test item 5-15- 3-10

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.023V, neg = -0.023V # Test item 5-15- 3-11

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.3% >> pos = 0.075V, neg = -0.121V # Test item 5-15- 3-12

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.022V, neg = 0.022V # Test item 5-15- 3-13

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.701 V, 20.6% >> pos = 2.373V, neg = -2.328V # Test item 5-15- 3-14

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.022V, neg = 0.022V # Test item 5-15- 3-15

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.6% >> pos = 0.804V, neg = -0.760V # Test item 5-15- 3-16

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.022V, neg = 0.022V # Test item 5-15- 3-17

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.3% >> pos = 0.120V, neg = -0.076V # Test item 5-15- 3-18

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = -0.022V, neg = -0.022V # Test item 5-15- 3-19

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.702 V, 20.5% >> pos = 2.328V, neg = -2.373V # Test item 5-15- 3-20

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.022V, neg = -0.022V # Test item 5-15- 3-21

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.5% >> pos = 0.760V, neg = -0.804V # Test item 5-15- 3-22

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.022V, neg = -0.022V # Test item 5-15- 3-23

T AMCA: MESE 2295: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.3% >> pos = 0.076V, neg = -0.120V # Test item 5-15- 3-24

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.043V, neg = 0.043V # Test item 6-15- 3- 1

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.695 V, 22.0% >> pos = 2.391V, neg = -2.304V # Test item 6-15- 3- 2

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.043V, neg = 0.043V # Test item 6-15- 3- 3

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.8% >> pos = 0.824V, neg = -0.738V # Test item 6-15- 3- 4

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.043V, neg = 0.043V # Test item 6-15- 3- 5

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 22.7% >> pos = 0.141V, neg = -0.054V # Test item 6-15- 3- 6

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.043V, neg = -0.043V # Test item 6-15- 3- 7

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.694 V, 22.1% >> pos = 2.304V, neg = -2.391V # Test item 6-15- 3- 8

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.043V, neg = -0.043V # Test item 6-15- 3- 9

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.8% >> pos = 0.738V, neg = -0.824V # Test item 6-15- 3-10

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.043V, neg = -0.043V # Test item 6-15- 3-11

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 22.8% >> pos = 0.054V, neg = -0.141V # Test item 6-15- 3-12

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.044V, neg = 0.044V # Test item 6-15- 3-13

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.693 V, 22.2% >> pos = 2.390V, neg = -2.303V # Test item 6-15- 3-14

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.044V, neg = 0.044V # Test item 6-15- 3-15

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.561 V, 24.2% >> pos = 0.824V, neg = -0.737V # Test item 6-15- 3-16

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.044V, neg = 0.044V # Test item 6-15- 3-17

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 22.9% >> pos = 0.141V, neg = -0.054V # Test item 6-15- 3-18

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.044V, neg = -0.044V # Test item 6-15- 3-19

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.694 V, 22.1% >> pos = 2.303V, neg = -2.391V # Test item 6-15- 3-20

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.044V, neg = -0.044V # Test item 6-15- 3-21

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.561 V, 24.2% >> pos = 0.737V, neg = -0.824V # Test item 6-15- 3-22

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.044V, neg = -0.044V # Test item 6-15- 3-23

T AMCA: MESE 2296: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 22.9% >> pos = 0.054V, neg = -0.141V # Test item 6-15- 3-24

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.019V, neg = 0.019V # Test item 7-15- 3- 1

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.702 V, 20.4% >> pos = 2.370V, neg = -2.332V # Test item 7-15- 3- 2

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.019V, neg = 0.019V # Test item 7-15- 3- 3

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.5% >> pos = 0.801V, neg = -0.763V # Test item 7-15- 3- 4

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.019V, neg = 0.019V # Test item 7-15- 3- 5

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.2% >> pos = 0.117V, neg = -0.079V # Test item 7-15- 3- 6

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.019V, neg = -0.019V # Test item 7-15- 3- 7

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.701 V, 20.7% >> pos = 2.331V, neg = -2.370V # Test item 7-15- 3- 8

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.019V, neg = -0.019V # Test item 7-15- 3- 9

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.6% >> pos = 0.763V, neg = -0.801V # Test item 7-15- 3-10

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.019V, neg = -0.019V # Test item 7-15- 3-11

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.2% >> pos = 0.079V, neg = -0.117V # Test item 7-15- 3-12

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.018V, neg = 0.018V # Test item 7-15- 3-13

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.700 V, 20.8% >> pos = 2.368V, neg = -2.332V # Test item 7-15- 3-14

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.018V, neg = 0.018V # Test item 7-15- 3-15

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.8% >> pos = 0.800V, neg = -0.764V # Test item 7-15- 3-16

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.018V, neg = 0.018V # Test item 7-15- 3-17

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.4% >> pos = 0.116V, neg = -0.080V # Test item 7-15- 3-18

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = -0.018V, neg = -0.018V # Test item 7-15- 3-19

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.701 V, 20.6% >> pos = 2.332V, neg = -2.369V # Test item 7-15- 3-20

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.018V, neg = -0.018V # Test item 7-15- 3-21

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.7% >> pos = 0.764V, neg = -0.800V # Test item 7-15- 3-22

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.018V, neg = -0.018V # Test item 7-15- 3-23

T AMCA: MESE 2297: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.4% >> pos = 0.080V, neg = -0.116V # Test item 7-15- 3-24

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.031V, neg = 0.031V # Test item 8-15- 3- 1

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.698 V, 21.3% >> pos = 2.380V, neg = -2.318V # Test item 8-15- 3- 2

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.031V, neg = 0.031V # Test item 8-15- 3- 3

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.563 V, 23.3% >> pos = 0.812V, neg = -0.750V # Test item 8-15- 3- 4

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.031V, neg = 0.031V # Test item 8-15- 3- 5

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 22.1% >> pos = 0.129V, neg = -0.067V # Test item 8-15- 3- 6

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.031V, neg = -0.031V # Test item 8-15- 3- 7

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.698 V, 21.3% >> pos = 2.318V, neg = -2.380V # Test item 8-15- 3- 8

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.031V, neg = -0.031V # Test item 8-15- 3- 9

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.563 V, 23.4% >> pos = 0.750V, neg = -0.813V # Test item 8-15- 3-10

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.031V, neg = -0.031V # Test item 8-15- 3-11

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 22.2% >> pos = 0.066V, neg = -0.129V # Test item 8-15- 3-12

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.031V, neg = 0.031V # Test item 8-15- 3-13

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.697 V, 21.4% >> pos = 2.380V, neg = -2.318V # Test item 8-15- 3-14

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.031V, neg = 0.031V # Test item 8-15- 3-15

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.5% >> pos = 0.812V, neg = -0.750V # Test item 8-15- 3-16

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.031V, neg = 0.031V # Test item 8-15- 3-17

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 22.0% >> pos = 0.129V, neg = -0.067V # Test item 8-15- 3-18

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.031V, neg = -0.031V # Test item 8-15- 3-19

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.699 V, 21.1% >> pos = 2.318V, neg = -2.380V # Test item 8-15- 3-20

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.031V, neg = -0.031V # Test item 8-15- 3-21

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.563 V, 23.4% >> pos = 0.750V, neg = -0.812V # Test item 8-15- 3-22

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.031V, neg = -0.031V # Test item 8-15- 3-23

T AMCA: MESE 2298: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 22.0% >> pos = 0.067V, neg = -0.129V # Test item 8-15- 3-24

T AMCA: MESE 2291: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.2% # Test item 1-16- 1- 1

T AMCA: MESE 2291: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.9% # Test item 1-16- 1- 2

T AMCA: MESE 2291: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.993 V, 2.3% # Test item 1-16- 1- 3

T AMCA: MESE 2291: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.1% # Test item 1-16- 1- 4

T AMCA: MESE 2291: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.9% # Test item 1-16- 1- 5

T AMCA: MESE 2291: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.993 V, 2.4% # Test item 1-16- 1- 6

T AMCA: MESE 2291: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.2% # Test item 1-16- 1- 7

T AMCA: MESE 2291: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.7% # Test item 1-16- 1- 8

T AMCA: MESE 2291: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.993 V, 2.3% # Test item 1-16- 1- 9

T AMCA: MESE 2291: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.2% # Test item 1-16- 1-10

T AMCA: MESE 2291: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.9% # Test item 1-16- 1-11

T AMCA: MESE 2291: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.993 V, 2.4% # Test item 1-16- 1-12

T AMCA: MESE 2292: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.000 V, 0.0% # Test item 2-16- 1- 1

T AMCA: MESE 2292: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% # Test item 2-16- 1- 2

T AMCA: MESE 2292: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.002 V, 0.8% # Test item 2-16- 1- 3

T AMCA: MESE 2292: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.000 V, 0.0% # Test item 2-16- 1- 4

T AMCA: MESE 2292: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% # Test item 2-16- 1- 5

T AMCA: MESE 2292: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.003 V, 0.9% # Test item 2-16- 1- 6

T AMCA: MESE 2292: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.000 V, 0.0% # Test item 2-16- 1- 7

T AMCA: MESE 2292: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% # Test item 2-16- 1- 8

T AMCA: MESE 2292: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.003 V, 0.9% # Test item 2-16- 1- 9

T AMCA: MESE 2292: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.000 V, 0.0% # Test item 2-16- 1-10

T AMCA: MESE 2292: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% # Test item 2-16- 1-11

T AMCA: MESE 2292: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.002 V, 0.8% # Test item 2-16- 1-12

T AMCA: MESE 2293: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 4.9% # Test item 3-16- 1- 1

T AMCA: MESE 2293: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 10.4% # Test item 3-16- 1- 2

T AMCA: MESE 2293: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.994 V, 1.9% # Test item 3-16- 1- 3

T AMCA: MESE 2293: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.8% # Test item 3-16- 1- 4

T AMCA: MESE 2293: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.011 V, 10.7% # Test item 3-16- 1- 5

T AMCA: MESE 2293: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.994 V, 1.9% # Test item 3-16- 1- 6

T AMCA: MESE 2293: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 4.9% # Test item 3-16- 1- 7

T AMCA: MESE 2293: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 10.4% # Test item 3-16- 1- 8

T AMCA: MESE 2293: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.8% # Test item 3-16- 1- 9

T AMCA: MESE 2293: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 4.9% # Test item 3-16- 1-10

T AMCA: MESE 2293: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.011 V, 10.8% # Test item 3-16- 1-11

T AMCA: MESE 2293: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.994 V, 1.9% # Test item 3-16- 1-12

T AMCA: MESE 2294: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.001 V, 0.4% # Test item 4-16- 1- 1

T AMCA: MESE 2294: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% # Test item 4-16- 1- 2

T AMCA: MESE 2294: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.2% # Test item 4-16- 1- 3

T AMCA: MESE 2294: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.001 V, 0.4% # Test item 4-16- 1- 4

T AMCA: MESE 2294: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% # Test item 4-16- 1- 5

T AMCA: MESE 2294: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.2% # Test item 4-16- 1- 6

T AMCA: MESE 2294: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.001 V, 0.5% # Test item 4-16- 1- 7

T AMCA: MESE 2294: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% # Test item 4-16- 1- 8

T AMCA: MESE 2294: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.2% # Test item 4-16- 1- 9

T AMCA: MESE 2294: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.001 V, 0.4% # Test item 4-16- 1-10

T AMCA: MESE 2294: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% # Test item 4-16- 1-11

T AMCA: MESE 2294: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.2% # Test item 4-16- 1-12

T AMCA: MESE 2295: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 5-16- 1- 1

T AMCA: MESE 2295: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.011 V, 11.3% # Test item 5-16- 1- 2

T AMCA: MESE 2295: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.992 V, 2.7% # Test item 5-16- 1- 3

T AMCA: MESE 2295: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 5-16- 1- 4

T AMCA: MESE 2295: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.011 V, 11.5% # Test item 5-16- 1- 5

T AMCA: MESE 2295: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.992 V, 2.7% # Test item 5-16- 1- 6

T AMCA: MESE 2295: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.7% # Test item 5-16- 1- 7

T AMCA: MESE 2295: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.011 V, 11.3% # Test item 5-16- 1- 8

T AMCA: MESE 2295: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.992 V, 2.7% # Test item 5-16- 1- 9

T AMCA: MESE 2295: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 5-16- 1-10

T AMCA: MESE 2295: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.011 V, 11.3% # Test item 5-16- 1-11

T AMCA: MESE 2295: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.992 V, 2.6% # Test item 5-16- 1-12

T AMCA: MESE 2296: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.1% # Test item 6-16- 1- 1

T AMCA: MESE 2296: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.6% # Test item 6-16- 1- 2

T AMCA: MESE 2296: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.8% # Test item 6-16- 1- 3

T AMCA: MESE 2296: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.1% # Test item 6-16- 1- 4

T AMCA: MESE 2296: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.6% # Test item 6-16- 1- 5

T AMCA: MESE 2296: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.994 V, 1.9% # Test item 6-16- 1- 6

T AMCA: MESE 2296: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.1% # Test item 6-16- 1- 7

T AMCA: MESE 2296: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.7% # Test item 6-16- 1- 8

T AMCA: MESE 2296: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.8% # Test item 6-16- 1- 9

T AMCA: MESE 2296: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.1% # Test item 6-16- 1-10

T AMCA: MESE 2296: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.6% # Test item 6-16- 1-11

T AMCA: MESE 2296: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.994 V, 1.9% # Test item 6-16- 1-12

T AMCA: MESE 2297: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.1% # Test item 7-16- 1- 1

T AMCA: MESE 2297: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.011 V, 11.0% # Test item 7-16- 1- 2

T AMCA: MESE 2297: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.992 V, 2.7% # Test item 7-16- 1- 3

T AMCA: MESE 2297: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.1% # Test item 7-16- 1- 4

T AMCA: MESE 2297: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.011 V, 11.0% # Test item 7-16- 1- 5

T AMCA: MESE 2297: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.992 V, 2.7% # Test item 7-16- 1- 6

T AMCA: MESE 2297: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.1% # Test item 7-16- 1- 7

T AMCA: MESE 2297: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.011 V, 11.1% # Test item 7-16- 1- 8

T AMCA: MESE 2297: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.992 V, 2.7% # Test item 7-16- 1- 9

T AMCA: MESE 2297: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.1% # Test item 7-16- 1-10

T AMCA: MESE 2297: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.011 V, 11.1% # Test item 7-16- 1-11

T AMCA: MESE 2297: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.992 V, 2.7% # Test item 7-16- 1-12

T AMCA: MESE 2298: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.1% # Test item 8-16- 1- 1

T AMCA: MESE 2298: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.014 V, 13.7% # Test item 8-16- 1- 2

T AMCA: MESE 2298: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.986 V, 4.8% # Test item 8-16- 1- 3

T AMCA: MESE 2298: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.0% # Test item 8-16- 1- 4

T AMCA: MESE 2298: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.014 V, 13.8% # Test item 8-16- 1- 5

T AMCA: MESE 2298: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.985 V, 4.8% # Test item 8-16- 1- 6

T AMCA: MESE 2298: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.1% # Test item 8-16- 1- 7

T AMCA: MESE 2298: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.014 V, 14.0% # Test item 8-16- 1- 8

T AMCA: MESE 2298: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.986 V, 4.8% # Test item 8-16- 1- 9

T AMCA: MESE 2298: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.1% # Test item 8-16- 1-10

T AMCA: MESE 2298: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.014 V, 14.0% # Test item 8-16- 1-11

T AMCA: MESE 2298: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.985 V, 4.8% # Test item 8-16- 1-12

T AMCA: MESE 2291: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9869.385 Ohm, 13.1% >> MV = 1.875V, offset = -0.099V # Test item 1-16- 2- 1

T AMCA: MESE 2291: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.732 Ohm, 21.9% >> MV = 0.173V, offset = 0.003V # Test item 1-16- 2- 2

T AMCA: MESE 2291: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.312 Ohm, 15.3% >> MV = 0.137V, offset = 0.004V # Test item 1-16- 2- 3

T AMCA: MESE 2291: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9875.050 Ohm, 12.5% >> MV = 1.874V, offset = -0.101V # Test item 1-16- 2- 4

T AMCA: MESE 2291: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.362 Ohm, 11.0% >> MV = 0.175V, offset = 0.003V # Test item 1-16- 2- 5

T AMCA: MESE 2291: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.857 Ohm, 3.2% >> MV = 0.139V, offset = 0.004V # Test item 1-16- 2- 6

T AMCA: MESE 2292: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9869.385 Ohm, 13.1% >> MV = 1.761V, offset = -0.213V # Test item 2-16- 2- 1

T AMCA: MESE 2292: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.026 Ohm, 16.8% >> MV = 0.171V, offset = 0.000V # Test item 2-16- 2- 2

T AMCA: MESE 2292: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.479 Ohm, 11.6% >> MV = 0.133V, offset = -0.000V # Test item 2-16- 2- 3

T AMCA: MESE 2292: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9874.421 Ohm, 12.6% >> MV = 1.758V, offset = -0.217V # Test item 2-16- 2- 4

T AMCA: MESE 2292: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.152 Ohm, 14.6% >> MV = 0.172V, offset = 0.000V # Test item 2-16- 2- 5

T AMCA: MESE 2292: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.731 Ohm, 6.0% >> MV = 0.134V, offset = -0.000V # Test item 2-16- 2- 6

T AMCA: MESE 2293: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9844.208 Ohm, 15.6% >> MV = 1.856V, offset = -0.112V # Test item 3-16- 2- 1

T AMCA: MESE 2293: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.487 Ohm, 8.8% >> MV = 0.172V, offset = -0.000V # Test item 3-16- 2- 2

T AMCA: MESE 2293: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.857 Ohm, 3.2% >> MV = 0.137V, offset = 0.002V # Test item 3-16- 2- 3

T AMCA: MESE 2293: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9836.025 Ohm, 16.4% >> MV = 1.858V, offset = -0.109V # Test item 3-16- 2- 4

T AMCA: MESE 2293: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.858 Ohm, 19.7% >> MV = 0.170V, offset = -0.000V # Test item 3-16- 2- 5

T AMCA: MESE 2293: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.353 Ohm, 14.4% >> MV = 0.135V, offset = 0.002V # Test item 3-16- 2- 6

T AMCA: MESE 2294: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9862.461 Ohm, 13.8% >> MV = 1.746V, offset = -0.226V # Test item 4-16- 2- 1

T AMCA: MESE 2294: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.068 Ohm, 16.1% >> MV = 0.173V, offset = 0.002V # Test item 4-16- 2- 2

T AMCA: MESE 2294: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.605 Ohm, 8.8% >> MV = 0.135V, offset = 0.001V # Test item 4-16- 2- 3

T AMCA: MESE 2294: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9864.350 Ohm, 13.6% >> MV = 1.751V, offset = -0.222V # Test item 4-16- 2- 4

T AMCA: MESE 2294: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.858 Ohm, 19.7% >> MV = 0.172V, offset = 0.002V # Test item 4-16- 2- 5

T AMCA: MESE 2294: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.563 Ohm, 9.7% >> MV = 0.135V, offset = 0.001V # Test item 4-16- 2- 6

T AMCA: MESE 2295: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9899.598 Ohm, 10.0% >> MV = 1.863V, offset = -0.117V # Test item 5-16- 2- 1

T AMCA: MESE 2295: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.900 Ohm, 19.0% >> MV = 0.174V, offset = 0.004V # Test item 5-16- 2- 2

T AMCA: MESE 2295: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.312 Ohm, 15.3% >> MV = 0.138V, offset = 0.006V # Test item 5-16- 2- 3

T AMCA: MESE 2295: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9892.674 Ohm, 10.7% >> MV = 1.862V, offset = -0.117V # Test item 5-16- 2- 4

T AMCA: MESE 2295: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.068 Ohm, 16.1% >> MV = 0.175V, offset = 0.004V # Test item 5-16- 2- 5

T AMCA: MESE 2295: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.563 Ohm, 9.7% >> MV = 0.139V, offset = 0.006V # Test item 5-16- 2- 6

T AMCA: MESE 2296: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9901.485 Ohm, 9.9% >> MV = 1.713V, offset = -0.268V # Test item 6-16- 2- 1

T AMCA: MESE 2296: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.648 Ohm, 23.3% >> MV = 0.171V, offset = 0.001V # Test item 6-16- 2- 2

T AMCA: MESE 2296: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.186 Ohm, 18.1% >> MV = 0.136V, offset = 0.003V # Test item 6-16- 2- 3

T AMCA: MESE 2296: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9902.744 Ohm, 9.7% >> MV = 1.713V, offset = -0.268V # Test item 6-16- 2- 4

T AMCA: MESE 2296: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.152 Ohm, 14.6% >> MV = 0.173V, offset = 0.001V # Test item 6-16- 2- 5

T AMCA: MESE 2296: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.647 Ohm, 7.8% >> MV = 0.137V, offset = 0.003V # Test item 6-16- 2- 6

T AMCA: MESE 2297: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9868.126 Ohm, 13.2% >> MV = 1.870V, offset = -0.103V # Test item 7-16- 2- 1

T AMCA: MESE 2297: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.858 Ohm, 19.7% >> MV = 0.174V, offset = 0.003V # Test item 7-16- 2- 2

T AMCA: MESE 2297: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.312 Ohm, 15.3% >> MV = 0.138V, offset = 0.005V # Test item 7-16- 2- 3

T AMCA: MESE 2297: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9862.461 Ohm, 13.8% >> MV = 1.872V, offset = -0.100V # Test item 7-16- 2- 4

T AMCA: MESE 2297: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.194 Ohm, 13.9% >> MV = 0.175V, offset = 0.003V # Test item 7-16- 2- 5

T AMCA: MESE 2297: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.731 Ohm, 6.0% >> MV = 0.139V, offset = 0.005V # Test item 7-16- 2- 6

T AMCA: MESE 2298: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9892.044 Ohm, 10.8% >> MV = 1.775V, offset = -0.204V # Test item 8-16- 2- 1

T AMCA: MESE 2298: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.438 Ohm, 26.9% >> MV = 0.175V, offset = 0.005V # Test item 8-16- 2- 2

T AMCA: MESE 2298: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 43.976 Ohm, 22.8% >> MV = 0.139V, offset = 0.007V # Test item 8-16- 2- 3

T AMCA: MESE 2298: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9888.268 Ohm, 11.2% >> MV = 1.773V, offset = -0.205V # Test item 8-16- 2- 4

T AMCA: MESE 2298: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.522 Ohm, 25.5% >> MV = 0.175V, offset = 0.006V # Test item 8-16- 2- 5

T AMCA: MESE 2298: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.228 Ohm, 17.2% >> MV = 0.140V, offset = 0.007V # Test item 8-16- 2- 6

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.1% >> pos = 0.063V, neg = 0.060V # Test item 1-16- 3- 1

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.221 V, 6.6% >> pos = 1.670V, neg = -1.551V # Test item 1-16- 3- 2

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = 0.038V, neg = 0.038V # Test item 1-16- 3- 3

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.591 V, 5.8% >> pos = 0.834V, neg = -0.757V # Test item 1-16- 3- 4

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = 0.028V, neg = 0.028V # Test item 1-16- 3- 5

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.796 V, 4.9% >> pos = 0.424V, neg = -0.372V # Test item 1-16- 3- 6

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = 0.022V, neg = 0.023V # Test item 1-16- 3- 7

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 6.1% >> pos = 0.146V, neg = -0.103V # Test item 1-16- 3- 8

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.5% >> pos = 0.019V, neg = 0.018V # Test item 1-16- 3- 9

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.4% >> pos = 0.069V, neg = -0.031V # Test item 1-16- 3-10

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% >> pos = 0.024V, neg = 0.022V # Test item 1-16- 3-11

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.166 V, 10.5% >> pos = 1.612V, neg = -1.554V # Test item 1-16- 3-12

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.013V, neg = 0.013V # Test item 1-16- 3-13

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.578 V, 13.8% >> pos = 0.795V, neg = -0.783V # Test item 1-16- 3-14

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.0% >> pos = -0.006V, neg = -0.008V # Test item 1-16- 3-15

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.790 V, 12.5% >> pos = 0.389V, neg = -0.401V # Test item 1-16- 3-16

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = -0.011V, neg = -0.009V # Test item 1-16- 3-17

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 2.6% >> pos = 0.113V, neg = -0.136V # Test item 1-16- 3-18

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = -0.014V, neg = -0.014V # Test item 1-16- 3-19

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.0% >> pos = 0.036V, neg = -0.063V # Test item 1-16- 3-20

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.058V, neg = 0.058V # Test item 1-16- 3-21

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.220 V, 6.4% >> pos = 1.668V, neg = -1.552V # Test item 1-16- 3-22

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.2% >> pos = 0.036V, neg = 0.037V # Test item 1-16- 3-23

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.588 V, 7.7% >> pos = 0.830V, neg = -0.758V # Test item 1-16- 3-24

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = 0.026V, neg = 0.027V # Test item 1-16- 3-25

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.796 V, 4.9% >> pos = 0.426V, neg = -0.370V # Test item 1-16- 3-26

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.021V, neg = 0.022V # Test item 1-16- 3-27

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 4.6% >> pos = 0.146V, neg = -0.103V # Test item 1-16- 3-28

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.019V, neg = 0.019V # Test item 1-16- 3-29

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.096 V, 21.3% >> pos = 0.065V, neg = -0.030V # Test item 1-16- 3-30

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.4% >> pos = 0.026V, neg = 0.023V # Test item 1-16- 3-31

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.167 V, 10.2% >> pos = 1.608V, neg = -1.559V # Test item 1-16- 3-32

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 4.3% >> pos = 0.002V, neg = 0.007V # Test item 1-16- 3-33

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.574 V, 16.0% >> pos = 0.792V, neg = -0.783V # Test item 1-16- 3-34

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = -0.003V, neg = -0.004V # Test item 1-16- 3-35

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.791 V, 11.0% >> pos = 0.390V, neg = -0.402V # Test item 1-16- 3-36

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = -0.012V, neg = -0.011V # Test item 1-16- 3-37

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 8.4% >> pos = 0.112V, neg = -0.135V # Test item 1-16- 3-38

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.014V, neg = -0.014V # Test item 1-16- 3-39

T AMCA: MESE 2291: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 3.2% >> pos = 0.036V, neg = -0.063V # Test item 1-16- 3-40

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.0% >> pos = 0.072V, neg = 0.075V # Test item 2-16- 3- 1

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.195 V, 1.5% >> pos = 1.670V, neg = -1.525V # Test item 2-16- 3- 2

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.052V, neg = 0.052V # Test item 2-16- 3- 3

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.573 V, 16.8% >> pos = 0.839V, neg = -0.734V # Test item 2-16- 3- 4

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.044V, neg = 0.044V # Test item 2-16- 3- 5

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.790 V, 12.4% >> pos = 0.438V, neg = -0.352V # Test item 2-16- 3- 6

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = 0.036V, neg = 0.035V # Test item 2-16- 3- 7

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.244 V, 22.8% >> pos = 0.158V, neg = -0.086V # Test item 2-16- 3- 8

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = 0.035V, neg = 0.034V # Test item 2-16- 3- 9

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 2.9% >> pos = 0.084V, neg = -0.015V # Test item 2-16- 3-10

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.6% >> pos = 0.011V, neg = 0.013V # Test item 2-16- 3-11

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.112 V, 27.5% >> pos = 1.564V, neg = -1.548V # Test item 2-16- 3-12

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.6% >> pos = 0.006V, neg = 0.005V # Test item 2-16- 3-13

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.553 V, 29.1% >> pos = 0.767V, neg = -0.786V # Test item 2-16- 3-14

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = -0.020V, neg = -0.021V # Test item 2-16- 3-15

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.777 V, 29.2% >> pos = 0.368V, neg = -0.409V # Test item 2-16- 3-16

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = -0.026V, neg = -0.026V # Test item 2-16- 3-17

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.246 V, 18.0% >> pos = 0.096V, neg = -0.150V # Test item 2-16- 3-18

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = -0.027V, neg = -0.028V # Test item 2-16- 3-19

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 10.4% >> pos = 0.021V, neg = -0.077V # Test item 2-16- 3-20

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = 0.075V, neg = 0.074V # Test item 2-16- 3-21

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.195 V, 1.6% >> pos = 1.673V, neg = -1.522V # Test item 2-16- 3-22

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.0% >> pos = 0.053V, neg = 0.054V # Test item 2-16- 3-23

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.572 V, 17.2% >> pos = 0.840V, neg = -0.733V # Test item 2-16- 3-24

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.8% >> pos = 0.045V, neg = 0.043V # Test item 2-16- 3-25

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.791 V, 10.8% >> pos = 0.439V, neg = -0.352V # Test item 2-16- 3-26

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.036V, neg = 0.036V # Test item 2-16- 3-27

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.245 V, 18.6% >> pos = 0.160V, neg = -0.085V # Test item 2-16- 3-28

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = 0.034V, neg = 0.036V # Test item 2-16- 3-29

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 6.7% >> pos = 0.084V, neg = -0.015V # Test item 2-16- 3-30

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.9% >> pos = 0.017V, neg = 0.010V # Test item 2-16- 3-31

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.108 V, 28.9% >> pos = 1.564V, neg = -1.544V # Test item 2-16- 3-32

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% >> pos = -0.011V, neg = -0.011V # Test item 2-16- 3-33

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.558 V, 26.5% >> pos = 0.770V, neg = -0.788V # Test item 2-16- 3-34

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.1% >> pos = -0.020V, neg = -0.018V # Test item 2-16- 3-35

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.775 V, 31.4% >> pos = 0.367V, neg = -0.408V # Test item 2-16- 3-36

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% >> pos = -0.025V, neg = -0.027V # Test item 2-16- 3-37

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.244 V, 23.3% >> pos = 0.094V, neg = -0.150V # Test item 2-16- 3-38

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = -0.029V, neg = -0.029V # Test item 2-16- 3-39

T AMCA: MESE 2292: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 11.7% >> pos = 0.020V, neg = -0.077V # Test item 2-16- 3-40

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.9% >> pos = 0.059V, neg = 0.062V # Test item 3-16- 3- 1

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.156 V, 13.7% >> pos = 1.638V, neg = -1.518V # Test item 3-16- 3- 2

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.9% >> pos = 0.039V, neg = 0.040V # Test item 3-16- 3- 3

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.571 V, 18.0% >> pos = 0.824V, neg = -0.747V # Test item 3-16- 3- 4

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.029V, neg = 0.029V # Test item 3-16- 3- 5

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.787 V, 16.7% >> pos = 0.422V, neg = -0.365V # Test item 3-16- 3- 6

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.2% >> pos = 0.023V, neg = 0.024V # Test item 3-16- 3- 7

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.245 V, 20.7% >> pos = 0.147V, neg = -0.098V # Test item 3-16- 3- 8

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.9% >> pos = 0.020V, neg = 0.021V # Test item 3-16- 3- 9

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 4.2% >> pos = 0.071V, neg = -0.028V # Test item 3-16- 3-10

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.006 V, 6.4% >> pos = 0.016V, neg = 0.022V # Test item 3-16- 3-11

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.182 V, 5.5% >> pos = 1.611V, neg = -1.572V # Test item 3-16- 3-12

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.012V, neg = 0.012V # Test item 3-16- 3-13

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.574 V, 16.5% >> pos = 0.790V, neg = -0.784V # Test item 3-16- 3-14

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = -0.010V, neg = -0.010V # Test item 3-16- 3-15

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.794 V, 6.9% >> pos = 0.385V, neg = -0.409V # Test item 3-16- 3-16

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = -0.014V, neg = -0.015V # Test item 3-16- 3-17

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.246 V, 14.3% >> pos = 0.109V, neg = -0.137V # Test item 3-16- 3-18

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = -0.015V, neg = -0.016V # Test item 3-16- 3-19

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 9.6% >> pos = 0.034V, neg = -0.064V # Test item 3-16- 3-20

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.2% >> pos = 0.062V, neg = 0.058V # Test item 3-16- 3-21

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.155 V, 14.0% >> pos = 1.637V, neg = -1.518V # Test item 3-16- 3-22

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = 0.041V, neg = 0.042V # Test item 3-16- 3-23

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.571 V, 18.1% >> pos = 0.824V, neg = -0.747V # Test item 3-16- 3-24

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.5% >> pos = 0.026V, neg = 0.028V # Test item 3-16- 3-25

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.787 V, 15.7% >> pos = 0.422V, neg = -0.366V # Test item 3-16- 3-26

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = 0.024V, neg = 0.025V # Test item 3-16- 3-27

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.246 V, 17.6% >> pos = 0.148V, neg = -0.098V # Test item 3-16- 3-28

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.5% >> pos = 0.021V, neg = 0.021V # Test item 3-16- 3-29

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.4% >> pos = 0.071V, neg = -0.029V # Test item 3-16- 3-30

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.1% >> pos = 0.018V, neg = 0.021V # Test item 3-16- 3-31

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.185 V, 4.8% >> pos = 1.613V, neg = -1.572V # Test item 3-16- 3-32

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.2% >> pos = -0.000V, neg = 0.002V # Test item 3-16- 3-33

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.581 V, 12.0% >> pos = 0.792V, neg = -0.789V # Test item 3-16- 3-34

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = -0.010V, neg = -0.010V # Test item 3-16- 3-35

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.787 V, 15.8% >> pos = 0.384V, neg = -0.404V # Test item 3-16- 3-36

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = -0.014V, neg = -0.015V # Test item 3-16- 3-37

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 3.1% >> pos = 0.110V, neg = -0.139V # Test item 3-16- 3-38

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.0% >> pos = -0.017V, neg = -0.016V # Test item 3-16- 3-39

T AMCA: MESE 2293: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 11.7% >> pos = 0.033V, neg = -0.065V # Test item 3-16- 3-40

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = 0.090V, neg = 0.090V # Test item 4-16- 3- 1

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.156 V, 13.9% >> pos = 1.673V, neg = -1.483V # Test item 4-16- 3- 2

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.4% >> pos = 0.061V, neg = 0.064V # Test item 4-16- 3- 3

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.576 V, 15.3% >> pos = 0.849V, neg = -0.726V # Test item 4-16- 3- 4

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.049V, neg = 0.049V # Test item 4-16- 3- 5

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.789 V, 14.1% >> pos = 0.443V, neg = -0.345V # Test item 4-16- 3- 6

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.6% >> pos = 0.039V, neg = 0.040V # Test item 4-16- 3- 7

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.246 V, 14.5% >> pos = 0.162V, neg = -0.084V # Test item 4-16- 3- 8

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.036V, neg = 0.036V # Test item 4-16- 3- 9

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 3.5% >> pos = 0.086V, neg = -0.014V # Test item 4-16- 3-10

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.9% >> pos = 0.028V, neg = 0.025V # Test item 4-16- 3-11

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.203 V, 1.0% >> pos = 1.629V, neg = -1.575V # Test item 4-16- 3-12

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.2% >> pos = 0.011V, neg = 0.014V # Test item 4-16- 3-13

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.590 V, 6.4% >> pos = 0.792V, neg = -0.798V # Test item 4-16- 3-14

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = -0.014V, neg = -0.015V # Test item 4-16- 3-15

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.792 V, 10.5% >> pos = 0.378V, neg = -0.413V # Test item 4-16- 3-16

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = -0.024V, neg = -0.025V # Test item 4-16- 3-17

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.245 V, 19.2% >> pos = 0.097V, neg = -0.148V # Test item 4-16- 3-18

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = -0.028V, neg = -0.028V # Test item 4-16- 3-19

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.0% >> pos = 0.022V, neg = -0.078V # Test item 4-16- 3-20

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.6% >> pos = 0.090V, neg = 0.092V # Test item 4-16- 3-21

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.156 V, 13.7% >> pos = 1.666V, neg = -1.490V # Test item 4-16- 3-22

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = 0.062V, neg = 0.062V # Test item 4-16- 3-23

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.571 V, 17.9% >> pos = 0.849V, neg = -0.723V # Test item 4-16- 3-24

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.9% >> pos = 0.048V, neg = 0.049V # Test item 4-16- 3-25

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.787 V, 15.7% >> pos = 0.443V, neg = -0.345V # Test item 4-16- 3-26

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = 0.040V, neg = 0.039V # Test item 4-16- 3-27

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 10.2% >> pos = 0.163V, neg = -0.084V # Test item 4-16- 3-28

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.036V, neg = 0.036V # Test item 4-16- 3-29

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 5.5% >> pos = 0.086V, neg = -0.013V # Test item 4-16- 3-30

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.3% >> pos = 0.030V, neg = 0.023V # Test item 4-16- 3-31

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.216 V, 5.1% >> pos = 1.634V, neg = -1.582V # Test item 4-16- 3-32

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.4% >> pos = -0.004V, neg = -0.000V # Test item 4-16- 3-33

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.587 V, 8.4% >> pos = 0.790V, neg = -0.796V # Test item 4-16- 3-34

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% >> pos = -0.015V, neg = -0.016V # Test item 4-16- 3-35

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.799 V, 1.4% >> pos = 0.382V, neg = -0.417V # Test item 4-16- 3-36

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 4-16- 3-37

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 4.6% >> pos = 0.099V, neg = -0.152V # Test item 4-16- 3-38

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = -0.028V, neg = -0.027V # Test item 4-16- 3-39

T AMCA: MESE 2294: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 2.5% >> pos = 0.022V, neg = -0.078V # Test item 4-16- 3-40

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.085V, neg = 0.085V # Test item 5-16- 3- 1

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.230 V, 9.3% >> pos = 1.699V, neg = -1.531V # Test item 5-16- 3- 2

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = 0.054V, neg = 0.053V # Test item 5-16- 3- 3

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.597 V, 2.0% >> pos = 0.850V, neg = -0.747V # Test item 5-16- 3- 4

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.5% >> pos = 0.035V, neg = 0.032V # Test item 5-16- 3- 5

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.793 V, 9.1% >> pos = 0.431V, neg = -0.362V # Test item 5-16- 3- 6

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.026V, neg = 0.026V # Test item 5-16- 3- 7

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 5.2% >> pos = 0.153V, neg = -0.099V # Test item 5-16- 3- 8

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = 0.023V, neg = 0.022V # Test item 5-16- 3- 9

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.5% >> pos = 0.072V, neg = -0.028V # Test item 5-16- 3-10

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = 0.050V, neg = 0.050V # Test item 5-16- 3-11

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.165 V, 11.0% >> pos = 1.631V, neg = -1.534V # Test item 5-16- 3-12

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.9% >> pos = 0.023V, neg = 0.026V # Test item 5-16- 3-13

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.578 V, 13.9% >> pos = 0.805V, neg = -0.772V # Test item 5-16- 3-14

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.7% >> pos = 0.003V, neg = 0.002V # Test item 5-16- 3-15

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.791 V, 11.3% >> pos = 0.397V, neg = -0.394V # Test item 5-16- 3-16

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = -0.008V, neg = -0.009V # Test item 5-16- 3-17

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 3.3% >> pos = 0.118V, neg = -0.133V # Test item 5-16- 3-18

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = -0.012V, neg = -0.011V # Test item 5-16- 3-19

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 9.0% >> pos = 0.037V, neg = -0.061V # Test item 5-16- 3-20

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.4% >> pos = 0.084V, neg = 0.086V # Test item 5-16- 3-21

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.236 V, 11.2% >> pos = 1.708V, neg = -1.527V # Test item 5-16- 3-22

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.5% >> pos = 0.055V, neg = 0.054V # Test item 5-16- 3-23

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.594 V, 3.7% >> pos = 0.850V, neg = -0.744V # Test item 5-16- 3-24

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.7% >> pos = 0.035V, neg = 0.037V # Test item 5-16- 3-25

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.798 V, 2.3% >> pos = 0.435V, neg = -0.363V # Test item 5-16- 3-26

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.027V, neg = 0.027V # Test item 5-16- 3-27

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 1.0% >> pos = 0.151V, neg = -0.099V # Test item 5-16- 3-28

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = 0.022V, neg = 0.021V # Test item 5-16- 3-29

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.3% >> pos = 0.073V, neg = -0.027V # Test item 5-16- 3-30

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = 0.050V, neg = 0.049V # Test item 5-16- 3-31

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.169 V, 9.7% >> pos = 1.634V, neg = -1.535V # Test item 5-16- 3-32

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.007 V, 6.7% >> pos = 0.010V, neg = 0.016V # Test item 5-16- 3-33

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.574 V, 16.4% >> pos = 0.805V, neg = -0.769V # Test item 5-16- 3-34

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.3% >> pos = 0.004V, neg = 0.003V # Test item 5-16- 3-35

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.788 V, 15.4% >> pos = 0.396V, neg = -0.392V # Test item 5-16- 3-36

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.006 V, 6.1% >> pos = -0.016V, neg = -0.010V # Test item 5-16- 3-37

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 9.5% >> pos = 0.115V, neg = -0.132V # Test item 5-16- 3-38

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.8% >> pos = -0.012V, neg = -0.014V # Test item 5-16- 3-39

T AMCA: MESE 2295: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.097 V, 15.1% >> pos = 0.038V, neg = -0.059V # Test item 5-16- 3-40

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.8% >> pos = 0.088V, neg = 0.081V # Test item 6-16- 3- 1

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.197 V, 1.0% >> pos = 1.684V, neg = -1.513V # Test item 6-16- 3- 2

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.061V, neg = 0.062V # Test item 6-16- 3- 3

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.590 V, 6.2% >> pos = 0.856V, neg = -0.734V # Test item 6-16- 3- 4

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.050V, neg = 0.050V # Test item 6-16- 3- 5

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.798 V, 2.3% >> pos = 0.446V, neg = -0.352V # Test item 6-16- 3- 6

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = 0.042V, neg = 0.041V # Test item 6-16- 3- 7

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.252 V, 7.7% >> pos = 0.168V, neg = -0.083V # Test item 6-16- 3- 8

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.6% >> pos = 0.037V, neg = 0.039V # Test item 6-16- 3- 9

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.5% >> pos = 0.089V, neg = -0.011V # Test item 6-16- 3-10

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.3% >> pos = 0.008V, neg = 0.012V # Test item 6-16- 3-11

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.229 V, 9.0% >> pos = 1.624V, neg = -1.604V # Test item 6-16- 3-12

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% >> pos = 0.003V, neg = 0.004V # Test item 6-16- 3-13

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.598 V, 1.4% >> pos = 0.784V, neg = -0.814V # Test item 6-16- 3-14

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.008 V, 8.3% >> pos = -0.034V, neg = -0.026V # Test item 6-16- 3-15

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.799 V, 0.8% >> pos = 0.373V, neg = -0.426V # Test item 6-16- 3-16

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = -0.032V, neg = -0.032V # Test item 6-16- 3-17

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 12.8% >> pos = 0.092V, neg = -0.155V # Test item 6-16- 3-18

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.5% >> pos = -0.036V, neg = -0.034V # Test item 6-16- 3-19

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.0% >> pos = 0.015V, neg = -0.085V # Test item 6-16- 3-20

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.1% >> pos = 0.083V, neg = 0.085V # Test item 6-16- 3-21

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.206 V, 1.8% >> pos = 1.687V, neg = -1.519V # Test item 6-16- 3-22

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = 0.061V, neg = 0.059V # Test item 6-16- 3-23

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.588 V, 7.7% >> pos = 0.853V, neg = -0.735V # Test item 6-16- 3-24

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% >> pos = 0.048V, neg = 0.046V # Test item 6-16- 3-25

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.795 V, 5.9% >> pos = 0.446V, neg = -0.350V # Test item 6-16- 3-26

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.042V, neg = 0.041V # Test item 6-16- 3-27

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 12.3% >> pos = 0.164V, neg = -0.083V # Test item 6-16- 3-28

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = 0.039V, neg = 0.039V # Test item 6-16- 3-29

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 4.3% >> pos = 0.087V, neg = -0.012V # Test item 6-16- 3-30

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = 0.008V, neg = 0.006V # Test item 6-16- 3-31

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.233 V, 10.2% >> pos = 1.626V, neg = -1.607V # Test item 6-16- 3-32

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = -0.013V, neg = -0.013V # Test item 6-16- 3-33

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.592 V, 5.2% >> pos = 0.781V, neg = -0.810V # Test item 6-16- 3-34

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.8% >> pos = -0.023V, neg = -0.026V # Test item 6-16- 3-35

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.795 V, 6.7% >> pos = 0.372V, neg = -0.422V # Test item 6-16- 3-36

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.7% >> pos = -0.034V, neg = -0.031V # Test item 6-16- 3-37

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 3.4% >> pos = 0.092V, neg = -0.159V # Test item 6-16- 3-38

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.1% >> pos = -0.033V, neg = -0.036V # Test item 6-16- 3-39

T AMCA: MESE 2296: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.101 V, 7.3% >> pos = 0.017V, neg = -0.084V # Test item 6-16- 3-40

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.059V, neg = 0.059V # Test item 7-16- 3- 1

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.222 V, 6.7% >> pos = 1.671V, neg = -1.550V # Test item 7-16- 3- 2

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.9% >> pos = 0.041V, neg = 0.038V # Test item 7-16- 3- 3

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.613 V, 7.9% >> pos = 0.844V, neg = -0.768V # Test item 7-16- 3- 4

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.2% >> pos = 0.027V, neg = 0.025V # Test item 7-16- 3- 5

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.803 V, 3.4% >> pos = 0.426V, neg = -0.376V # Test item 7-16- 3- 6

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.9% >> pos = 0.020V, neg = 0.022V # Test item 7-16- 3- 7

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.252 V, 9.3% >> pos = 0.148V, neg = -0.104V # Test item 7-16- 3- 8

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = 0.019V, neg = 0.018V # Test item 7-16- 3- 9

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 10.6% >> pos = 0.068V, neg = -0.030V # Test item 7-16- 3-10

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = 0.027V, neg = 0.028V # Test item 7-16- 3-11

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.258 V, 18.1% >> pos = 1.657V, neg = -1.601V # Test item 7-16- 3-12

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.013V, neg = 0.012V # Test item 7-16- 3-13

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.616 V, 9.8% >> pos = 0.815V, neg = -0.801V # Test item 7-16- 3-14

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = -0.007V, neg = -0.007V # Test item 7-16- 3-15

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.810 V, 12.3% >> pos = 0.399V, neg = -0.411V # Test item 7-16- 3-16

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = -0.010V, neg = -0.010V # Test item 7-16- 3-17

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.254 V, 16.1% >> pos = 0.116V, neg = -0.138V # Test item 7-16- 3-18

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = -0.013V, neg = -0.013V # Test item 7-16- 3-19

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.102 V, 12.0% >> pos = 0.038V, neg = -0.064V # Test item 7-16- 3-20

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% >> pos = 0.061V, neg = 0.059V # Test item 7-16- 3-21

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.225 V, 7.9% >> pos = 1.672V, neg = -1.553V # Test item 7-16- 3-22

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = 0.036V, neg = 0.037V # Test item 7-16- 3-23

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.599 V, 0.6% >> pos = 0.834V, neg = -0.766V # Test item 7-16- 3-24

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = 0.024V, neg = 0.023V # Test item 7-16- 3-25

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.805 V, 6.5% >> pos = 0.428V, neg = -0.377V # Test item 7-16- 3-26

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.021V, neg = 0.021V # Test item 7-16- 3-27

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 3.9% >> pos = 0.147V, neg = -0.104V # Test item 7-16- 3-28

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = 0.019V, neg = 0.018V # Test item 7-16- 3-29

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 2.0% >> pos = 0.070V, neg = -0.031V # Test item 7-16- 3-30

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.026V, neg = 0.026V # Test item 7-16- 3-31

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.253 V, 16.4% >> pos = 1.652V, neg = -1.600V # Test item 7-16- 3-32

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.5% >> pos = 0.010V, neg = 0.003V # Test item 7-16- 3-33

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.616 V, 10.3% >> pos = 0.814V, neg = -0.802V # Test item 7-16- 3-34

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = -0.006V, neg = -0.007V # Test item 7-16- 3-35

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.807 V, 8.4% >> pos = 0.396V, neg = -0.411V # Test item 7-16- 3-36

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = -0.010V, neg = -0.008V # Test item 7-16- 3-37

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.252 V, 8.1% >> pos = 0.116V, neg = -0.136V # Test item 7-16- 3-38

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = -0.013V, neg = -0.015V # Test item 7-16- 3-39

T AMCA: MESE 2297: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.102 V, 9.5% >> pos = 0.038V, neg = -0.064V # Test item 7-16- 3-40

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.4% >> pos = 0.089V, neg = 0.092V # Test item 8-16- 3- 1

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.242 V, 13.2% >> pos = 1.714V, neg = -1.528V # Test item 8-16- 3- 2

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.061V, neg = 0.061V # Test item 8-16- 3- 3

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.606 V, 3.6% >> pos = 0.862V, neg = -0.744V # Test item 8-16- 3- 4

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.8% >> pos = 0.047V, neg = 0.049V # Test item 8-16- 3- 5

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.803 V, 3.6% >> pos = 0.447V, neg = -0.355V # Test item 8-16- 3- 6

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = 0.036V, neg = 0.037V # Test item 8-16- 3- 7

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 9.0% >> pos = 0.160V, neg = -0.088V # Test item 8-16- 3- 8

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = 0.033V, neg = 0.033V # Test item 8-16- 3- 9

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.1% >> pos = 0.082V, neg = -0.018V # Test item 8-16- 3-10

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.6% >> pos = 0.032V, neg = 0.034V # Test item 8-16- 3-11

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.196 V, 1.4% >> pos = 1.633V, neg = -1.563V # Test item 8-16- 3-12

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = 0.016V, neg = 0.015V # Test item 8-16- 3-13

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.597 V, 2.2% >> pos = 0.801V, neg = -0.795V # Test item 8-16- 3-14

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = -0.012V, neg = -0.013V # Test item 8-16- 3-15

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.798 V, 2.4% >> pos = 0.387V, neg = -0.411V # Test item 8-16- 3-16

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = -0.020V, neg = -0.021V # Test item 8-16- 3-17

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 8.6% >> pos = 0.103V, neg = -0.145V # Test item 8-16- 3-18

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.0% >> pos = -0.025V, neg = -0.024V # Test item 8-16- 3-19

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.8% >> pos = 0.024V, neg = -0.076V # Test item 8-16- 3-20

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.094V, neg = 0.094V # Test item 8-16- 3-21

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.250 V, 15.7% >> pos = 1.719V, neg = -1.532V # Test item 8-16- 3-22

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.1% >> pos = 0.062V, neg = 0.060V # Test item 8-16- 3-23

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.608 V, 4.8% >> pos = 0.865V, neg = -0.742V # Test item 8-16- 3-24

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.045V, neg = 0.044V # Test item 8-16- 3-25

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.805 V, 6.6% >> pos = 0.448V, neg = -0.357V # Test item 8-16- 3-26

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.037V, neg = 0.036V # Test item 8-16- 3-27

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 4.6% >> pos = 0.161V, neg = -0.088V # Test item 8-16- 3-28

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 8-16- 3-29

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.5% >> pos = 0.083V, neg = -0.017V # Test item 8-16- 3-30

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.1% >> pos = 0.036V, neg = 0.034V # Test item 8-16- 3-31

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.196 V, 1.2% >> pos = 1.634V, neg = -1.562V # Test item 8-16- 3-32

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.6% >> pos = 0.002V, neg = 0.001V # Test item 8-16- 3-33

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.593 V, 4.5% >> pos = 0.799V, neg = -0.794V # Test item 8-16- 3-34

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.5% >> pos = -0.014V, neg = -0.014V # Test item 8-16- 3-35

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.796 V, 5.3% >> pos = 0.386V, neg = -0.410V # Test item 8-16- 3-36

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.4% >> pos = -0.022V, neg = -0.020V # Test item 8-16- 3-37

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 6.1% >> pos = 0.102V, neg = -0.146V # Test item 8-16- 3-38

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = -0.025V, neg = -0.024V # Test item 8-16- 3-39

T AMCA: MESE 2298: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.102 V, 8.3% >> pos = 0.025V, neg = -0.077V # Test item 8-16- 3-40

T AMCA: MESE 2291: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.779 V, 21.2% >> POS = 0.812V, NEG = 0.033V # Test item 1-16- 4- 1

T AMCA: MESE 2291: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.759 V, 41.1% >> POS = 0.791V, NEG = 0.032V # Test item 1-16- 4- 2

T AMCA: MESE 2291: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.745 V, 54.7% >> POS = 0.768V, NEG = 0.023V # Test item 1-16- 4- 3

T AMCA: MESE 2291: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.797 V, 3.3% >> POS = 0.834V, NEG = 0.037V # Test item 1-16- 4- 4

T AMCA: MESE 2292: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.773 V, 26.7% >> POS = 0.822V, NEG = 0.048V # Test item 2-16- 4- 1

T AMCA: MESE 2292: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.754 V, 46.1% >> POS = 0.800V, NEG = 0.046V # Test item 2-16- 4- 2

T AMCA: MESE 2292: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.738 V, 61.6% >> POS = 0.776V, NEG = 0.038V # Test item 2-16- 4- 3

T AMCA: MESE 2292: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.784 V, 15.8% >> POS = 0.838V, NEG = 0.053V # Test item 2-16- 4- 4

T AMCA: MESE 2293: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.774 V, 26.1% >> POS = 0.808V, NEG = 0.034V # Test item 3-16- 4- 1

T AMCA: MESE 2293: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.754 V, 46.1% >> POS = 0.788V, NEG = 0.034V # Test item 3-16- 4- 2

T AMCA: MESE 2293: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.735 V, 64.6% >> POS = 0.760V, NEG = 0.025V # Test item 3-16- 4- 3

T AMCA: MESE 2293: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.784 V, 16.3% >> POS = 0.823V, NEG = 0.039V # Test item 3-16- 4- 4

T AMCA: MESE 2294: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.770 V, 29.5% >> POS = 0.829V, NEG = 0.058V # Test item 4-16- 4- 1

T AMCA: MESE 2294: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.753 V, 47.1% >> POS = 0.807V, NEG = 0.054V # Test item 4-16- 4- 2

T AMCA: MESE 2294: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.737 V, 63.1% >> POS = 0.783V, NEG = 0.046V # Test item 4-16- 4- 3

T AMCA: MESE 2294: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.785 V, 14.5% >> POS = 0.846V, NEG = 0.061V # Test item 4-16- 4- 4

T AMCA: MESE 2295: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.785 V, 15.0% >> POS = 0.832V, NEG = 0.047V # Test item 5-16- 4- 1

T AMCA: MESE 2295: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.765 V, 35.5% >> POS = 0.813V, NEG = 0.049V # Test item 5-16- 4- 2

T AMCA: MESE 2295: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.751 V, 48.7% >> POS = 0.785V, NEG = 0.034V # Test item 5-16- 4- 3

T AMCA: MESE 2295: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.795 V, 5.0% >> POS = 0.848V, NEG = 0.053V # Test item 5-16- 4- 4

T AMCA: MESE 2296: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.781 V, 19.3% >> POS = 0.836V, NEG = 0.055V # Test item 6-16- 4- 1

T AMCA: MESE 2296: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.761 V, 38.9% >> POS = 0.815V, NEG = 0.054V # Test item 6-16- 4- 2

T AMCA: MESE 2296: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.746 V, 53.8% >> POS = 0.790V, NEG = 0.044V # Test item 6-16- 4- 3

T AMCA: MESE 2296: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.792 V, 7.6% >> POS = 0.853V, NEG = 0.061V # Test item 6-16- 4- 4

T AMCA: MESE 2297: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.792 V, 8.1% >> POS = 0.825V, NEG = 0.033V # Test item 7-16- 4- 1

T AMCA: MESE 2297: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.767 V, 33.4% >> POS = 0.800V, NEG = 0.033V # Test item 7-16- 4- 2

T AMCA: MESE 2297: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.753 V, 47.2% >> POS = 0.777V, NEG = 0.024V # Test item 7-16- 4- 3

T AMCA: MESE 2297: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.804 V, 4.4% >> POS = 0.844V, NEG = 0.040V # Test item 7-16- 4- 4

T AMCA: MESE 2298: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.791 V, 9.2% >> POS = 0.846V, NEG = 0.056V # Test item 8-16- 4- 1

T AMCA: MESE 2298: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.763 V, 36.5% >> POS = 0.818V, NEG = 0.055V # Test item 8-16- 4- 2

T AMCA: MESE 2298: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.751 V, 48.6% >> POS = 0.796V, NEG = 0.045V # Test item 8-16- 4- 3

T AMCA: MESE 2298: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.808 V, 7.8% >> POS = 0.865V, NEG = 0.057V # Test item 8-16- 4- 4

T AMCA: MESE 2291: Signal\_Input/Output\_relay\_test\_Resistance\_A+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5021.049 Ohm, 0.6% >> vOffset = -0.063V, vMeas = 2.448V, iForce = 0.500mA # Test item 1-20- 1- 1

T AMCA: MESE 2291: Signal\_Input/Output\_relay\_test\_Resistance\_A-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5018.028 Ohm, 1.2% >> vOffset = -0.062V, vMeas = 2.447V, iForce = 0.500mA # Test item 1-20- 1- 2

T AMCA: MESE 2291: Signal\_Input/Output\_relay\_test\_Resistance\_AA+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1001.541 Ohm, 1.5% >> vOffset = -0.008V, vMeas = 0.493V, iForce = 0.500mA # Test item 1-20- 1- 3

T AMCA: MESE 2291: Signal\_Input/Output\_relay\_test\_Resistance\_AA-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1003.807 Ohm, 3.8% >> vOffset = -0.008V, vMeas = 0.494V, iForce = 0.500mA # Test item 1-20- 1- 4

T AMCA: MESE 2292: Signal\_Input/Output\_relay\_test\_Resistance\_B+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5022.057 Ohm, 0.4% >> vOffset = -0.124V, vMeas = 2.387V, iForce = 0.500mA # Test item 2-20- 1- 1

T AMCA: MESE 2292: Signal\_Input/Output\_relay\_test\_Resistance\_B-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5018.784 Ohm, 1.0% >> vOffset = -0.123V, vMeas = 2.386V, iForce = 0.500mA # Test item 2-20- 1- 2

T AMCA: MESE 2292: Signal\_Input/Output\_relay\_test\_Resistance\_BB+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1002.548 Ohm, 2.5% >> vOffset = -0.022V, vMeas = 0.479V, iForce = 0.500mA # Test item 2-20- 1- 3

T AMCA: MESE 2292: Signal\_Input/Output\_relay\_test\_Resistance\_BB-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1003.807 Ohm, 3.8% >> vOffset = -0.022V, vMeas = 0.480V, iForce = 0.500mA # Test item 2-20- 1- 4

T AMCA: MESE 2293: Signal\_Input/Output\_relay\_test\_Resistance\_C+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5012.741 Ohm, 2.2% >> vOffset = -0.071V, vMeas = 2.435V, iForce = 0.500mA # Test item 3-20- 1- 1

T AMCA: MESE 2293: Signal\_Input/Output\_relay\_test\_Resistance\_C-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5014.000 Ohm, 2.0% >> vOffset = -0.072V, vMeas = 2.435V, iForce = 0.500mA # Test item 3-20- 1- 2

T AMCA: MESE 2293: Signal\_Input/Output\_relay\_test\_Resistance\_CC+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1005.821 Ohm, 5.8% >> vOffset = -0.012V, vMeas = 0.491V, iForce = 0.500mA # Test item 3-20- 1- 3

T AMCA: MESE 2293: Signal\_Input/Output\_relay\_test\_Resistance\_CC-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1005.821 Ohm, 5.8% >> vOffset = -0.012V, vMeas = 0.490V, iForce = 0.500mA # Test item 3-20- 1- 4

T AMCA: MESE 2294: Signal\_Input/Output\_relay\_test\_Resistance\_D+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5016.518 Ohm, 1.5% >> vOffset = -0.128V, vMeas = 2.380V, iForce = 0.500mA # Test item 4-20- 1- 1

T AMCA: MESE 2294: Signal\_Input/Output\_relay\_test\_Resistance\_D-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5014.503 Ohm, 1.9% >> vOffset = -0.127V, vMeas = 2.380V, iForce = 0.500mA # Test item 4-20- 1- 2

T AMCA: MESE 2294: Signal\_Input/Output\_relay\_test\_Resistance\_DD+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1009.094 Ohm, 9.1% >> vOffset = -0.022V, vMeas = 0.482V, iForce = 0.500mA # Test item 4-20- 1- 3

T AMCA: MESE 2294: Signal\_Input/Output\_relay\_test\_Resistance\_DD-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1006.577 Ohm, 6.6% >> vOffset = -0.022V, vMeas = 0.481V, iForce = 0.500mA # Test item 4-20- 1- 4

T AMCA: MESE 2295: Signal\_Input/Output\_relay\_test\_Resistance\_E+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5027.092 Ohm, 0.6% >> vOffset = -0.075V, vMeas = 2.438V, iForce = 0.500mA # Test item 5-20- 1- 1

T AMCA: MESE 2295: Signal\_Input/Output\_relay\_test\_Resistance\_E-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5027.596 Ohm, 0.7% >> vOffset = -0.076V, vMeas = 2.438V, iForce = 0.500mA # Test item 5-20- 1- 2

T AMCA: MESE 2295: Signal\_Input/Output\_relay\_test\_Resistance\_EE+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5028.099 Ohm, 0.8% >> vOffset = -0.076V, vMeas = 2.439V, iForce = 0.500mA # Test item 5-20- 1- 3

T AMCA: MESE 2295: Signal\_Input/Output\_relay\_test\_Resistance\_EE-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5028.351 Ohm, 0.9% >> vOffset = -0.076V, vMeas = 2.438V, iForce = 0.500mA # Test item 5-20- 1- 4

T AMCA: MESE 2296: Signal\_Input/Output\_relay\_test\_Resistance\_F+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5027.596 Ohm, 0.7% >> vOffset = -0.156V, vMeas = 2.358V, iForce = 0.500mA # Test item 6-20- 1- 1

T AMCA: MESE 2296: Signal\_Input/Output\_relay\_test\_Resistance\_F-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5030.113 Ohm, 1.2% >> vOffset = -0.157V, vMeas = 2.358V, iForce = 0.500mA # Test item 6-20- 1- 2

T AMCA: MESE 2296: Signal\_Input/Output\_relay\_test\_Resistance\_FF+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5027.092 Ohm, 0.6% >> vOffset = -0.156V, vMeas = 2.358V, iForce = 0.500mA # Test item 6-20- 1- 3

T AMCA: MESE 2296: Signal\_Input/Output\_relay\_test\_Resistance\_FF-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5028.854 Ohm, 1.0% >> vOffset = -0.156V, vMeas = 2.358V, iForce = 0.500mA # Test item 6-20- 1- 4

T AMCA: MESE 2297: Signal\_Input/Output\_relay\_test\_Resistance\_G+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5015.007 Ohm, 1.8% >> vOffset = -0.067V, vMeas = 2.440V, iForce = 0.500mA # Test item 7-20- 1- 1

T AMCA: MESE 2297: Signal\_Input/Output\_relay\_test\_Resistance\_G-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5015.762 Ohm, 1.6% >> vOffset = -0.068V, vMeas = 2.440V, iForce = 0.500mA # Test item 7-20- 1- 2

T AMCA: MESE 2297: Signal\_Input/Output\_relay\_test\_Resistance\_GG+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5018.280 Ohm, 1.1% >> vOffset = -0.068V, vMeas = 2.441V, iForce = 0.500mA # Test item 7-20- 1- 3

T AMCA: MESE 2297: Signal\_Input/Output\_relay\_test\_Resistance\_GG-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5017.273 Ohm, 1.3% >> vOffset = -0.067V, vMeas = 2.442V, iForce = 0.500mA # Test item 7-20- 1- 4

T AMCA: MESE 2298: Signal\_Input/Output\_relay\_test\_Resistance\_H+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5022.057 Ohm, 0.4% >> vOffset = -0.118V, vMeas = 2.393V, iForce = 0.500mA # Test item 8-20- 1- 1

T AMCA: MESE 2298: Signal\_Input/Output\_relay\_test\_Resistance\_H-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5025.833 Ohm, 0.4% >> vOffset = -0.119V, vMeas = 2.394V, iForce = 0.500mA # Test item 8-20- 1- 2

T AMCA: MESE 2298: Signal\_Input/Output\_relay\_test\_Resistance\_HH+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5023.819 Ohm, 0.0% >> vOffset = -0.118V, vMeas = 2.394V, iForce = 0.500mA # Test item 8-20- 1- 3

T AMCA: MESE 2298: Signal\_Input/Output\_relay\_test\_Resistance\_HH-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5025.330 Ohm, 0.3% >> vOffset = -0.118V, vMeas = 2.394V, iForce = 0.500mA # Test item 8-20- 1- 4

T AMCA: MCE 2290: Common\_trigger\_input\_relay\_test\_Common\_Trigger\_1: R\_exp = 470.000 Ohm, [369.000 ... 571.000 Ohm] : R\_meas = 469.425 Ohm, 0.6% >> vMeas = 1.407V, vOffset = -0.001V, usedUnit = 1 # Test item 0-21- 1- 1

T AMCA: MCE 2290: Common\_trigger\_input\_relay\_test\_Common\_Trigger\_2: R\_exp = 470.000 Ohm, [369.000 ... 571.000 Ohm] : R\_meas = 469.761 Ohm, 0.2% >> vMeas = 1.408V, vOffset = -0.001V, usedUnit = 1 # Test item 0-21- 1- 2

T AMCA: MCE 2300: Unit\_FPGA\_configuration\_test\_Unit\_1: exp = passed: meas = passed # Test item 1- 1- 1- 1

T AMCA: MCE 2300: Unit\_FPGA\_configuration\_test\_Unit\_2: exp = passed: meas = passed # Test item 2- 1- 1- 1

T AMCA: MCE 2300: Unit\_FPGA\_configuration\_test\_Unit\_3: exp = passed: meas = passed # Test item 3- 1- 1- 1

T AMCA: MCE 2300: Unit\_FPGA\_configuration\_test\_Unit\_4: exp = passed: meas = passed # Test item 4- 1- 1- 1

T AMCA: MCE 2300: Unit\_FPGA\_configuration\_test\_Unit\_5: exp = passed: meas = passed # Test item 5- 1- 1- 1

T AMCA: MCE 2300: Unit\_FPGA\_configuration\_test\_Unit\_6: exp = passed: meas = passed # Test item 6- 1- 1- 1

T AMCA: MCE 2300: Unit\_FPGA\_configuration\_test\_Unit\_7: exp = passed: meas = passed # Test item 7- 1- 1- 1

T AMCA: MCE 2300: Unit\_FPGA\_configuration\_test\_Unit\_8: exp = passed: meas = passed # Test item 8- 1- 1- 1

T AMCA: MCE 2300: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Data\_Bus\_Test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 0- 1- 2- 1

T AMCA: MCE 2300: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Data\_Bus\_Test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 1- 2- 2

T AMCA: MCE 2300: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 1- 2- 3

T AMCA: MESE 2301: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 1- 8- 1

T AMCA: MESE 2302: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 1- 8- 1

T AMCA: MESE 2303: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 1- 8- 1

T AMCA: MESE 2304: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 1- 8- 1

T AMCA: MESE 2305: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 1- 8- 1

T AMCA: MESE 2306: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 1- 8- 1

T AMCA: MESE 2307: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 1- 8- 1

T AMCA: MESE 2308: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 1- 8- 1

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_DGND\_B\_(U10144): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.018 V, 3.6% # Test item 0- 1- 3- 1

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_(U10145): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.019 V, 3.9% # Test item 1- 1- 3- 2

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_IF\_(U10145): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.036 V, 7.2% # Test item 2- 1- 3- 3

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_(U811): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.005 V, 1.0% # Test item 3- 1- 3- 4

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.979 V, 1.3% # Test item 0- 1- 3- 5

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.001 V, 9.2% # Test item 1- 1- 3- 6

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_B: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.985 V, 4.8% # Test item 2- 1- 3- 7

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_B: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.025 V, 14.6% # Test item 3- 1- 3- 8

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5VD\_B: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.977 V, 17.4% # Test item 4- 1- 3- 9

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_B: V\_exp = 1.020 V, [0.918 ... 1.122 V] : V\_meas = 1.041 V, 20.3% # Test item 5- 1- 3-10

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_TEMP\_BUSIF: V\_exp = 0.750 V, [0.500 ... 1.000 V] : V\_meas = 0.626 V, 49.7% # Test item 6- 1- 3-11

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5.5VA: V\_exp = 1.050 V, [0.945 ... 1.155 V] : V\_meas = 1.065 V, 14.0% # Test item 7- 1- 3-12

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5.5VA: V\_exp = -1.140 V, [-1.254 ... -1.026 V] : V\_meas = -1.122 V, 15.6% # Test item 8- 1- 3-13

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+10VA: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.999 V, 1.3% # Test item 9- 1- 3-14

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.2VA\_IF: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.996 V, 4.3% # Test item 10- 1- 3-15

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U13: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.979 V, 11.4% # Test item 11- 1- 3-16

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U24: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.983 V, 7.3% # Test item 12- 1- 3-17

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U57: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.983 V, 7.3% # Test item 13- 1- 3-18

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U68: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.985 V, 5.3% # Test item 14- 1- 3-19

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U13: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.010 V, 19.9% # Test item 15- 1- 3-20

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U24: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.014 V, 24.0% # Test item 16- 1- 3-21

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U57: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.011 V, 21.0% # Test item 17- 1- 3-22

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U68: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.008 V, 17.9% # Test item 18- 1- 3-23

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_F\_TG: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.005 V, 33.7% # Test item 19- 1- 3-24

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.5VD\_F\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.980 V, 20.0% # Test item 20- 1- 3-25

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_F: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.004 V, 34.6% # Test item 21- 1- 3-26

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.5VD\_F: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.979 V, 21.0% # Test item 22- 1- 3-27

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5VD\_F: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.955 V, 5.2% # Test item 23- 1- 3-28

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5VA\_F: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.957 V, 3.4% # Test item 24- 1- 3-29

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5VA\_F: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.969 V, 9.6% # Test item 25- 1- 3-30

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VA\_F: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.008 V, 31.0% # Test item 26- 1- 3-31

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.974 V, 6.1% # Test item 27- 1- 3-32

T AMCA: MCE 2300: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_IF\_IF2: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.981 V, 19.0% # Test item 28- 1- 3-33

T AMCA: MCE 2300: Unit\_bus\_data\_line\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 0- 1- 4- 1

T AMCA: MCE 2300: Unit\_bus\_data\_line\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 1- 4- 2

T AMCA: MCE 2300: Unit\_bus\_address/control\_line\_test\_WE\_line\_and\_bit\_0\_of\_addr.\_bus: Reg\_exp = 0x00030002: Reg\_meas = 0x00030002 # Test item 0- 1- 5- 1

T AMCA: MCE 2300: Unit\_bus\_address/control\_line\_test\_OE\_line\_\_unit\_select\_line\_for\_unit\_1--4\_and\_bit\_1\_of\_addr.\_bus: Reg\_exp = 0x0002F004: Reg\_meas = 0x0002F004 # Test item 1- 1- 5- 2

T AMCA: MCE 2300: Unit\_bus\_address/control\_line\_test\_Unit\_select\_line\_for\_unit\_5--8\_and\_bit\_2\_of\_addr.\_bus: Reg\_exp = 0x00020F08: Reg\_meas = 0x00020F08 # Test item 2- 1- 5- 3

T AMCA: MCE 2300: Unit\_bus\_address/control\_line\_test\_Reset: Reg\_exp = 0x00060000: Reg\_meas = 0x00060000 # Test item 3- 1- 5- 4

T AMCA: MCE 2300: Analog\_interface\_module\_EEPROM\_access\_test\_Analog\_Interface\_Module: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 0- 1- 6- 1

T AMCA: MCE 2300: Front\_module\_serial\_bus\_test\_1k\_Ohm\_Rref\_connect\_relay\_on\_test: Reg\_exp = 0x00000003: Reg\_meas = 0x00000003 # Test item 0- 1- 7- 1

T AMCA: MCE 2300: Temperature\_sensor\_test\_START: V\_exp = 0.912 V, [0.450 ... 1.375 V] : V\_meas = 0.808 V, 22.6% >> degree = 34.320degree # Test item 0- 2- 3- 1

T AMCA: MCE 2300: MCLK\_test\_START: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 12.0% >> D\_MCLK\_DC = 0.918V, D\_MCLK\_DC\* = 0.930V # Test item 0- 2- 4- 1

T AMCA: MCE 2300: MCLK\_test\_STOP: V\_exp = 0.300 V, [0.200 ... 19.980 V] : V\_meas = 0.332 V, 0.2% >> D\_MCLK\_DC = 0.757V, D\_MCLK\_DC\* = 1.089V # Test item 0- 2- 4- 2

T AMCA: MCE 2300: DC\_rail\_path\_test: R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1000.833 Ohm, 0.8% # Test item 0- 2- 8- 1

T AMCA: MCE 2300: DC\_rail\_path\_test\_GND\_connection\_switch\_On: R\_exp = 5.000 Ohm, [0.000 ... 10.000 Ohm] : R\_meas = 0.833 Ohm, 83.3% # Test item 0- 2- 8- 2

T AMCA: MESE 2301: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 3- 1- 1

T AMCA: MESE 2302: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 3- 1- 1

T AMCA: MESE 2303: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 3- 1- 1

T AMCA: MESE 2304: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 3- 1- 1

T AMCA: MESE 2305: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 3- 1- 1

T AMCA: MESE 2306: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 3- 1- 1

T AMCA: MESE 2307: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 3- 1- 1

T AMCA: MESE 2308: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 3- 1- 1

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.997 V, 17.9% # Test item 1- 3- 2- 1

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.996 V, 15.8% # Test item 1- 3- 2- 2

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.040 V, 30.2% # Test item 1- 3- 2- 3

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.012 V, 2.0% # Test item 1- 3- 2- 4

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.977 V, 3.1% # Test item 1- 3- 2- 5

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.984 V, 16.0% # Test item 1- 3- 2- 6

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.969 V, 9.6% # Test item 1- 3- 2- 7

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.959 V, 1.3% # Test item 1- 3- 2- 8

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.017 V, 38.3% # Test item 2- 3- 2- 1

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.022 V, 42.3% # Test item 2- 3- 2- 2

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.059 V, 49.0% # Test item 2- 3- 2- 3

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.010 V, 0.0% # Test item 2- 3- 2- 4

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.976 V, 4.1% # Test item 2- 3- 2- 5

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.980 V, 20.0% # Test item 2- 3- 2- 6

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.979 V, 20.1% # Test item 2- 3- 2- 7

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.954 V, 6.5% # Test item 2- 3- 2- 8

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.032 V, 52.6% # Test item 3- 3- 2- 1

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.026 V, 46.4% # Test item 3- 3- 2- 2

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.069 V, 57.9% # Test item 3- 3- 2- 3

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.006 V, 4.0% # Test item 3- 3- 2- 4

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.975 V, 5.1% # Test item 3- 3- 2- 5

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.980 V, 20.0% # Test item 3- 3- 2- 6

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.962 V, 2.3% # Test item 3- 3- 2- 7

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.962 V, 1.8% # Test item 3- 3- 2- 8

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.997 V, 17.9% # Test item 4- 3- 2- 1

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.997 V, 16.8% # Test item 4- 3- 2- 2

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.039 V, 28.2% # Test item 4- 3- 2- 3

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.009 V, 1.0% # Test item 4- 3- 2- 4

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.968 V, 12.2% # Test item 4- 3- 2- 5

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.988 V, 12.0% # Test item 4- 3- 2- 6

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.974 V, 14.8% # Test item 4- 3- 2- 7

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.969 V, 9.1% # Test item 4- 3- 2- 8

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.029 V, 49.5% # Test item 5- 3- 2- 1

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.032 V, 52.6% # Test item 5- 3- 2- 2

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.072 V, 61.9% # Test item 5- 3- 2- 3

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.012 V, 2.0% # Test item 5- 3- 2- 4

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.968 V, 12.2% # Test item 5- 3- 2- 5

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.983 V, 17.0% # Test item 5- 3- 2- 6

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.978 V, 19.0% # Test item 5- 3- 2- 7

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.951 V, 9.6% # Test item 5- 3- 2- 8

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.026 V, 47.4% # Test item 6- 3- 2- 1

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.030 V, 50.5% # Test item 6- 3- 2- 2

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.065 V, 54.0% # Test item 6- 3- 2- 3

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.008 V, 2.0% # Test item 6- 3- 2- 4

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.974 V, 6.1% # Test item 6- 3- 2- 5

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.982 V, 18.0% # Test item 6- 3- 2- 6

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.968 V, 8.6% # Test item 6- 3- 2- 7

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.985 V, 25.8% # Test item 6- 3- 2- 8

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.023 V, 43.4% # Test item 7- 3- 2- 1

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.033 V, 54.6% # Test item 7- 3- 2- 2

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.065 V, 54.0% # Test item 7- 3- 2- 3

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.008 V, 2.0% # Test item 7- 3- 2- 4

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.979 V, 1.0% # Test item 7- 3- 2- 5

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.984 V, 16.0% # Test item 7- 3- 2- 6

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.986 V, 27.3% # Test item 7- 3- 2- 7

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.952 V, 8.6% # Test item 7- 3- 2- 8

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.019 V, 39.3% # Test item 8- 3- 2- 1

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.023 V, 44.4% # Test item 8- 3- 2- 2

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.066 V, 55.0% # Test item 8- 3- 2- 3

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.013 V, 3.0% # Test item 8- 3- 2- 4

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.971 V, 9.2% # Test item 8- 3- 2- 5

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.984 V, 16.0% # Test item 8- 3- 2- 6

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.980 V, 21.1% # Test item 8- 3- 2- 7

T AMCA: MCE 2300: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.945 V, 15.9% # Test item 8- 3- 2- 8

T AMCA: MESE 2301: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 3- 3- 1

T AMCA: MESE 2302: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 3- 3- 1

T AMCA: MESE 2303: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 3- 3- 1

T AMCA: MESE 2304: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 3- 3- 1

T AMCA: MESE 2305: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 3- 3- 1

T AMCA: MESE 2306: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 3- 3- 1

T AMCA: MESE 2307: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 3- 3- 1

T AMCA: MESE 2308: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 3- 3- 1

T AMCA: MESE 2301: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 1- 3- 4- 1

T AMCA: MESE 2301: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 3- 4- 2

T AMCA: MESE 2302: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 2- 3- 4- 1

T AMCA: MESE 2302: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 2- 3- 4- 2

T AMCA: MESE 2303: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 3- 3- 4- 1

T AMCA: MESE 2303: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 3- 3- 4- 2

T AMCA: MESE 2304: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 4- 3- 4- 1

T AMCA: MESE 2304: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 4- 3- 4- 2

T AMCA: MESE 2305: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 5- 3- 4- 1

T AMCA: MESE 2305: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 5- 3- 4- 2

T AMCA: MESE 2306: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 6- 3- 4- 1

T AMCA: MESE 2306: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 6- 3- 4- 2

T AMCA: MESE 2307: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 7- 3- 4- 1

T AMCA: MESE 2307: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 7- 3- 4- 2

T AMCA: MESE 2308: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 8- 3- 4- 1

T AMCA: MESE 2308: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 8- 3- 4- 2

T AMCA: MESE 2301: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 3- 5- 1

T AMCA: MESE 2302: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 3- 5- 1

T AMCA: MESE 2303: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 3- 5- 1

T AMCA: MESE 2304: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 3- 5- 1

T AMCA: MESE 2305: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 3- 5- 1

T AMCA: MESE 2306: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 3- 5- 1

T AMCA: MESE 2307: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 3- 5- 1

T AMCA: MESE 2308: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 3- 5- 1

T AMCA: MESE 2301: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 3- 5- 2

T AMCA: MESE 2302: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 3- 5- 2

T AMCA: MESE 2303: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 3- 5- 2

T AMCA: MESE 2304: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 3- 5- 2

T AMCA: MESE 2305: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 3- 5- 2

T AMCA: MESE 2306: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 3- 5- 2

T AMCA: MESE 2307: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 3- 5- 2

T AMCA: MESE 2308: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 3- 5- 2

T AMCA: MESE 2301: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 3- 5- 3

T AMCA: MESE 2302: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 3- 5- 3

T AMCA: MESE 2303: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 3- 5- 3

T AMCA: MESE 2304: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 3- 5- 3

T AMCA: MESE 2305: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 3- 5- 3

T AMCA: MESE 2306: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 3- 5- 3

T AMCA: MESE 2307: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 3- 5- 3

T AMCA: MESE 2308: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 3- 5- 3

T AMCA: MESE 2301: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 3- 5- 4

T AMCA: MESE 2302: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 3- 5- 4

T AMCA: MESE 2303: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 3- 5- 4

T AMCA: MESE 2304: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 3- 5- 4

T AMCA: MESE 2305: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 3- 5- 4

T AMCA: MESE 2306: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 3- 5- 4

T AMCA: MESE 2307: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 3- 5- 4

T AMCA: MESE 2308: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 3- 5- 4

T AMCA: MESE 2301: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 1- 3- 6- 1

T AMCA: MESE 2302: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 2- 3- 6- 1

T AMCA: MESE 2303: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 3- 3- 6- 1

T AMCA: MESE 2304: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 4- 3- 6- 1

T AMCA: MESE 2305: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 5- 3- 6- 1

T AMCA: MESE 2306: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 6- 3- 6- 1

T AMCA: MESE 2307: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 7- 3- 6- 1

T AMCA: MESE 2308: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 8- 3- 6- 1

T AMCA: MESE 2301: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 1- 3- 6- 2

T AMCA: MESE 2302: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 2- 3- 6- 2

T AMCA: MESE 2303: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 3- 3- 6- 2

T AMCA: MESE 2304: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 4- 3- 6- 2

T AMCA: MESE 2305: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 5- 3- 6- 2

T AMCA: MESE 2306: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 6- 3- 6- 2

T AMCA: MESE 2307: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 7- 3- 6- 2

T AMCA: MESE 2308: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 8- 3- 6- 2

T AMCA: MESE 2301: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 3- 7- 1

T AMCA: MESE 2302: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 3- 7- 1

T AMCA: MESE 2303: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 3- 7- 1

T AMCA: MESE 2304: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 3- 7- 1

T AMCA: MESE 2305: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 3- 7- 1

T AMCA: MESE 2306: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 3- 7- 1

T AMCA: MESE 2307: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 3- 7- 1

T AMCA: MESE 2308: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 3- 7- 1

T AMCA: MESE 2301: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 1-22- 1- 1

T AMCA: MESE 2301: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 1-22- 1- 2

T AMCA: MESE 2302: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 2-22- 1- 1

T AMCA: MESE 2302: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 2-22- 1- 2

T AMCA: MESE 2303: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 3-22- 1- 1

T AMCA: MESE 2303: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 3-22- 1- 2

T AMCA: MESE 2304: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 4-22- 1- 1

T AMCA: MESE 2304: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 4-22- 1- 2

T AMCA: MESE 2305: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 5-22- 1- 1

T AMCA: MESE 2305: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 5-22- 1- 2

T AMCA: MESE 2306: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 6-22- 1- 1

T AMCA: MESE 2306: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 6-22- 1- 2

T AMCA: MESE 2307: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 7-22- 1- 1

T AMCA: MESE 2307: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 7-22- 1- 2

T AMCA: MESE 2308: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 8-22- 1- 1

T AMCA: MESE 2308: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 8-22- 1- 2

T AMCA: MESE 2301: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.886 V, 5.6% # Test item 1- 4- 1- 1

T AMCA: MESE 2301: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.852 V, 6.3% # Test item 1- 4- 1- 2

T AMCA: MESE 2302: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.888 V, 6.3% # Test item 2- 4- 1- 1

T AMCA: MESE 2302: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.849 V, 7.1% # Test item 2- 4- 1- 2

T AMCA: MESE 2303: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.888 V, 6.3% # Test item 3- 4- 1- 1

T AMCA: MESE 2303: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.850 V, 7.0% # Test item 3- 4- 1- 2

T AMCA: MESE 2304: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.887 V, 6.0% # Test item 4- 4- 1- 1

T AMCA: MESE 2304: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.852 V, 6.4% # Test item 4- 4- 1- 2

T AMCA: MESE 2305: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.892 V, 7.8% # Test item 5- 4- 1- 1

T AMCA: MESE 2305: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.846 V, 8.4% # Test item 5- 4- 1- 2

T AMCA: MESE 2306: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.888 V, 6.4% # Test item 6- 4- 1- 1

T AMCA: MESE 2306: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.848 V, 7.6% # Test item 6- 4- 1- 2

T AMCA: MESE 2307: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.887 V, 5.9% # Test item 7- 4- 1- 1

T AMCA: MESE 2307: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.848 V, 7.8% # Test item 7- 4- 1- 2

T AMCA: MESE 2308: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.888 V, 6.1% # Test item 8- 4- 1- 1

T AMCA: MESE 2308: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.848 V, 7.6% # Test item 8- 4- 1- 2

T AMCA: MESE 2301: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.2% # Test item 1- 4- 2- 1

T AMCA: MESE 2301: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.020 V, 20.3% # Test item 1- 4- 2- 2

T AMCA: MESE 2301: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.975 V, 8.4% # Test item 1- 4- 2- 3

T AMCA: MESE 2302: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.018 V, 5.9% # Test item 2- 4- 2- 1

T AMCA: MESE 2302: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.021 V, 20.6% # Test item 2- 4- 2- 2

T AMCA: MESE 2302: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.976 V, 8.0% # Test item 2- 4- 2- 3

T AMCA: MESE 2303: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.6% # Test item 3- 4- 2- 1

T AMCA: MESE 2303: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.020 V, 19.6% # Test item 3- 4- 2- 2

T AMCA: MESE 2303: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.978 V, 7.4% # Test item 3- 4- 2- 3

T AMCA: MESE 2304: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.5% # Test item 4- 4- 2- 1

T AMCA: MESE 2304: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.021 V, 20.5% # Test item 4- 4- 2- 2

T AMCA: MESE 2304: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.976 V, 8.0% # Test item 4- 4- 2- 3

T AMCA: MESE 2305: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.021 V, 6.9% # Test item 5- 4- 2- 1

T AMCA: MESE 2305: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.023 V, 23.4% # Test item 5- 4- 2- 2

T AMCA: MESE 2305: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.974 V, 8.8% # Test item 5- 4- 2- 3

T AMCA: MESE 2306: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.4% # Test item 6- 4- 2- 1

T AMCA: MESE 2306: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.021 V, 21.0% # Test item 6- 4- 2- 2

T AMCA: MESE 2306: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.974 V, 8.6% # Test item 6- 4- 2- 3

T AMCA: MESE 2307: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.2% # Test item 7- 4- 2- 1

T AMCA: MESE 2307: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.019 V, 19.4% # Test item 7- 4- 2- 2

T AMCA: MESE 2307: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.977 V, 7.8% # Test item 7- 4- 2- 3

T AMCA: MESE 2301: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.881 mA, 39.8% # Test item 1- 4- 3- 1

T AMCA: MESE 2301: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.011 mA, 7.5% # Test item 1- 4- 3- 2

T AMCA: MESE 2301: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.844 mA, 34.6% # Test item 1- 4- 3- 3

T AMCA: MESE 2301: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.009 mA, 6.0% # Test item 1- 4- 3- 4

T AMCA: MESE 2302: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.883 mA, 39.0% # Test item 2- 4- 3- 1

T AMCA: MESE 2302: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.012 mA, 7.9% # Test item 2- 4- 3- 2

T AMCA: MESE 2302: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.842 mA, 35.1% # Test item 2- 4- 3- 3

T AMCA: MESE 2302: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.007 mA, 4.4% # Test item 2- 4- 3- 4

T AMCA: MESE 2303: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.881 mA, 39.7% # Test item 3- 4- 3- 1

T AMCA: MESE 2303: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.010 mA, 6.4% # Test item 3- 4- 3- 2

T AMCA: MESE 2303: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.845 mA, 34.5% # Test item 3- 4- 3- 3

T AMCA: MESE 2303: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.009 mA, 5.7% # Test item 3- 4- 3- 4

T AMCA: MESE 2304: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.878 mA, 40.7% # Test item 4- 4- 3- 1

T AMCA: MESE 2304: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.009 mA, 5.9% # Test item 4- 4- 3- 2

T AMCA: MESE 2304: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.844 mA, 34.6% # Test item 4- 4- 3- 3

T AMCA: MESE 2304: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.008 mA, 5.3% # Test item 4- 4- 3- 4

T AMCA: MESE 2305: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.887 mA, 37.8% # Test item 5- 4- 3- 1

T AMCA: MESE 2305: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.012 mA, 7.8% # Test item 5- 4- 3- 2

T AMCA: MESE 2305: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.839 mA, 35.8% # Test item 5- 4- 3- 3

T AMCA: MESE 2305: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.007 mA, 4.9% # Test item 5- 4- 3- 4

T AMCA: MESE 2306: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.881 mA, 39.7% # Test item 6- 4- 3- 1

T AMCA: MESE 2306: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.011 mA, 7.3% # Test item 6- 4- 3- 2

T AMCA: MESE 2306: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.840 mA, 35.5% # Test item 6- 4- 3- 3

T AMCA: MESE 2306: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.007 mA, 4.8% # Test item 6- 4- 3- 4

T AMCA: MESE 2307: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.881 mA, 39.6% # Test item 7- 4- 3- 1

T AMCA: MESE 2307: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.011 mA, 7.0% # Test item 7- 4- 3- 2

T AMCA: MESE 2307: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.841 mA, 35.2% # Test item 7- 4- 3- 3

T AMCA: MESE 2307: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.007 mA, 4.9% # Test item 7- 4- 3- 4

T AMCA: MESE 2308: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.881 mA, 39.7% # Test item 8- 4- 3- 1

T AMCA: MESE 2308: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.010 mA, 6.4% # Test item 8- 4- 3- 2

T AMCA: MESE 2308: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.843 mA, 35.0% # Test item 8- 4- 3- 3

T AMCA: MESE 2308: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.009 mA, 5.7% # Test item 8- 4- 3- 4

T AMCA: MESE 2301: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.988 V, 4.1% # Test item 1- 4- 4- 1

T AMCA: MESE 2301: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.274 V, 7.5% # Test item 1- 4- 4- 2

T AMCA: MESE 2301: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.009 V, 2.0% # Test item 1- 4- 4- 3

T AMCA: MESE 2301: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.272 V, 1.7% # Test item 1- 4- 4- 4

T AMCA: MESE 2302: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.988 V, 3.9% # Test item 2- 4- 4- 1

T AMCA: MESE 2302: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.274 V, 7.1% # Test item 2- 4- 4- 2

T AMCA: MESE 2302: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.015 V, 3.4% # Test item 2- 4- 4- 3

T AMCA: MESE 2302: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.272 V, 1.1% # Test item 2- 4- 4- 4

T AMCA: MESE 2303: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.988 V, 3.9% # Test item 3- 4- 4- 1

T AMCA: MESE 2303: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.273 V, 2.9% # Test item 3- 4- 4- 2

T AMCA: MESE 2303: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.017 V, 3.7% # Test item 3- 4- 4- 3

T AMCA: MESE 2303: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.274 V, 6.6% # Test item 3- 4- 4- 4

T AMCA: MESE 2304: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.989 V, 3.7% # Test item 4- 4- 4- 1

T AMCA: MESE 2304: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.274 V, 6.6% # Test item 4- 4- 4- 2

T AMCA: MESE 2304: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.012 V, 2.6% # Test item 4- 4- 4- 3

T AMCA: MESE 2304: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.273 V, 2.0% # Test item 4- 4- 4- 4

T AMCA: MESE 2305: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.984 V, 5.3% # Test item 5- 4- 4- 1

T AMCA: MESE 2305: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.272 V, 1.5% # Test item 5- 4- 4- 2

T AMCA: MESE 2305: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.020 V, 4.5% # Test item 5- 4- 4- 3

T AMCA: MESE 2305: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.275 V, 11.2% # Test item 5- 4- 4- 4

T AMCA: MESE 2306: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.985 V, 4.9% # Test item 6- 4- 4- 1

T AMCA: MESE 2306: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.274 V, 7.5% # Test item 6- 4- 4- 2

T AMCA: MESE 2306: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.014 V, 3.1% # Test item 6- 4- 4- 3

T AMCA: MESE 2306: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.274 V, 7.5% # Test item 6- 4- 4- 4

T AMCA: MESE 2307: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.986 V, 4.5% # Test item 7- 4- 4- 1

T AMCA: MESE 2307: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.274 V, 7.5% # Test item 7- 4- 4- 2

T AMCA: MESE 2307: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.015 V, 3.3% # Test item 7- 4- 4- 3

T AMCA: MESE 2307: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.274 V, 6.6% # Test item 7- 4- 4- 4

T AMCA: MESE 2308: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.989 V, 3.6% # Test item 8- 4- 4- 1

T AMCA: MESE 2308: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.274 V, 6.2% # Test item 8- 4- 4- 2

T AMCA: MESE 2308: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.017 V, 3.7% # Test item 8- 4- 4- 3

T AMCA: MESE 2308: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.273 V, 4.3% # Test item 8- 4- 4- 4

T AMCA: MESE 2301: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.995 kOhm, 5.2% # Test item 1- 4- 5- 1

T AMCA: MESE 2302: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.996 kOhm, 4.4% # Test item 2- 4- 5- 1

T AMCA: MESE 2303: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.995 kOhm, 4.5% # Test item 3- 4- 5- 1

T AMCA: MESE 2304: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.995 kOhm, 4.5% # Test item 4- 4- 5- 1

T AMCA: MESE 2305: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.994 kOhm, 5.8% # Test item 5- 4- 5- 1

T AMCA: MESE 2306: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.994 kOhm, 5.6% # Test item 6- 4- 5- 1

T AMCA: MESE 2307: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.995 kOhm, 4.9% # Test item 7- 4- 5- 1

T AMCA: MESE 2308: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.996 kOhm, 3.7% # Test item 8- 4- 5- 1

T AMCA: MCE 2300: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_1: R\_exp = 10220.000 Ohm, [9198.000 ... 11242.000 Ohm] : R\_meas = 9982.681 Ohm, 23.2% >> vMeas = 2.851V, vOffset = -0.143V, usedUnit = 1 # Test item 0- 5- 1- 1

T AMCA: MCE 2300: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_1\_(with\_GND): R\_exp = 59.000 Ohm, [49.000 ... 69.000 Ohm] : R\_meas = 58.453 Ohm, 5.5% >> vMeas = 0.180V, vOffset = 0.005V, usedUnit = 1 # Test item 0- 5- 1- 2

T AMCA: MCE 2300: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_2: R\_exp = 10220.000 Ohm, [9198.000 ... 11242.000 Ohm] : R\_meas = 9983.939 Ohm, 23.1% >> vMeas = 2.852V, vOffset = -0.144V, usedUnit = 1 # Test item 0- 5- 1- 3

T AMCA: MCE 2300: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_2\_(with\_GND): R\_exp = 59.000 Ohm, [49.000 ... 69.000 Ohm] : R\_meas = 58.285 Ohm, 7.2% >> vMeas = 0.179V, vOffset = 0.004V, usedUnit = 1 # Test item 0- 5- 1- 4

T AMCA: MCE 2300: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_\_0.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 1

T AMCA: MCE 2300: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_\_1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 2

T AMCA: MCE 2300: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_-1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 3

T AMCA: MCE 2300: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_\_0.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 4

T AMCA: MCE 2300: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_\_1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 5

T AMCA: MCE 2300: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_-1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 6

T AMCA: MESE 2301: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.498 V, 1.5% >> vOffset = -0.010V # Test item 1- 2- 9- 1

T AMCA: MESE 2301: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 20.1% >> vOffset = 0.004V # Test item 1- 2- 9- 2

T AMCA: MESE 2302: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.500 V, 0.3% >> vOffset = -0.012V # Test item 2- 2- 9- 1

T AMCA: MESE 2302: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 20.1% >> vOffset = 0.002V # Test item 2- 2- 9- 2

T AMCA: MESE 2303: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.500 V, 0.1% >> vOffset = -0.013V # Test item 3- 2- 9- 1

T AMCA: MESE 2303: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 22.7% >> vOffset = 0.001V # Test item 3- 2- 9- 2

T AMCA: MESE 2304: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 0.5% >> vOffset = -0.012V # Test item 4- 2- 9- 1

T AMCA: MESE 2304: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 21.4% >> vOffset = 0.003V # Test item 4- 2- 9- 2

T AMCA: MESE 2305: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.500 V, 0.3% >> vOffset = -0.016V # Test item 5- 2- 9- 1

T AMCA: MESE 2305: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 23.9% >> vOffset = 0.005V # Test item 5- 2- 9- 2

T AMCA: MESE 2306: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.498 V, 1.5% >> vOffset = -0.013V # Test item 6- 2- 9- 1

T AMCA: MESE 2306: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 21.4% >> vOffset = 0.005V # Test item 6- 2- 9- 2

T AMCA: MESE 2307: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 0.9% >> vOffset = -0.012V # Test item 7- 2- 9- 1

T AMCA: MESE 2307: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 20.1% >> vOffset = 0.005V # Test item 7- 2- 9- 2

T AMCA: MESE 2308: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.500 V, 0.2% >> vOffset = -0.012V # Test item 8- 2- 9- 1

T AMCA: MESE 2308: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 20.1% >> vOffset = 0.005V # Test item 8- 2- 9- 2

T AMCA: MESE 2301: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 1- 7- 1- 1

T AMCA: MESE 2302: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 2- 7- 1- 1

T AMCA: MESE 2303: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 3- 7- 1- 1

T AMCA: MESE 2304: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 4- 7- 1- 1

T AMCA: MESE 2305: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 5- 7- 1- 1

T AMCA: MESE 2306: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 6- 7- 1- 1

T AMCA: MESE 2307: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 7- 7- 1- 1

T AMCA: MESE 2308: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 8- 7- 1- 1

T AMCA: MESE 2301: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 1- 7- 1- 2

T AMCA: MESE 2302: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 2- 7- 1- 2

T AMCA: MESE 2303: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 3- 7- 1- 2

T AMCA: MESE 2304: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 4- 7- 1- 2

T AMCA: MESE 2305: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 5- 7- 1- 2

T AMCA: MESE 2306: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 6- 7- 1- 2

T AMCA: MESE 2307: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 7- 7- 1- 2

T AMCA: MESE 2308: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 8- 7- 1- 2

T AMCA: MESE 2301: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 1- 7- 2- 1

T AMCA: MESE 2302: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 2- 7- 2- 1

T AMCA: MESE 2303: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 3- 7- 2- 1

T AMCA: MESE 2304: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 4- 7- 2- 1

T AMCA: MESE 2305: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 5- 7- 2- 1

T AMCA: MESE 2306: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 6- 7- 2- 1

T AMCA: MESE 2307: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 7- 7- 2- 1

T AMCA: MESE 2308: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 8- 7- 2- 1

T AMCA: MESE 2301: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 7- 3- 1

T AMCA: MESE 2301: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 7- 3- 2

T AMCA: MESE 2302: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 7- 3- 1

T AMCA: MESE 2302: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 7- 3- 2

T AMCA: MESE 2303: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 7- 3- 1

T AMCA: MESE 2303: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 7- 3- 2

T AMCA: MESE 2304: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 7- 3- 1

T AMCA: MESE 2304: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 7- 3- 2

T AMCA: MESE 2305: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 7- 3- 1

T AMCA: MESE 2305: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 7- 3- 2

T AMCA: MESE 2306: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 7- 3- 1

T AMCA: MESE 2306: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 7- 3- 2

T AMCA: MESE 2307: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 7- 3- 1

T AMCA: MESE 2307: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 7- 3- 2

T AMCA: MESE 2308: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 7- 3- 1

T AMCA: MESE 2308: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 7- 3- 2

T AMCA: MESE 2301: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 1- 8- 1- 1

T AMCA: MESE 2301: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 1- 8- 1- 2

T AMCA: MESE 2301: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 1- 8- 1- 3

T AMCA: MESE 2301: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 4

T AMCA: MESE 2301: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 1- 8- 1- 5

T AMCA: MESE 2301: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 6

T AMCA: MESE 2301: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 1- 8- 1- 7

T AMCA: MESE 2301: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 8

T AMCA: MESE 2301: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 9

T AMCA: MESE 2302: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 2- 8- 1- 1

T AMCA: MESE 2302: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 2- 8- 1- 2

T AMCA: MESE 2302: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 2- 8- 1- 3

T AMCA: MESE 2302: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 4

T AMCA: MESE 2302: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 2- 8- 1- 5

T AMCA: MESE 2302: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 6

T AMCA: MESE 2302: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 2- 8- 1- 7

T AMCA: MESE 2302: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 8

T AMCA: MESE 2302: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 9

T AMCA: MESE 2303: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 3- 8- 1- 1

T AMCA: MESE 2303: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 3- 8- 1- 2

T AMCA: MESE 2303: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 3- 8- 1- 3

T AMCA: MESE 2303: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 4

T AMCA: MESE 2303: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 3- 8- 1- 5

T AMCA: MESE 2303: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 6

T AMCA: MESE 2303: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 3- 8- 1- 7

T AMCA: MESE 2303: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 8

T AMCA: MESE 2303: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 9

T AMCA: MESE 2304: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 4- 8- 1- 1

T AMCA: MESE 2304: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 4- 8- 1- 2

T AMCA: MESE 2304: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 4- 8- 1- 3

T AMCA: MESE 2304: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 4

T AMCA: MESE 2304: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 4- 8- 1- 5

T AMCA: MESE 2304: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 6

T AMCA: MESE 2304: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 4- 8- 1- 7

T AMCA: MESE 2304: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 8

T AMCA: MESE 2304: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 9

T AMCA: MESE 2305: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 5- 8- 1- 1

T AMCA: MESE 2305: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 5- 8- 1- 2

T AMCA: MESE 2305: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 5- 8- 1- 3

T AMCA: MESE 2305: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 4

T AMCA: MESE 2305: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 5- 8- 1- 5

T AMCA: MESE 2305: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 6

T AMCA: MESE 2305: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 5- 8- 1- 7

T AMCA: MESE 2305: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 8

T AMCA: MESE 2305: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 9

T AMCA: MESE 2306: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 6- 8- 1- 1

T AMCA: MESE 2306: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 6- 8- 1- 2

T AMCA: MESE 2306: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 6- 8- 1- 3

T AMCA: MESE 2306: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 4

T AMCA: MESE 2306: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 6- 8- 1- 5

T AMCA: MESE 2306: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 6

T AMCA: MESE 2306: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 6- 8- 1- 7

T AMCA: MESE 2306: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 8

T AMCA: MESE 2306: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 9

T AMCA: MESE 2307: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 7- 8- 1- 1

T AMCA: MESE 2307: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 7- 8- 1- 2

T AMCA: MESE 2307: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 7- 8- 1- 3

T AMCA: MESE 2307: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 4

T AMCA: MESE 2307: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 7- 8- 1- 5

T AMCA: MESE 2307: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 6

T AMCA: MESE 2307: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 7- 8- 1- 7

T AMCA: MESE 2307: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 8

T AMCA: MESE 2307: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 9

T AMCA: MESE 2308: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 8- 8- 1- 1

T AMCA: MESE 2308: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 8- 8- 1- 2

T AMCA: MESE 2308: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 8- 8- 1- 3

T AMCA: MESE 2308: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 4

T AMCA: MESE 2308: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 8- 8- 1- 5

T AMCA: MESE 2308: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 6

T AMCA: MESE 2308: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 8- 8- 1- 7

T AMCA: MESE 2308: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 8

T AMCA: MESE 2308: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 9

T AMCA: MESE 2301: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 1, masterModule = 1, slaveUnit = 3, slaveModule = 1 # Test item 1- 8- 3- 1

T AMCA: MESE 2301: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 1, masterModule = 1, slaveUnit = 3, slaveModule = 1 # Test item 1- 8- 3- 2

T AMCA: MESE 2303: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 3, masterModule = 1, slaveUnit = 2, slaveModule = 2 # Test item 3- 8- 3- 3

T AMCA: MESE 2303: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 3, masterModule = 1, slaveUnit = 2, slaveModule = 2 # Test item 3- 8- 3- 4

T AMCA: MESE 2302: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 2, masterModule = 2, slaveUnit = 4, slaveModule = 2 # Test item 2- 8- 3- 5

T AMCA: MESE 2302: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 2, masterModule = 2, slaveUnit = 4, slaveModule = 2 # Test item 2- 8- 3- 6

T AMCA: MESE 2304: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 4, masterModule = 2, slaveUnit = 5, slaveModule = 3 # Test item 4- 8- 3- 7

T AMCA: MESE 2304: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 4, masterModule = 2, slaveUnit = 5, slaveModule = 3 # Test item 4- 8- 3- 8

T AMCA: MESE 2305: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 5, masterModule = 3, slaveUnit = 7, slaveModule = 3 # Test item 5- 8- 3- 9

T AMCA: MESE 2305: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 5, masterModule = 3, slaveUnit = 7, slaveModule = 3 # Test item 5- 8- 3-10

T AMCA: MESE 2307: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 7, masterModule = 3, slaveUnit = 8, slaveModule = 4 # Test item 7- 8- 3-11

T AMCA: MESE 2307: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 7, masterModule = 3, slaveUnit = 8, slaveModule = 4 # Test item 7- 8- 3-12

T AMCA: MESE 2308: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 8, masterModule = 4, slaveUnit = 6, slaveModule = 4 # Test item 8- 8- 3-13

T AMCA: MESE 2308: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 8, masterModule = 4, slaveUnit = 6, slaveModule = 4 # Test item 8- 8- 3-14

T AMCA: MESE 2306: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 6, masterModule = 4, slaveUnit = 1, slaveModule = 1 # Test item 6- 8- 3-15

T AMCA: MESE 2306: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 6, masterModule = 4, slaveUnit = 1, slaveModule = 1 # Test item 6- 8- 3-16

T AMCA: MESE 2301: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 10.040 ns, 1.3% >> short = 60232, long = 29998 # Test item 1- 8- 6- 1

T AMCA: MESE 2302: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.883 ns, 5.8% >> short = 60181, long = 30222 # Test item 2- 8- 6- 1

T AMCA: MESE 2303: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.933 ns, 3.4% >> short = 60649, long = 30264 # Test item 3- 8- 6- 1

T AMCA: MESE 2304: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.905 ns, 4.8% >> short = 60548, long = 30281 # Test item 4- 8- 6- 1

T AMCA: MESE 2305: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 10.120 ns, 4.0% >> short = 59759, long = 29761 # Test item 5- 8- 6- 1

T AMCA: MESE 2306: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.874 ns, 6.3% >> short = 61431, long = 30548 # Test item 6- 8- 6- 1

T AMCA: MESE 2307: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 10.158 ns, 5.3% >> short = 60721, long = 29941 # Test item 7- 8- 6- 1

T AMCA: MESE 2308: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.827 ns, 8.7% >> short = 60816, long = 30468 # Test item 8- 8- 6- 1

T AMCA: MESE 2301: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 1- 8- 7- 1

T AMCA: MESE 2301: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17998, 50.0% # Test item 1- 8- 7- 2

T AMCA: MESE 2301: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000464E: Reg\_meas = 0x0000464E # Test item 1- 8- 7- 3

T AMCA: MESE 2301: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 8- 7- 4

T AMCA: MESE 2302: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 2- 8- 7- 1

T AMCA: MESE 2302: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17535, 61.6% # Test item 2- 8- 7- 2

T AMCA: MESE 2302: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000447F: Reg\_meas = 0x0000447F # Test item 2- 8- 7- 3

T AMCA: MESE 2302: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 8- 7- 4

T AMCA: MESE 2303: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 3- 8- 7- 1

T AMCA: MESE 2303: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17195, 70.1% # Test item 3- 8- 7- 2

T AMCA: MESE 2303: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000432B: Reg\_meas = 0x0000432B # Test item 3- 8- 7- 3

T AMCA: MESE 2303: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 8- 7- 4

T AMCA: MESE 2304: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 4- 8- 7- 1

T AMCA: MESE 2304: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 16867, 78.3% # Test item 4- 8- 7- 2

T AMCA: MESE 2304: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x000041E3: Reg\_meas = 0x000041E3 # Test item 4- 8- 7- 3

T AMCA: MESE 2304: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 8- 7- 4

T AMCA: MESE 2305: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 5- 8- 7- 1

T AMCA: MESE 2305: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17739, 56.5% # Test item 5- 8- 7- 2

T AMCA: MESE 2305: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000454B: Reg\_meas = 0x0000454B # Test item 5- 8- 7- 3

T AMCA: MESE 2305: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 8- 7- 4

T AMCA: MESE 2306: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 6- 8- 7- 1

T AMCA: MESE 2306: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 16923, 76.9% # Test item 6- 8- 7- 2

T AMCA: MESE 2306: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000421B: Reg\_meas = 0x0000421B # Test item 6- 8- 7- 3

T AMCA: MESE 2306: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 8- 7- 4

T AMCA: MESE 2307: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 7- 8- 7- 1

T AMCA: MESE 2307: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17201, 70.0% # Test item 7- 8- 7- 2

T AMCA: MESE 2307: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004331: Reg\_meas = 0x00004331 # Test item 7- 8- 7- 3

T AMCA: MESE 2307: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 8- 7- 4

T AMCA: MESE 2308: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 8- 8- 7- 1

T AMCA: MESE 2308: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17433, 64.2% # Test item 8- 8- 7- 2

T AMCA: MESE 2308: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004419: Reg\_meas = 0x00004419 # Test item 8- 8- 7- 3

T AMCA: MESE 2308: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 8- 7- 4

T AMCA: MESE 2301: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 9- 1- 1

T AMCA: MESE 2301: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 9- 1- 2

T AMCA: MESE 2301: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 3

T AMCA: MESE 2301: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 4

T AMCA: MESE 2301: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 5

T AMCA: MESE 2301: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 6

T AMCA: MESE 2301: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 7

T AMCA: MESE 2301: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 1- 9- 1- 8

T AMCA: MESE 2301: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 9

T AMCA: MESE 2301: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1-10

T AMCA: MESE 2301: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 1- 9- 1-11

T AMCA: MESE 2301: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 1- 9- 1-12

T AMCA: MESE 2301: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 1- 9- 1-13

T AMCA: MESE 2301: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 9- 1-14

T AMCA: MESE 2302: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 9- 1- 1

T AMCA: MESE 2302: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 9- 1- 2

T AMCA: MESE 2302: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 3

T AMCA: MESE 2302: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 4

T AMCA: MESE 2302: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 5

T AMCA: MESE 2302: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 6

T AMCA: MESE 2302: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 7

T AMCA: MESE 2302: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 2- 9- 1- 8

T AMCA: MESE 2302: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 9

T AMCA: MESE 2302: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1-10

T AMCA: MESE 2302: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 2- 9- 1-11

T AMCA: MESE 2302: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 2- 9- 1-12

T AMCA: MESE 2302: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 2- 9- 1-13

T AMCA: MESE 2302: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 9- 1-14

T AMCA: MESE 2303: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 9- 1- 1

T AMCA: MESE 2303: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 9- 1- 2

T AMCA: MESE 2303: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 3

T AMCA: MESE 2303: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 4

T AMCA: MESE 2303: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 5

T AMCA: MESE 2303: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 6

T AMCA: MESE 2303: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 7

T AMCA: MESE 2303: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 3- 9- 1- 8

T AMCA: MESE 2303: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 9

T AMCA: MESE 2303: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1-10

T AMCA: MESE 2303: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 3- 9- 1-11

T AMCA: MESE 2303: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 3- 9- 1-12

T AMCA: MESE 2303: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 3- 9- 1-13

T AMCA: MESE 2303: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 9- 1-14

T AMCA: MESE 2304: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 9- 1- 1

T AMCA: MESE 2304: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 9- 1- 2

T AMCA: MESE 2304: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 3

T AMCA: MESE 2304: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 4

T AMCA: MESE 2304: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 5

T AMCA: MESE 2304: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 6

T AMCA: MESE 2304: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 7

T AMCA: MESE 2304: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 4- 9- 1- 8

T AMCA: MESE 2304: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 9

T AMCA: MESE 2304: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1-10

T AMCA: MESE 2304: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 4- 9- 1-11

T AMCA: MESE 2304: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 4- 9- 1-12

T AMCA: MESE 2304: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 4- 9- 1-13

T AMCA: MESE 2304: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 9- 1-14

T AMCA: MESE 2305: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 9- 1- 1

T AMCA: MESE 2305: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 9- 1- 2

T AMCA: MESE 2305: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 3

T AMCA: MESE 2305: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 4

T AMCA: MESE 2305: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 5

T AMCA: MESE 2305: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 6

T AMCA: MESE 2305: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 7

T AMCA: MESE 2305: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 5- 9- 1- 8

T AMCA: MESE 2305: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 9

T AMCA: MESE 2305: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1-10

T AMCA: MESE 2305: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 5- 9- 1-11

T AMCA: MESE 2305: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 5- 9- 1-12

T AMCA: MESE 2305: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 5- 9- 1-13

T AMCA: MESE 2305: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 9- 1-14

T AMCA: MESE 2306: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 9- 1- 1

T AMCA: MESE 2306: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 9- 1- 2

T AMCA: MESE 2306: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 3

T AMCA: MESE 2306: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 4

T AMCA: MESE 2306: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 5

T AMCA: MESE 2306: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 6

T AMCA: MESE 2306: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 7

T AMCA: MESE 2306: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 6- 9- 1- 8

T AMCA: MESE 2306: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 9

T AMCA: MESE 2306: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1-10

T AMCA: MESE 2306: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 6- 9- 1-11

T AMCA: MESE 2306: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 6- 9- 1-12

T AMCA: MESE 2306: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 6- 9- 1-13

T AMCA: MESE 2306: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 9- 1-14

T AMCA: MESE 2307: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 9- 1- 1

T AMCA: MESE 2307: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 9- 1- 2

T AMCA: MESE 2307: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 3

T AMCA: MESE 2307: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 4

T AMCA: MESE 2307: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 5

T AMCA: MESE 2307: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 6

T AMCA: MESE 2307: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 7

T AMCA: MESE 2307: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 7- 9- 1- 8

T AMCA: MESE 2307: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 9

T AMCA: MESE 2307: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1-10

T AMCA: MESE 2307: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 7- 9- 1-11

T AMCA: MESE 2307: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 7- 9- 1-12

T AMCA: MESE 2307: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 7- 9- 1-13

T AMCA: MESE 2307: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 9- 1-14

T AMCA: MESE 2308: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 9- 1- 1

T AMCA: MESE 2308: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 9- 1- 2

T AMCA: MESE 2308: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 3

T AMCA: MESE 2308: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 4

T AMCA: MESE 2308: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 5

T AMCA: MESE 2308: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 6

T AMCA: MESE 2308: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 7

T AMCA: MESE 2308: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 8- 9- 1- 8

T AMCA: MESE 2308: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 9

T AMCA: MESE 2308: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1-10

T AMCA: MESE 2308: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 8- 9- 1-11

T AMCA: MESE 2308: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 8- 9- 1-12

T AMCA: MESE 2308: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 8- 9- 1-13

T AMCA: MESE 2308: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 9- 1-14

T AMCA: MESE 2301: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.964 V, 5.5% # Test item 1-17- 1- 1

T AMCA: MESE 2301: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.954 V, 3.6% # Test item 1-17- 1- 2

T AMCA: MESE 2301: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.963 V, 4.5% # Test item 1-17- 1- 3

T AMCA: MESE 2301: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.951 V, 6.4% # Test item 1-17- 1- 4

T AMCA: MESE 2301: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.062 V, 6.7% # Test item 1-17- 1- 5

T AMCA: MESE 2301: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.984 V, 4.5% # Test item 1-17- 1- 6

T AMCA: MESE 2301: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.979 V, 5.5% # Test item 1-17- 1- 7

T AMCA: MESE 2301: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.916 V, 8.4% # Test item 1-17- 1- 8

T AMCA: MESE 2301: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.973 V, 5.5% # Test item 1-17- 1- 9

T AMCA: MESE 2301: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.993 V, 0.0% # Test item 1-17- 1-10

T AMCA: MESE 2301: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.972 V, 3.6% # Test item 1-17- 1-11

T AMCA: MESE 2302: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.964 V, 5.5% # Test item 2-17- 1- 1

T AMCA: MESE 2302: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.962 V, 3.6% # Test item 2-17- 1- 2

T AMCA: MESE 2302: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.947 V, 10.0% # Test item 2-17- 1- 3

T AMCA: MESE 2302: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.958 V, 0.0% # Test item 2-17- 1- 4

T AMCA: MESE 2302: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.075 V, 7.8% # Test item 2-17- 1- 5

T AMCA: MESE 2302: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.985 V, 5.5% # Test item 2-17- 1- 6

T AMCA: MESE 2302: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.970 V, 2.7% # Test item 2-17- 1- 7

T AMCA: MESE 2302: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.906 V, 3.6% # Test item 2-17- 1- 8

T AMCA: MESE 2302: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.974 V, 4.5% # Test item 2-17- 1- 9

T AMCA: MESE 2302: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.993 V, 0.0% # Test item 2-17- 1-10

T AMCA: MESE 2302: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.972 V, 3.6% # Test item 2-17- 1-11

T AMCA: MESE 2303: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.961 V, 2.7% # Test item 3-17- 1- 1

T AMCA: MESE 2303: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.959 V, 0.9% # Test item 3-17- 1- 2

T AMCA: MESE 2303: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.958 V, 0.0% # Test item 3-17- 1- 3

T AMCA: MESE 2303: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.960 V, 1.8% # Test item 3-17- 1- 4

T AMCA: MESE 2303: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.064 V, 3.6% # Test item 3-17- 1- 5

T AMCA: MESE 2303: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.986 V, 6.4% # Test item 3-17- 1- 6

T AMCA: MESE 2303: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.969 V, 3.6% # Test item 3-17- 1- 7

T AMCA: MESE 2303: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.649 V, 14.1% # Test item 3-17- 1- 8

T AMCA: MESE 2303: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.973 V, 5.5% # Test item 3-17- 1- 9

T AMCA: MESE 2303: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.710 V, 16.4% # Test item 3-17- 1-10

T AMCA: MESE 2304: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.957 V, 0.9% # Test item 4-17- 1- 1

T AMCA: MESE 2304: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.952 V, 5.5% # Test item 4-17- 1- 2

T AMCA: MESE 2304: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.958 V, 0.0% # Test item 4-17- 1- 3

T AMCA: MESE 2304: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.953 V, 4.5% # Test item 4-17- 1- 4

T AMCA: MESE 2304: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.059 V, 8.2% # Test item 4-17- 1- 5

T AMCA: MESE 2304: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.981 V, 1.8% # Test item 4-17- 1- 6

T AMCA: MESE 2304: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.973 V, 0.0% # Test item 4-17- 1- 7

T AMCA: MESE 2304: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.643 V, 8.7% # Test item 4-17- 1- 8

T AMCA: MESE 2304: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.976 V, 2.7% # Test item 4-17- 1- 9

T AMCA: MESE 2304: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.708 V, 18.2% # Test item 4-17- 1-10

T AMCA: MESE 2305: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.953 V, 4.5% # Test item 5-17- 1- 1

T AMCA: MESE 2305: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.962 V, 3.6% # Test item 5-17- 1- 2

T AMCA: MESE 2305: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.954 V, 3.6% # Test item 5-17- 1- 3

T AMCA: MESE 2305: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.955 V, 2.7% # Test item 5-17- 1- 4

T AMCA: MESE 2305: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.072 V, 4.4% # Test item 5-17- 1- 5

T AMCA: MESE 2305: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.979 V, 0.0% # Test item 5-17- 1- 6

T AMCA: MESE 2305: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.972 V, 0.9% # Test item 5-17- 1- 7

T AMCA: MESE 2305: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.896 V, 2.2% # Test item 5-17- 1- 8

T AMCA: MESE 2305: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.978 V, 0.9% # Test item 5-17- 1- 9

T AMCA: MESE 2305: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.988 V, 4.5% # Test item 5-17- 1-10

T AMCA: MESE 2305: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.963 V, 11.8% # Test item 5-17- 1-11

T AMCA: MESE 2306: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.951 V, 6.4% # Test item 6-17- 1- 1

T AMCA: MESE 2306: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.954 V, 3.6% # Test item 6-17- 1- 2

T AMCA: MESE 2306: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.952 V, 5.5% # Test item 6-17- 1- 3

T AMCA: MESE 2306: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.954 V, 3.6% # Test item 6-17- 1- 4

T AMCA: MESE 2306: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.064 V, 3.6% # Test item 6-17- 1- 5

T AMCA: MESE 2306: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.983 V, 3.6% # Test item 6-17- 1- 6

T AMCA: MESE 2306: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.972 V, 0.9% # Test item 6-17- 1- 7

T AMCA: MESE 2306: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.631 V, 2.2% # Test item 6-17- 1- 8

T AMCA: MESE 2306: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.981 V, 1.8% # Test item 6-17- 1- 9

T AMCA: MESE 2306: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.711 V, 15.5% # Test item 6-17- 1-10

T AMCA: MESE 2307: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.958 V, 0.0% # Test item 7-17- 1- 1

T AMCA: MESE 2307: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.956 V, 1.8% # Test item 7-17- 1- 2

T AMCA: MESE 2307: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.962 V, 3.6% # Test item 7-17- 1- 3

T AMCA: MESE 2307: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.951 V, 6.4% # Test item 7-17- 1- 4

T AMCA: MESE 2307: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.063 V, 4.5% # Test item 7-17- 1- 5

T AMCA: MESE 2307: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.989 V, 9.1% # Test item 7-17- 1- 6

T AMCA: MESE 2307: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.970 V, 2.7% # Test item 7-17- 1- 7

T AMCA: MESE 2307: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.640 V, 5.9% # Test item 7-17- 1- 8

T AMCA: MESE 2307: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.974 V, 4.5% # Test item 7-17- 1- 9

T AMCA: MESE 2307: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.708 V, 18.2% # Test item 7-17- 1-10

T AMCA: MESE 2308: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.953 V, 4.5% # Test item 8-17- 1- 1

T AMCA: MESE 2308: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.961 V, 2.7% # Test item 8-17- 1- 2

T AMCA: MESE 2308: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.949 V, 8.2% # Test item 8-17- 1- 3

T AMCA: MESE 2308: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.960 V, 1.8% # Test item 8-17- 1- 4

T AMCA: MESE 2308: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.059 V, 10.0% # Test item 8-17- 1- 5

T AMCA: MESE 2308: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.984 V, 4.5% # Test item 8-17- 1- 6

T AMCA: MESE 2308: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.976 V, 2.7% # Test item 8-17- 1- 7

T AMCA: MESE 2308: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.910 V, 5.5% # Test item 8-17- 1- 8

T AMCA: MESE 2308: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.976 V, 2.7% # Test item 8-17- 1- 9

T AMCA: MESE 2308: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.989 V, 3.6% # Test item 8-17- 1-10

T AMCA: MESE 2308: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.968 V, 7.3% # Test item 8-17- 1-11

T AMCA: MESE 2301: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.5% # Test item 1-15- 1- 1

T AMCA: MESE 2301: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.4% # Test item 1-15- 1- 2

T AMCA: MESE 2301: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.6% # Test item 1-15- 1- 3

T AMCA: MESE 2301: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.5% # Test item 1-15- 1- 4

T AMCA: MESE 2301: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.2% # Test item 1-15- 1- 5

T AMCA: MESE 2301: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.7% # Test item 1-15- 1- 6

T AMCA: MESE 2301: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.4% # Test item 1-15- 1- 7

T AMCA: MESE 2301: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.3% # Test item 1-15- 1- 8

T AMCA: MESE 2301: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.6% # Test item 1-15- 1- 9

T AMCA: MESE 2301: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.4% # Test item 1-15- 1-10

T AMCA: MESE 2301: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.3% # Test item 1-15- 1-11

T AMCA: MESE 2301: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.7% # Test item 1-15- 1-12

T AMCA: MESE 2302: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.3% # Test item 2-15- 1- 1

T AMCA: MESE 2302: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.4% # Test item 2-15- 1- 2

T AMCA: MESE 2302: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.4% # Test item 2-15- 1- 3

T AMCA: MESE 2302: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.2% # Test item 2-15- 1- 4

T AMCA: MESE 2302: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.5% # Test item 2-15- 1- 5

T AMCA: MESE 2302: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.4% # Test item 2-15- 1- 6

T AMCA: MESE 2302: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 2-15- 1- 7

T AMCA: MESE 2302: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% # Test item 2-15- 1- 8

T AMCA: MESE 2302: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.014 V, 4.8% # Test item 2-15- 1- 9

T AMCA: MESE 2302: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.7% # Test item 2-15- 1-10

T AMCA: MESE 2302: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.0% # Test item 2-15- 1-11

T AMCA: MESE 2302: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.014 V, 4.8% # Test item 2-15- 1-12

T AMCA: MESE 2303: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.7% # Test item 3-15- 1- 1

T AMCA: MESE 2303: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% # Test item 3-15- 1- 2

T AMCA: MESE 2303: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.015 V, 4.9% # Test item 3-15- 1- 3

T AMCA: MESE 2303: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.7% # Test item 3-15- 1- 4

T AMCA: MESE 2303: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% # Test item 3-15- 1- 5

T AMCA: MESE 2303: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.015 V, 4.9% # Test item 3-15- 1- 6

T AMCA: MESE 2303: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.8% # Test item 3-15- 1- 7

T AMCA: MESE 2303: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.0% # Test item 3-15- 1- 8

T AMCA: MESE 2303: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.5% # Test item 3-15- 1- 9

T AMCA: MESE 2303: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.8% # Test item 3-15- 1-10

T AMCA: MESE 2303: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.2% # Test item 3-15- 1-11

T AMCA: MESE 2303: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.4% # Test item 3-15- 1-12

T AMCA: MESE 2304: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.4% # Test item 4-15- 1- 1

T AMCA: MESE 2304: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.0% # Test item 4-15- 1- 2

T AMCA: MESE 2304: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.009 V, 3.1% # Test item 4-15- 1- 3

T AMCA: MESE 2304: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.4% # Test item 4-15- 1- 4

T AMCA: MESE 2304: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.8% # Test item 4-15- 1- 5

T AMCA: MESE 2304: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.009 V, 3.1% # Test item 4-15- 1- 6

T AMCA: MESE 2304: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.2% # Test item 4-15- 1- 7

T AMCA: MESE 2304: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.8% # Test item 4-15- 1- 8

T AMCA: MESE 2304: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.009 V, 2.9% # Test item 4-15- 1- 9

T AMCA: MESE 2304: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.3% # Test item 4-15- 1-10

T AMCA: MESE 2304: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.8% # Test item 4-15- 1-11

T AMCA: MESE 2304: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.009 V, 2.8% # Test item 4-15- 1-12

T AMCA: MESE 2305: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.7% # Test item 5-15- 1- 1

T AMCA: MESE 2305: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.7% # Test item 5-15- 1- 2

T AMCA: MESE 2305: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.2% # Test item 5-15- 1- 3

T AMCA: MESE 2305: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.5% # Test item 5-15- 1- 4

T AMCA: MESE 2305: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.8% # Test item 5-15- 1- 5

T AMCA: MESE 2305: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.2% # Test item 5-15- 1- 6

T AMCA: MESE 2305: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.018 V, 6.0% # Test item 5-15- 1- 7

T AMCA: MESE 2305: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.4% # Test item 5-15- 1- 8

T AMCA: MESE 2305: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.3% # Test item 5-15- 1- 9

T AMCA: MESE 2305: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.018 V, 5.9% # Test item 5-15- 1-10

T AMCA: MESE 2305: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.2% # Test item 5-15- 1-11

T AMCA: MESE 2305: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.3% # Test item 5-15- 1-12

T AMCA: MESE 2306: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 4.9% # Test item 6-15- 1- 1

T AMCA: MESE 2306: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.4% # Test item 6-15- 1- 2

T AMCA: MESE 2306: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.012 V, 4.0% # Test item 6-15- 1- 3

T AMCA: MESE 2306: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.7% # Test item 6-15- 1- 4

T AMCA: MESE 2306: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.7% # Test item 6-15- 1- 5

T AMCA: MESE 2306: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.012 V, 3.9% # Test item 6-15- 1- 6

T AMCA: MESE 2306: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.4% # Test item 6-15- 1- 7

T AMCA: MESE 2306: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.7% # Test item 6-15- 1- 8

T AMCA: MESE 2306: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.011 V, 3.6% # Test item 6-15- 1- 9

T AMCA: MESE 2306: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.4% # Test item 6-15- 1-10

T AMCA: MESE 2306: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.5% # Test item 6-15- 1-11

T AMCA: MESE 2306: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.011 V, 3.6% # Test item 6-15- 1-12

T AMCA: MESE 2307: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.018 V, 5.9% # Test item 7-15- 1- 1

T AMCA: MESE 2307: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.0% # Test item 7-15- 1- 2

T AMCA: MESE 2307: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.012 V, 3.9% # Test item 7-15- 1- 3

T AMCA: MESE 2307: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.018 V, 5.9% # Test item 7-15- 1- 4

T AMCA: MESE 2307: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.9% # Test item 7-15- 1- 5

T AMCA: MESE 2307: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.012 V, 3.8% # Test item 7-15- 1- 6

T AMCA: MESE 2307: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.7% # Test item 7-15- 1- 7

T AMCA: MESE 2307: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.9% # Test item 7-15- 1- 8

T AMCA: MESE 2307: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.011 V, 3.8% # Test item 7-15- 1- 9

T AMCA: MESE 2307: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.6% # Test item 7-15- 1-10

T AMCA: MESE 2307: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.9% # Test item 7-15- 1-11

T AMCA: MESE 2307: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.011 V, 3.7% # Test item 7-15- 1-12

T AMCA: MESE 2308: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 4.9% # Test item 8-15- 1- 1

T AMCA: MESE 2308: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.1% # Test item 8-15- 1- 2

T AMCA: MESE 2308: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.3% # Test item 8-15- 1- 3

T AMCA: MESE 2308: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.8% # Test item 8-15- 1- 4

T AMCA: MESE 2308: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.1% # Test item 8-15- 1- 5

T AMCA: MESE 2308: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.2% # Test item 8-15- 1- 6

T AMCA: MESE 2308: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 4.9% # Test item 8-15- 1- 7

T AMCA: MESE 2308: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.6% # Test item 8-15- 1- 8

T AMCA: MESE 2308: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.5% # Test item 8-15- 1- 9

T AMCA: MESE 2308: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 4.9% # Test item 8-15- 1-10

T AMCA: MESE 2308: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.9% # Test item 8-15- 1-11

T AMCA: MESE 2308: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.014 V, 4.5% # Test item 8-15- 1-12

T AMCA: MESE 2301: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.886 MOhm, 7.6% # Test item 1-15- 2- 1

T AMCA: MESE 2301: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.847 MOhm, 10.2% # Test item 1-15- 2- 2

T AMCA: MESE 2301: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.864 MOhm, 9.1% # Test item 1-15- 2- 3

T AMCA: MESE 2301: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.783 MOhm, 14.4% # Test item 1-15- 2- 4

T AMCA: MESE 2302: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.869 MOhm, 8.7% # Test item 2-15- 2- 1

T AMCA: MESE 2302: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.888 MOhm, 7.4% # Test item 2-15- 2- 2

T AMCA: MESE 2302: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.806 MOhm, 12.9% # Test item 2-15- 2- 3

T AMCA: MESE 2302: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.825 MOhm, 11.7% # Test item 2-15- 2- 4

T AMCA: MESE 2303: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.807 MOhm, 12.9% # Test item 3-15- 2- 1

T AMCA: MESE 2303: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.846 MOhm, 10.3% # Test item 3-15- 2- 2

T AMCA: MESE 2303: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.748 MOhm, 16.8% # Test item 3-15- 2- 3

T AMCA: MESE 2303: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.760 MOhm, 16.0% # Test item 3-15- 2- 4

T AMCA: MESE 2304: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.834 MOhm, 11.1% # Test item 4-15- 2- 1

T AMCA: MESE 2304: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.793 MOhm, 13.8% # Test item 4-15- 2- 2

T AMCA: MESE 2304: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.789 MOhm, 14.1% # Test item 4-15- 2- 3

T AMCA: MESE 2304: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.767 MOhm, 15.6% # Test item 4-15- 2- 4

T AMCA: MESE 2305: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.817 MOhm, 12.2% # Test item 5-15- 2- 1

T AMCA: MESE 2305: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.893 MOhm, 7.2% # Test item 5-15- 2- 2

T AMCA: MESE 2305: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.767 MOhm, 15.6% # Test item 5-15- 2- 3

T AMCA: MESE 2305: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.806 MOhm, 12.9% # Test item 5-15- 2- 4

T AMCA: MESE 2306: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.848 MOhm, 10.1% # Test item 6-15- 2- 1

T AMCA: MESE 2306: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.797 MOhm, 13.5% # Test item 6-15- 2- 2

T AMCA: MESE 2306: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.741 MOhm, 17.3% # Test item 6-15- 2- 3

T AMCA: MESE 2306: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.744 MOhm, 17.1% # Test item 6-15- 2- 4

T AMCA: MESE 2307: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.927 MOhm, 4.9% # Test item 7-15- 2- 1

T AMCA: MESE 2307: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.892 MOhm, 7.2% # Test item 7-15- 2- 2

T AMCA: MESE 2307: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.828 MOhm, 11.4% # Test item 7-15- 2- 3

T AMCA: MESE 2307: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.897 MOhm, 6.9% # Test item 7-15- 2- 4

T AMCA: MESE 2308: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.887 MOhm, 7.6% # Test item 8-15- 2- 1

T AMCA: MESE 2308: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.842 MOhm, 10.5% # Test item 8-15- 2- 2

T AMCA: MESE 2308: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.802 MOhm, 13.2% # Test item 8-15- 2- 3

T AMCA: MESE 2308: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.823 MOhm, 11.8% # Test item 8-15- 2- 4

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.014V, neg = 0.014V # Test item 1-15- 3- 1

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.678 V, 25.5% >> pos = 2.353V, neg = -2.325V # Test item 1-15- 3- 2

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.014V, neg = 0.014V # Test item 1-15- 3- 3

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.556 V, 27.5% >> pos = 0.792V, neg = -0.764V # Test item 1-15- 3- 4

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.014V, neg = 0.014V # Test item 1-15- 3- 5

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 26.1% >> pos = 0.111V, neg = -0.084V # Test item 1-15- 3- 6

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.014V, neg = -0.014V # Test item 1-15- 3- 7

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.678 V, 25.5% >> pos = 2.325V, neg = -2.353V # Test item 1-15- 3- 8

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.014V, neg = -0.014V # Test item 1-15- 3- 9

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.556 V, 27.5% >> pos = 0.764V, neg = -0.792V # Test item 1-15- 3-10

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.014V, neg = -0.014V # Test item 1-15- 3-11

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 26.1% >> pos = 0.083V, neg = -0.111V # Test item 1-15- 3-12

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.014V, neg = 0.014V # Test item 1-15- 3-13

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.676 V, 25.7% >> pos = 2.353V, neg = -2.324V # Test item 1-15- 3-14

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.014V, neg = 0.014V # Test item 1-15- 3-15

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.556 V, 27.6% >> pos = 0.792V, neg = -0.764V # Test item 1-15- 3-16

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.014V, neg = 0.014V # Test item 1-15- 3-17

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 26.3% >> pos = 0.112V, neg = -0.083V # Test item 1-15- 3-18

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.014V, neg = -0.014V # Test item 1-15- 3-19

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.677 V, 25.6% >> pos = 2.324V, neg = -2.353V # Test item 1-15- 3-20

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.014V, neg = -0.014V # Test item 1-15- 3-21

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.556 V, 27.6% >> pos = 0.764V, neg = -0.792V # Test item 1-15- 3-22

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.014V, neg = -0.014V # Test item 1-15- 3-23

T AMCA: MESE 2301: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 26.2% >> pos = 0.083V, neg = -0.112V # Test item 1-15- 3-24

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 2-15- 3- 1

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.694 V, 22.0% >> pos = 2.364V, neg = -2.330V # Test item 2-15- 3- 2

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 2-15- 3- 3

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.9% >> pos = 0.798V, neg = -0.764V # Test item 2-15- 3- 4

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 2-15- 3- 5

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 22.6% >> pos = 0.115V, neg = -0.081V # Test item 2-15- 3- 6

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 2-15- 3- 7

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.695 V, 21.8% >> pos = 2.330V, neg = -2.365V # Test item 2-15- 3- 8

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 2-15- 3- 9

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.8% >> pos = 0.764V, neg = -0.798V # Test item 2-15- 3-10

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 2-15- 3-11

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 22.6% >> pos = 0.080V, neg = -0.115V # Test item 2-15- 3-12

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.019V, neg = 0.019V # Test item 2-15- 3-13

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.695 V, 21.8% >> pos = 2.366V, neg = -2.329V # Test item 2-15- 3-14

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.019V, neg = 0.019V # Test item 2-15- 3-15

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.8% >> pos = 0.800V, neg = -0.762V # Test item 2-15- 3-16

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.019V, neg = 0.019V # Test item 2-15- 3-17

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 22.4% >> pos = 0.116V, neg = -0.079V # Test item 2-15- 3-18

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.019V, neg = -0.019V # Test item 2-15- 3-19

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.695 V, 21.8% >> pos = 2.329V, neg = -2.366V # Test item 2-15- 3-20

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.019V, neg = -0.019V # Test item 2-15- 3-21

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.8% >> pos = 0.762V, neg = -0.800V # Test item 2-15- 3-22

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.019V, neg = -0.019V # Test item 2-15- 3-23

T AMCA: MESE 2302: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 22.5% >> pos = 0.079V, neg = -0.117V # Test item 2-15- 3-24

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.018V, neg = 0.018V # Test item 3-15- 3- 1

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.686 V, 23.8% >> pos = 2.361V, neg = -2.325V # Test item 3-15- 3- 2

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.018V, neg = 0.018V # Test item 3-15- 3- 3

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.559 V, 25.6% >> pos = 0.797V, neg = -0.762V # Test item 3-15- 3- 4

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.018V, neg = 0.018V # Test item 3-15- 3- 5

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 24.4% >> pos = 0.115V, neg = -0.080V # Test item 3-15- 3- 6

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.018V, neg = -0.018V # Test item 3-15- 3- 7

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.687 V, 23.6% >> pos = 2.325V, neg = -2.361V # Test item 3-15- 3- 8

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.018V, neg = -0.018V # Test item 3-15- 3- 9

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.559 V, 25.6% >> pos = 0.762V, neg = -0.797V # Test item 3-15- 3-10

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.018V, neg = -0.018V # Test item 3-15- 3-11

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 24.4% >> pos = 0.080V, neg = -0.115V # Test item 3-15- 3-12

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.015V, neg = 0.015V # Test item 3-15- 3-13

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.687 V, 23.5% >> pos = 2.359V, neg = -2.328V # Test item 3-15- 3-14

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.016V, neg = 0.016V # Test item 3-15- 3-15

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.559 V, 25.5% >> pos = 0.795V, neg = -0.764V # Test item 3-15- 3-16

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.016V, neg = 0.016V # Test item 3-15- 3-17

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 24.3% >> pos = 0.113V, neg = -0.082V # Test item 3-15- 3-18

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.016V, neg = -0.016V # Test item 3-15- 3-19

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.687 V, 23.6% >> pos = 2.328V, neg = -2.359V # Test item 3-15- 3-20

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.016V, neg = -0.016V # Test item 3-15- 3-21

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.559 V, 25.5% >> pos = 0.764V, neg = -0.795V # Test item 3-15- 3-22

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.016V, neg = -0.016V # Test item 3-15- 3-23

T AMCA: MESE 2303: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 24.1% >> pos = 0.082V, neg = -0.113V # Test item 3-15- 3-24

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.014V, neg = 0.014V # Test item 4-15- 3- 1

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.694 V, 22.1% >> pos = 2.362V, neg = -2.332V # Test item 4-15- 3- 2

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.014V, neg = 0.014V # Test item 4-15- 3- 3

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.561 V, 24.1% >> pos = 0.795V, neg = -0.766V # Test item 4-15- 3- 4

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.014V, neg = 0.014V # Test item 4-15- 3- 5

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 22.8% >> pos = 0.112V, neg = -0.083V # Test item 4-15- 3- 6

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.014V, neg = -0.014V # Test item 4-15- 3- 7

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.694 V, 22.0% >> pos = 2.333V, neg = -2.362V # Test item 4-15- 3- 8

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.014V, neg = -0.014V # Test item 4-15- 3- 9

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.561 V, 24.1% >> pos = 0.766V, neg = -0.795V # Test item 4-15- 3-10

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.015V, neg = -0.015V # Test item 4-15- 3-11

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 22.8% >> pos = 0.083V, neg = -0.112V # Test item 4-15- 3-12

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.015V, neg = 0.015V # Test item 4-15- 3-13

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.695 V, 21.9% >> pos = 2.362V, neg = -2.333V # Test item 4-15- 3-14

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.015V, neg = 0.015V # Test item 4-15- 3-15

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.9% >> pos = 0.796V, neg = -0.766V # Test item 4-15- 3-16

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.015V, neg = 0.015V # Test item 4-15- 3-17

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 22.6% >> pos = 0.112V, neg = -0.083V # Test item 4-15- 3-18

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = -0.015V, neg = -0.015V # Test item 4-15- 3-19

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.694 V, 22.0% >> pos = 2.332V, neg = -2.362V # Test item 4-15- 3-20

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.015V, neg = -0.015V # Test item 4-15- 3-21

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 24.0% >> pos = 0.766V, neg = -0.796V # Test item 4-15- 3-22

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.015V, neg = -0.015V # Test item 4-15- 3-23

T AMCA: MESE 2304: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 22.7% >> pos = 0.083V, neg = -0.113V # Test item 4-15- 3-24

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 5-15- 3- 1

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.675 V, 26.1% >> pos = 2.355V, neg = -2.320V # Test item 5-15- 3- 2

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 5-15- 3- 3

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.555 V, 28.1% >> pos = 0.795V, neg = -0.760V # Test item 5-15- 3- 4

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 5-15- 3- 5

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 26.6% >> pos = 0.115V, neg = -0.080V # Test item 5-15- 3- 6

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 5-15- 3- 7

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.675 V, 26.1% >> pos = 2.320V, neg = -2.355V # Test item 5-15- 3- 8

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 5-15- 3- 9

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.555 V, 28.1% >> pos = 0.760V, neg = -0.795V # Test item 5-15- 3-10

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.018V, neg = -0.018V # Test item 5-15- 3-11

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 26.6% >> pos = 0.080V, neg = -0.115V # Test item 5-15- 3-12

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 5-15- 3-13

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.672 V, 26.6% >> pos = 2.354V, neg = -2.319V # Test item 5-15- 3-14

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 5-15- 3-15

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.554 V, 28.5% >> pos = 0.795V, neg = -0.760V # Test item 5-15- 3-16

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 5-15- 3-17

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 27.2% >> pos = 0.115V, neg = -0.080V # Test item 5-15- 3-18

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 5-15- 3-19

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.674 V, 26.3% >> pos = 2.319V, neg = -2.355V # Test item 5-15- 3-20

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 5-15- 3-21

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.555 V, 28.4% >> pos = 0.760V, neg = -0.795V # Test item 5-15- 3-22

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 5-15- 3-23

T AMCA: MESE 2305: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 27.2% >> pos = 0.080V, neg = -0.115V # Test item 5-15- 3-24

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 6-15- 3- 1

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.690 V, 22.9% >> pos = 2.362V, neg = -2.327V # Test item 6-15- 3- 2

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 6-15- 3- 3

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.560 V, 24.9% >> pos = 0.797V, neg = -0.763V # Test item 6-15- 3- 4

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 6-15- 3- 5

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.3% >> pos = 0.115V, neg = -0.080V # Test item 6-15- 3- 6

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 6-15- 3- 7

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.690 V, 22.9% >> pos = 2.327V, neg = -2.362V # Test item 6-15- 3- 8

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 6-15- 3- 9

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.560 V, 24.9% >> pos = 0.763V, neg = -0.797V # Test item 6-15- 3-10

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 6-15- 3-11

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.2% >> pos = 0.080V, neg = -0.115V # Test item 6-15- 3-12

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 6-15- 3-13

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.691 V, 22.7% >> pos = 2.363V, neg = -2.328V # Test item 6-15- 3-14

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.018V, neg = 0.018V # Test item 6-15- 3-15

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.560 V, 24.7% >> pos = 0.798V, neg = -0.763V # Test item 6-15- 3-16

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.018V, neg = 0.018V # Test item 6-15- 3-17

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.5% >> pos = 0.115V, neg = -0.080V # Test item 6-15- 3-18

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.018V, neg = -0.018V # Test item 6-15- 3-19

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.691 V, 22.8% >> pos = 2.328V, neg = -2.363V # Test item 6-15- 3-20

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.018V, neg = -0.018V # Test item 6-15- 3-21

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.560 V, 24.8% >> pos = 0.762V, neg = -0.798V # Test item 6-15- 3-22

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.018V, neg = -0.018V # Test item 6-15- 3-23

T AMCA: MESE 2306: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.4% >> pos = 0.080V, neg = -0.115V # Test item 6-15- 3-24

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.014V, neg = 0.014V # Test item 7-15- 3- 1

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.670 V, 27.1% >> pos = 2.349V, neg = -2.320V # Test item 7-15- 3- 2

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.014V, neg = 0.014V # Test item 7-15- 3- 3

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.553 V, 29.1% >> pos = 0.791V, neg = -0.762V # Test item 7-15- 3- 4

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.014V, neg = 0.014V # Test item 7-15- 3- 5

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.194 V, 28.1% >> pos = 0.111V, neg = -0.083V # Test item 7-15- 3- 6

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.014V, neg = -0.014V # Test item 7-15- 3- 7

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.669 V, 27.2% >> pos = 2.320V, neg = -2.349V # Test item 7-15- 3- 8

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.014V, neg = -0.014V # Test item 7-15- 3- 9

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.553 V, 29.1% >> pos = 0.762V, neg = -0.791V # Test item 7-15- 3-10

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.014V, neg = -0.014V # Test item 7-15- 3-11

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.194 V, 28.0% >> pos = 0.083V, neg = -0.112V # Test item 7-15- 3-12

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.014V, neg = 0.014V # Test item 7-15- 3-13

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.670 V, 27.0% >> pos = 2.350V, neg = -2.321V # Test item 7-15- 3-14

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.014V, neg = 0.014V # Test item 7-15- 3-15

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.554 V, 28.9% >> pos = 0.791V, neg = -0.762V # Test item 7-15- 3-16

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.015V, neg = 0.015V # Test item 7-15- 3-17

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.194 V, 27.8% >> pos = 0.112V, neg = -0.083V # Test item 7-15- 3-18

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.015V, neg = -0.015V # Test item 7-15- 3-19

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.670 V, 27.0% >> pos = 2.321V, neg = -2.350V # Test item 7-15- 3-20

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.015V, neg = -0.015V # Test item 7-15- 3-21

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.554 V, 28.9% >> pos = 0.762V, neg = -0.791V # Test item 7-15- 3-22

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.015V, neg = -0.015V # Test item 7-15- 3-23

T AMCA: MESE 2307: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.194 V, 27.8% >> pos = 0.083V, neg = -0.112V # Test item 7-15- 3-24

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.016V, neg = 0.016V # Test item 8-15- 3- 1

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.691 V, 22.7% >> pos = 2.362V, neg = -2.329V # Test item 8-15- 3- 2

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.016V, neg = 0.016V # Test item 8-15- 3- 3

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.561 V, 24.5% >> pos = 0.797V, neg = -0.764V # Test item 8-15- 3- 4

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.017V, neg = 0.016V # Test item 8-15- 3- 5

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.0% >> pos = 0.114V, neg = -0.081V # Test item 8-15- 3- 6

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.016V, neg = -0.016V # Test item 8-15- 3- 7

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.691 V, 22.6% >> pos = 2.329V, neg = -2.362V # Test item 8-15- 3- 8

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.016V, neg = -0.016V # Test item 8-15- 3- 9

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.561 V, 24.5% >> pos = 0.764V, neg = -0.797V # Test item 8-15- 3-10

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 8-15- 3-11

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.1% >> pos = 0.081V, neg = -0.114V # Test item 8-15- 3-12

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.017V, neg = 0.017V # Test item 8-15- 3-13

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.691 V, 22.7% >> pos = 2.363V, neg = -2.328V # Test item 8-15- 3-14

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 8-15- 3-15

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.560 V, 24.8% >> pos = 0.797V, neg = -0.763V # Test item 8-15- 3-16

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.017V, neg = 0.017V # Test item 8-15- 3-17

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.2% >> pos = 0.115V, neg = -0.081V # Test item 8-15- 3-18

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 8-15- 3-19

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.691 V, 22.7% >> pos = 2.328V, neg = -2.363V # Test item 8-15- 3-20

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 8-15- 3-21

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.560 V, 24.8% >> pos = 0.763V, neg = -0.797V # Test item 8-15- 3-22

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.017V, neg = -0.017V # Test item 8-15- 3-23

T AMCA: MESE 2308: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 23.2% >> pos = 0.081V, neg = -0.115V # Test item 8-15- 3-24

T AMCA: MESE 2301: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.5% # Test item 1-16- 1- 1

T AMCA: MESE 2301: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.7% # Test item 1-16- 1- 2

T AMCA: MESE 2301: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.7% # Test item 1-16- 1- 3

T AMCA: MESE 2301: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 1-16- 1- 4

T AMCA: MESE 2301: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.7% # Test item 1-16- 1- 5

T AMCA: MESE 2301: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.7% # Test item 1-16- 1- 6

T AMCA: MESE 2301: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.5% # Test item 1-16- 1- 7

T AMCA: MESE 2301: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.8% # Test item 1-16- 1- 8

T AMCA: MESE 2301: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.7% # Test item 1-16- 1- 9

T AMCA: MESE 2301: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.5% # Test item 1-16- 1-10

T AMCA: MESE 2301: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.4% # Test item 1-16- 1-11

T AMCA: MESE 2301: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.7% # Test item 1-16- 1-12

T AMCA: MESE 2302: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 2-16- 1- 1

T AMCA: MESE 2302: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.0% # Test item 2-16- 1- 2

T AMCA: MESE 2302: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.1% # Test item 2-16- 1- 3

T AMCA: MESE 2302: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.7% # Test item 2-16- 1- 4

T AMCA: MESE 2302: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.2% # Test item 2-16- 1- 5

T AMCA: MESE 2302: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.2% # Test item 2-16- 1- 6

T AMCA: MESE 2302: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 2-16- 1- 7

T AMCA: MESE 2302: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.0% # Test item 2-16- 1- 8

T AMCA: MESE 2302: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.2% # Test item 2-16- 1- 9

T AMCA: MESE 2302: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 2-16- 1-10

T AMCA: MESE 2302: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.0% # Test item 2-16- 1-11

T AMCA: MESE 2302: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.2% # Test item 2-16- 1-12

T AMCA: MESE 2303: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.0% # Test item 3-16- 1- 1

T AMCA: MESE 2303: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.2% # Test item 3-16- 1- 2

T AMCA: MESE 2303: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.4% # Test item 3-16- 1- 3

T AMCA: MESE 2303: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.1% # Test item 3-16- 1- 4

T AMCA: MESE 2303: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.3% # Test item 3-16- 1- 5

T AMCA: MESE 2303: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.5% # Test item 3-16- 1- 6

T AMCA: MESE 2303: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.1% # Test item 3-16- 1- 7

T AMCA: MESE 2303: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.3% # Test item 3-16- 1- 8

T AMCA: MESE 2303: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.5% # Test item 3-16- 1- 9

T AMCA: MESE 2303: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.0% # Test item 3-16- 1-10

T AMCA: MESE 2303: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.3% # Test item 3-16- 1-11

T AMCA: MESE 2303: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.4% # Test item 3-16- 1-12

T AMCA: MESE 2304: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.5% # Test item 4-16- 1- 1

T AMCA: MESE 2304: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 10.1% # Test item 4-16- 1- 2

T AMCA: MESE 2304: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 1.1% # Test item 4-16- 1- 3

T AMCA: MESE 2304: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.5% # Test item 4-16- 1- 4

T AMCA: MESE 2304: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 10.1% # Test item 4-16- 1- 5

T AMCA: MESE 2304: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 1.1% # Test item 4-16- 1- 6

T AMCA: MESE 2304: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.4% # Test item 4-16- 1- 7

T AMCA: MESE 2304: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 10.1% # Test item 4-16- 1- 8

T AMCA: MESE 2304: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 1.1% # Test item 4-16- 1- 9

T AMCA: MESE 2304: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.5% # Test item 4-16- 1-10

T AMCA: MESE 2304: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 10.2% # Test item 4-16- 1-11

T AMCA: MESE 2304: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 1.1% # Test item 4-16- 1-12

T AMCA: MESE 2305: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.4% # Test item 5-16- 1- 1

T AMCA: MESE 2305: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 10.4% # Test item 5-16- 1- 2

T AMCA: MESE 2305: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 1.1% # Test item 5-16- 1- 3

T AMCA: MESE 2305: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.4% # Test item 5-16- 1- 4

T AMCA: MESE 2305: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 10.3% # Test item 5-16- 1- 5

T AMCA: MESE 2305: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.996 V, 1.3% # Test item 5-16- 1- 6

T AMCA: MESE 2305: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.5% # Test item 5-16- 1- 7

T AMCA: MESE 2305: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 10.1% # Test item 5-16- 1- 8

T AMCA: MESE 2305: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 1.1% # Test item 5-16- 1- 9

T AMCA: MESE 2305: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.4% # Test item 5-16- 1-10

T AMCA: MESE 2305: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 10.4% # Test item 5-16- 1-11

T AMCA: MESE 2305: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.996 V, 1.2% # Test item 5-16- 1-12

T AMCA: MESE 2306: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.1% # Test item 6-16- 1- 1

T AMCA: MESE 2306: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.2% # Test item 6-16- 1- 2

T AMCA: MESE 2306: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.1% # Test item 6-16- 1- 3

T AMCA: MESE 2306: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.1% # Test item 6-16- 1- 4

T AMCA: MESE 2306: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.4% # Test item 6-16- 1- 5

T AMCA: MESE 2306: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.1% # Test item 6-16- 1- 6

T AMCA: MESE 2306: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.1% # Test item 6-16- 1- 7

T AMCA: MESE 2306: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.5% # Test item 6-16- 1- 8

T AMCA: MESE 2306: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.2% # Test item 6-16- 1- 9

T AMCA: MESE 2306: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.2% # Test item 6-16- 1-10

T AMCA: MESE 2306: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.2% # Test item 6-16- 1-11

T AMCA: MESE 2306: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.1% # Test item 6-16- 1-12

T AMCA: MESE 2307: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.019 V, 6.2% # Test item 7-16- 1- 1

T AMCA: MESE 2307: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 12.1% # Test item 7-16- 1- 2

T AMCA: MESE 2307: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.7% # Test item 7-16- 1- 3

T AMCA: MESE 2307: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.019 V, 6.3% # Test item 7-16- 1- 4

T AMCA: MESE 2307: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 12.3% # Test item 7-16- 1- 5

T AMCA: MESE 2307: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.8% # Test item 7-16- 1- 6

T AMCA: MESE 2307: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.019 V, 6.2% # Test item 7-16- 1- 7

T AMCA: MESE 2307: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 12.2% # Test item 7-16- 1- 8

T AMCA: MESE 2307: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.7% # Test item 7-16- 1- 9

T AMCA: MESE 2307: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.018 V, 6.1% # Test item 7-16- 1-10

T AMCA: MESE 2307: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 12.2% # Test item 7-16- 1-11

T AMCA: MESE 2307: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.8% # Test item 7-16- 1-12

T AMCA: MESE 2308: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.3% # Test item 8-16- 1- 1

T AMCA: MESE 2308: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 7.6% # Test item 8-16- 1- 2

T AMCA: MESE 2308: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.2% # Test item 8-16- 1- 3

T AMCA: MESE 2308: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.3% # Test item 8-16- 1- 4

T AMCA: MESE 2308: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 7.9% # Test item 8-16- 1- 5

T AMCA: MESE 2308: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.3% # Test item 8-16- 1- 6

T AMCA: MESE 2308: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.3% # Test item 8-16- 1- 7

T AMCA: MESE 2308: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 7.7% # Test item 8-16- 1- 8

T AMCA: MESE 2308: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.2% # Test item 8-16- 1- 9

T AMCA: MESE 2308: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.3% # Test item 8-16- 1-10

T AMCA: MESE 2308: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 7.8% # Test item 8-16- 1-11

T AMCA: MESE 2308: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.2% # Test item 8-16- 1-12

T AMCA: MESE 2301: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9854.908 Ohm, 14.5% >> MV = 1.888V, offset = -0.083V # Test item 1-16- 2- 1

T AMCA: MESE 2301: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.774 Ohm, 21.1% >> MV = 0.173V, offset = 0.003V # Test item 1-16- 2- 2

T AMCA: MESE 2301: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.353 Ohm, 14.4% >> MV = 0.137V, offset = 0.004V # Test item 1-16- 2- 3

T AMCA: MESE 2301: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9858.056 Ohm, 14.2% >> MV = 1.878V, offset = -0.093V # Test item 1-16- 2- 4

T AMCA: MESE 2301: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.648 Ohm, 23.3% >> MV = 0.173V, offset = 0.003V # Test item 1-16- 2- 5

T AMCA: MESE 2301: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.228 Ohm, 17.2% >> MV = 0.137V, offset = 0.005V # Test item 1-16- 2- 6

T AMCA: MESE 2302: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9878.826 Ohm, 12.1% >> MV = 1.876V, offset = -0.100V # Test item 2-16- 2- 1

T AMCA: MESE 2302: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.732 Ohm, 21.9% >> MV = 0.172V, offset = 0.001V # Test item 2-16- 2- 2

T AMCA: MESE 2302: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.270 Ohm, 16.2% >> MV = 0.135V, offset = 0.002V # Test item 2-16- 2- 3

T AMCA: MESE 2302: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9876.309 Ohm, 12.4% >> MV = 1.875V, offset = -0.100V # Test item 2-16- 2- 4

T AMCA: MESE 2302: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.984 Ohm, 17.5% >> MV = 0.172V, offset = 0.001V # Test item 2-16- 2- 5

T AMCA: MESE 2302: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.437 Ohm, 12.5% >> MV = 0.136V, offset = 0.003V # Test item 2-16- 2- 6

T AMCA: MESE 2303: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9893.303 Ohm, 10.7% >> MV = 1.880V, offset = -0.099V # Test item 3-16- 2- 1

T AMCA: MESE 2303: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.152 Ohm, 14.6% >> MV = 0.172V, offset = 0.000V # Test item 3-16- 2- 2

T AMCA: MESE 2303: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.689 Ohm, 6.9% >> MV = 0.136V, offset = 0.002V # Test item 3-16- 2- 3

T AMCA: MESE 2303: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9886.380 Ohm, 11.4% >> MV = 1.877V, offset = -0.100V # Test item 3-16- 2- 4

T AMCA: MESE 2303: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.900 Ohm, 19.0% >> MV = 0.171V, offset = 0.000V # Test item 3-16- 2- 5

T AMCA: MESE 2303: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.353 Ohm, 14.4% >> MV = 0.135V, offset = 0.002V # Test item 3-16- 2- 6

T AMCA: MESE 2304: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9876.309 Ohm, 12.4% >> MV = 1.881V, offset = -0.095V # Test item 4-16- 2- 1

T AMCA: MESE 2304: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.438 Ohm, 26.9% >> MV = 0.172V, offset = 0.003V # Test item 4-16- 2- 2

T AMCA: MESE 2304: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.102 Ohm, 20.0% >> MV = 0.136V, offset = 0.004V # Test item 4-16- 2- 3

T AMCA: MESE 2304: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9876.938 Ohm, 12.3% >> MV = 1.879V, offset = -0.097V # Test item 4-16- 2- 4

T AMCA: MESE 2304: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.438 Ohm, 26.9% >> MV = 0.172V, offset = 0.003V # Test item 4-16- 2- 5

T AMCA: MESE 2304: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.060 Ohm, 20.9% >> MV = 0.136V, offset = 0.004V # Test item 4-16- 2- 6

T AMCA: MESE 2305: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9883.861 Ohm, 11.6% >> MV = 1.846V, offset = -0.130V # Test item 5-16- 2- 1

T AMCA: MESE 2305: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.774 Ohm, 21.1% >> MV = 0.174V, offset = 0.004V # Test item 5-16- 2- 2

T AMCA: MESE 2305: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.228 Ohm, 17.2% >> MV = 0.138V, offset = 0.005V # Test item 5-16- 2- 3

T AMCA: MESE 2305: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9886.380 Ohm, 11.4% >> MV = 1.850V, offset = -0.127V # Test item 5-16- 2- 4

T AMCA: MESE 2305: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.774 Ohm, 21.1% >> MV = 0.174V, offset = 0.004V # Test item 5-16- 2- 5

T AMCA: MESE 2305: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.228 Ohm, 17.2% >> MV = 0.138V, offset = 0.005V # Test item 5-16- 2- 6

T AMCA: MESE 2306: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9839.802 Ohm, 16.0% >> MV = 1.867V, offset = -0.101V # Test item 6-16- 2- 1

T AMCA: MESE 2306: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.606 Ohm, 24.0% >> MV = 0.174V, offset = 0.004V # Test item 6-16- 2- 2

T AMCA: MESE 2306: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.270 Ohm, 16.2% >> MV = 0.137V, offset = 0.004V # Test item 6-16- 2- 3

T AMCA: MESE 2306: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9834.138 Ohm, 16.6% >> MV = 1.864V, offset = -0.103V # Test item 6-16- 2- 4

T AMCA: MESE 2306: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.564 Ohm, 24.8% >> MV = 0.174V, offset = 0.004V # Test item 6-16- 2- 5

T AMCA: MESE 2306: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.102 Ohm, 20.0% >> MV = 0.137V, offset = 0.005V # Test item 6-16- 2- 6

T AMCA: MESE 2307: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9866.867 Ohm, 13.3% >> MV = 1.879V, offset = -0.094V # Test item 7-16- 2- 1

T AMCA: MESE 2307: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.732 Ohm, 21.9% >> MV = 0.174V, offset = 0.004V # Test item 7-16- 2- 2

T AMCA: MESE 2307: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.186 Ohm, 18.1% >> MV = 0.138V, offset = 0.006V # Test item 7-16- 2- 3

T AMCA: MESE 2307: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9858.685 Ohm, 14.1% >> MV = 1.878V, offset = -0.094V # Test item 7-16- 2- 4

T AMCA: MESE 2307: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.690 Ohm, 22.6% >> MV = 0.174V, offset = 0.004V # Test item 7-16- 2- 5

T AMCA: MESE 2307: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.102 Ohm, 20.0% >> MV = 0.138V, offset = 0.006V # Test item 7-16- 2- 6

T AMCA: MESE 2308: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9899.598 Ohm, 10.0% >> MV = 1.890V, offset = -0.090V # Test item 8-16- 2- 1

T AMCA: MESE 2308: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.355 Ohm, 28.4% >> MV = 0.173V, offset = 0.004V # Test item 8-16- 2- 2

T AMCA: MESE 2308: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 43.934 Ohm, 23.7% >> MV = 0.136V, offset = 0.005V # Test item 8-16- 2- 3

T AMCA: MESE 2308: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9904.003 Ohm, 9.6% >> MV = 1.890V, offset = -0.091V # Test item 8-16- 2- 4

T AMCA: MESE 2308: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.606 Ohm, 24.0% >> MV = 0.174V, offset = 0.004V # Test item 8-16- 2- 5

T AMCA: MESE 2308: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.060 Ohm, 20.9% >> MV = 0.137V, offset = 0.005V # Test item 8-16- 2- 6

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.054V, neg = 0.054V # Test item 1-16- 3- 1

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.242 V, 13.0% >> pos = 1.679V, neg = -1.562V # Test item 1-16- 3- 2

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = 0.038V, neg = 0.037V # Test item 1-16- 3- 3

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.597 V, 1.6% >> pos = 0.837V, neg = -0.760V # Test item 1-16- 3- 4

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% >> pos = 0.029V, neg = 0.029V # Test item 1-16- 3- 5

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.801 V, 1.1% >> pos = 0.428V, neg = -0.373V # Test item 1-16- 3- 6

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.6% >> pos = 0.022V, neg = 0.021V # Test item 1-16- 3- 7

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 9.9% >> pos = 0.145V, neg = -0.102V # Test item 1-16- 3- 8

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = 0.020V, neg = 0.019V # Test item 1-16- 3- 9

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 4.9% >> pos = 0.069V, neg = -0.030V # Test item 1-16- 3-10

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.1% >> pos = 0.029V, neg = 0.025V # Test item 1-16- 3-11

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.137 V, 19.7% >> pos = 1.594V, neg = -1.543V # Test item 1-16- 3-12

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = 0.014V, neg = 0.013V # Test item 1-16- 3-13

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.576 V, 15.1% >> pos = 0.794V, neg = -0.782V # Test item 1-16- 3-14

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.6% >> pos = -0.003V, neg = -0.001V # Test item 1-16- 3-15

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.789 V, 13.7% >> pos = 0.392V, neg = -0.397V # Test item 1-16- 3-16

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = -0.009V, neg = -0.010V # Test item 1-16- 3-17

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 8.1% >> pos = 0.114V, neg = -0.134V # Test item 1-16- 3-18

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.5% >> pos = -0.011V, neg = -0.013V # Test item 1-16- 3-19

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.6% >> pos = 0.038V, neg = -0.062V # Test item 1-16- 3-20

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.9% >> pos = 0.058V, neg = 0.052V # Test item 1-16- 3-21

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.243 V, 13.3% >> pos = 1.678V, neg = -1.565V # Test item 1-16- 3-22

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = 0.037V, neg = 0.036V # Test item 1-16- 3-23

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.599 V, 0.4% >> pos = 0.837V, neg = -0.762V # Test item 1-16- 3-24

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.029V, neg = 0.029V # Test item 1-16- 3-25

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.802 V, 3.0% >> pos = 0.428V, neg = -0.375V # Test item 1-16- 3-26

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.0% >> pos = 0.022V, neg = 0.020V # Test item 1-16- 3-27

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 8.2% >> pos = 0.146V, neg = -0.102V # Test item 1-16- 3-28

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = 0.019V, neg = 0.020V # Test item 1-16- 3-29

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.9% >> pos = 0.069V, neg = -0.031V # Test item 1-16- 3-30

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.4% >> pos = 0.029V, neg = 0.025V # Test item 1-16- 3-31

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.143 V, 17.8% >> pos = 1.597V, neg = -1.546V # Test item 1-16- 3-32

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.4% >> pos = 0.005V, neg = 0.007V # Test item 1-16- 3-33

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.579 V, 13.0% >> pos = 0.796V, neg = -0.783V # Test item 1-16- 3-34

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.6% >> pos = -0.003V, neg = -0.004V # Test item 1-16- 3-35

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.793 V, 8.3% >> pos = 0.392V, neg = -0.401V # Test item 1-16- 3-36

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = -0.010V, neg = -0.010V # Test item 1-16- 3-37

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 8.2% >> pos = 0.114V, neg = -0.134V # Test item 1-16- 3-38

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = -0.012V, neg = -0.011V # Test item 1-16- 3-39

T AMCA: MESE 2301: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 5.3% >> pos = 0.038V, neg = -0.061V # Test item 1-16- 3-40

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.6% >> pos = 0.071V, neg = 0.070V # Test item 2-16- 3- 1

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.224 V, 7.6% >> pos = 1.679V, neg = -1.545V # Test item 2-16- 3- 2

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = 0.042V, neg = 0.042V # Test item 2-16- 3- 3

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.595 V, 2.9% >> pos = 0.841V, neg = -0.754V # Test item 2-16- 3- 4

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.032V, neg = 0.033V # Test item 2-16- 3- 5

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.799 V, 1.4% >> pos = 0.432V, neg = -0.367V # Test item 2-16- 3- 6

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.025V, neg = 0.025V # Test item 2-16- 3- 7

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 10.4% >> pos = 0.148V, neg = -0.099V # Test item 2-16- 3- 8

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.7% >> pos = 0.020V, neg = 0.022V # Test item 2-16- 3- 9

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 2.6% >> pos = 0.071V, neg = -0.029V # Test item 2-16- 3-10

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.9% >> pos = 0.035V, neg = 0.031V # Test item 2-16- 3-11

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.137 V, 19.7% >> pos = 1.601V, neg = -1.536V # Test item 2-16- 3-12

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = 0.016V, neg = 0.015V # Test item 2-16- 3-13

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.570 V, 18.8% >> pos = 0.794V, neg = -0.776V # Test item 2-16- 3-14

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = -0.005V, neg = -0.005V # Test item 2-16- 3-15

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.790 V, 12.8% >> pos = 0.393V, neg = -0.397V # Test item 2-16- 3-16

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.8% >> pos = -0.013V, neg = -0.011V # Test item 2-16- 3-17

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 11.0% >> pos = 0.112V, neg = -0.135V # Test item 2-16- 3-18

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = -0.014V, neg = -0.013V # Test item 2-16- 3-19

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 8.1% >> pos = 0.035V, neg = -0.063V # Test item 2-16- 3-20

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.0% >> pos = 0.068V, neg = 0.071V # Test item 2-16- 3-21

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.226 V, 8.2% >> pos = 1.681V, neg = -1.545V # Test item 2-16- 3-22

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.1% >> pos = 0.044V, neg = 0.046V # Test item 2-16- 3-23

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.595 V, 3.3% >> pos = 0.840V, neg = -0.755V # Test item 2-16- 3-24

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.033V, neg = 0.033V # Test item 2-16- 3-25

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.798 V, 2.0% >> pos = 0.432V, neg = -0.366V # Test item 2-16- 3-26

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = 0.023V, neg = 0.024V # Test item 2-16- 3-27

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 7.8% >> pos = 0.149V, neg = -0.099V # Test item 2-16- 3-28

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = 0.021V, neg = 0.022V # Test item 2-16- 3-29

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.8% >> pos = 0.072V, neg = -0.028V # Test item 2-16- 3-30

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.7% >> pos = 0.029V, neg = 0.033V # Test item 2-16- 3-31

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.134 V, 20.7% >> pos = 1.600V, neg = -1.534V # Test item 2-16- 3-32

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.0% >> pos = 0.008V, neg = 0.005V # Test item 2-16- 3-33

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.567 V, 20.6% >> pos = 0.791V, neg = -0.776V # Test item 2-16- 3-34

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.2% >> pos = -0.005V, neg = -0.002V # Test item 2-16- 3-35

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.788 V, 15.6% >> pos = 0.388V, neg = -0.400V # Test item 2-16- 3-36

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = -0.011V, neg = -0.012V # Test item 2-16- 3-37

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.246 V, 14.2% >> pos = 0.112V, neg = -0.135V # Test item 2-16- 3-38

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.5% >> pos = -0.013V, neg = -0.014V # Test item 2-16- 3-39

T AMCA: MESE 2302: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 7.1% >> pos = 0.036V, neg = -0.063V # Test item 2-16- 3-40

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.4% >> pos = 0.059V, neg = 0.062V # Test item 3-16- 3- 1

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.145 V, 17.3% >> pos = 1.636V, neg = -1.509V # Test item 3-16- 3- 2

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.0% >> pos = 0.041V, neg = 0.038V # Test item 3-16- 3- 3

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.579 V, 13.3% >> pos = 0.830V, neg = -0.748V # Test item 3-16- 3- 4

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.1% >> pos = 0.027V, neg = 0.024V # Test item 3-16- 3- 5

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.786 V, 17.6% >> pos = 0.420V, neg = -0.366V # Test item 3-16- 3- 6

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = 0.023V, neg = 0.024V # Test item 3-16- 3- 7

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 11.6% >> pos = 0.147V, neg = -0.100V # Test item 3-16- 3- 8

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.7% >> pos = 0.017V, neg = 0.020V # Test item 3-16- 3- 9

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.1% >> pos = 0.070V, neg = -0.030V # Test item 3-16- 3-10

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.5% >> pos = 0.027V, neg = 0.025V # Test item 3-16- 3-11

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.235 V, 10.8% >> pos = 1.641V, neg = -1.594V # Test item 3-16- 3-12

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.9% >> pos = 0.011V, neg = 0.012V # Test item 3-16- 3-13

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.599 V, 0.7% >> pos = 0.801V, neg = -0.798V # Test item 3-16- 3-14

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.4% >> pos = -0.014V, neg = -0.012V # Test item 3-16- 3-15

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.799 V, 1.9% >> pos = 0.389V, neg = -0.410V # Test item 3-16- 3-16

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = -0.013V, neg = -0.013V # Test item 3-16- 3-17

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.256 V, 24.6% >> pos = 0.112V, neg = -0.145V # Test item 3-16- 3-18

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.7% >> pos = -0.019V, neg = -0.017V # Test item 3-16- 3-19

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 5.4% >> pos = 0.033V, neg = -0.066V # Test item 3-16- 3-20

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 4.5% >> pos = 0.060V, neg = 0.064V # Test item 3-16- 3-21

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.145 V, 17.1% >> pos = 1.630V, neg = -1.515V # Test item 3-16- 3-22

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.039V, neg = 0.039V # Test item 3-16- 3-23

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.576 V, 15.0% >> pos = 0.827V, neg = -0.749V # Test item 3-16- 3-24

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.3% >> pos = 0.022V, neg = 0.026V # Test item 3-16- 3-25

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.788 V, 15.4% >> pos = 0.419V, neg = -0.368V # Test item 3-16- 3-26

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.7% >> pos = 0.022V, neg = 0.024V # Test item 3-16- 3-27

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 3.0% >> pos = 0.149V, neg = -0.100V # Test item 3-16- 3-28

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = 0.020V, neg = 0.020V # Test item 3-16- 3-29

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 11.8% >> pos = 0.068V, neg = -0.030V # Test item 3-16- 3-30

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.020V, neg = 0.020V # Test item 3-16- 3-31

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.233 V, 10.3% >> pos = 1.640V, neg = -1.593V # Test item 3-16- 3-32

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.6% >> pos = 0.003V, neg = -0.000V # Test item 3-16- 3-33

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.598 V, 1.4% >> pos = 0.801V, neg = -0.797V # Test item 3-16- 3-34

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.009 V, 8.7% >> pos = -0.020V, neg = -0.011V # Test item 3-16- 3-35

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.799 V, 0.7% >> pos = 0.388V, neg = -0.412V # Test item 3-16- 3-36

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.2% >> pos = -0.013V, neg = -0.015V # Test item 3-16- 3-37

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 6.6% >> pos = 0.110V, neg = -0.138V # Test item 3-16- 3-38

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.5% >> pos = -0.016V, neg = -0.017V # Test item 3-16- 3-39

T AMCA: MESE 2303: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 9.0% >> pos = 0.033V, neg = -0.065V # Test item 3-16- 3-40

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.005 V, 4.7% >> pos = 0.054V, neg = 0.058V # Test item 4-16- 3- 1

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.150 V, 15.8% >> pos = 1.634V, neg = -1.516V # Test item 4-16- 3- 2

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.005 V, 5.1% >> pos = 0.037V, neg = 0.042V # Test item 4-16- 3- 3

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.576 V, 14.9% >> pos = 0.826V, neg = -0.750V # Test item 4-16- 3- 4

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = 0.026V, neg = 0.025V # Test item 4-16- 3- 5

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.789 V, 13.6% >> pos = 0.422V, neg = -0.367V # Test item 4-16- 3- 6

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = 0.021V, neg = 0.020V # Test item 4-16- 3- 7

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 4.1% >> pos = 0.146V, neg = -0.103V # Test item 4-16- 3- 8

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = 0.019V, neg = 0.018V # Test item 4-16- 3- 9

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.1% >> pos = 0.068V, neg = -0.032V # Test item 4-16- 3-10

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 7.8% >> pos = 0.028V, neg = 0.021V # Test item 4-16- 3-11

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.236 V, 11.3% >> pos = 1.642V, neg = -1.594V # Test item 4-16- 3-12

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.5% >> pos = 0.013V, neg = 0.014V # Test item 4-16- 3-13

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.596 V, 2.7% >> pos = 0.803V, neg = -0.793V # Test item 4-16- 3-14

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = -0.008V, neg = -0.008V # Test item 4-16- 3-15

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.800 V, 0.0% >> pos = 0.393V, neg = -0.407V # Test item 4-16- 3-16

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = -0.011V, neg = -0.011V # Test item 4-16- 3-17

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 5.5% >> pos = 0.114V, neg = -0.135V # Test item 4-16- 3-18

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = -0.014V, neg = -0.014V # Test item 4-16- 3-19

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 2.7% >> pos = 0.036V, neg = -0.063V # Test item 4-16- 3-20

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 4.0% >> pos = 0.056V, neg = 0.060V # Test item 4-16- 3-21

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.152 V, 15.0% >> pos = 1.636V, neg = -1.516V # Test item 4-16- 3-22

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.7% >> pos = 0.039V, neg = 0.036V # Test item 4-16- 3-23

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.576 V, 14.8% >> pos = 0.826V, neg = -0.750V # Test item 4-16- 3-24

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = 0.026V, neg = 0.026V # Test item 4-16- 3-25

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.789 V, 13.6% >> pos = 0.421V, neg = -0.368V # Test item 4-16- 3-26

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = 0.021V, neg = 0.021V # Test item 4-16- 3-27

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.246 V, 17.0% >> pos = 0.144V, neg = -0.102V # Test item 4-16- 3-28

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.018V, neg = 0.018V # Test item 4-16- 3-29

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 4.0% >> pos = 0.068V, neg = -0.031V # Test item 4-16- 3-30

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.6% >> pos = 0.029V, neg = 0.026V # Test item 4-16- 3-31

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.231 V, 9.6% >> pos = 1.641V, neg = -1.590V # Test item 4-16- 3-32

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% >> pos = 0.005V, neg = 0.006V # Test item 4-16- 3-33

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.597 V, 1.6% >> pos = 0.801V, neg = -0.796V # Test item 4-16- 3-34

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = -0.006V, neg = -0.007V # Test item 4-16- 3-35

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.798 V, 3.1% >> pos = 0.393V, neg = -0.405V # Test item 4-16- 3-36

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% >> pos = -0.010V, neg = -0.012V # Test item 4-16- 3-37

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 0.5% >> pos = 0.114V, neg = -0.136V # Test item 4-16- 3-38

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = -0.013V, neg = -0.014V # Test item 4-16- 3-39

T AMCA: MESE 2304: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.4% >> pos = 0.037V, neg = -0.064V # Test item 4-16- 3-40

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.4% >> pos = 0.074V, neg = 0.070V # Test item 5-16- 3- 1

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.249 V, 15.2% >> pos = 1.691V, neg = -1.558V # Test item 5-16- 3- 2

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = 0.043V, neg = 0.043V # Test item 5-16- 3- 3

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.597 V, 1.7% >> pos = 0.841V, neg = -0.756V # Test item 5-16- 3- 4

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.3% >> pos = 0.029V, neg = 0.033V # Test item 5-16- 3- 5

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.799 V, 0.7% >> pos = 0.430V, neg = -0.370V # Test item 5-16- 3- 6

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.0% >> pos = 0.024V, neg = 0.019V # Test item 5-16- 3- 7

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 2.7% >> pos = 0.149V, neg = -0.101V # Test item 5-16- 3- 8

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.2% >> pos = 0.020V, neg = 0.021V # Test item 5-16- 3- 9

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.6% >> pos = 0.071V, neg = -0.030V # Test item 5-16- 3-10

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.8% >> pos = 0.032V, neg = 0.034V # Test item 5-16- 3-11

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.141 V, 18.5% >> pos = 1.607V, neg = -1.533V # Test item 5-16- 3-12

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 4.0% >> pos = 0.013V, neg = 0.017V # Test item 5-16- 3-13

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.575 V, 15.7% >> pos = 0.792V, neg = -0.783V # Test item 5-16- 3-14

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = -0.004V, neg = -0.004V # Test item 5-16- 3-15

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.788 V, 15.1% >> pos = 0.391V, neg = -0.396V # Test item 5-16- 3-16

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = -0.011V, neg = -0.013V # Test item 5-16- 3-17

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 7.1% >> pos = 0.113V, neg = -0.135V # Test item 5-16- 3-18

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = -0.014V, neg = -0.015V # Test item 5-16- 3-19

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.0% >> pos = 0.035V, neg = -0.065V # Test item 5-16- 3-20

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.070V, neg = 0.069V # Test item 5-16- 3-21

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.244 V, 13.7% >> pos = 1.692V, neg = -1.552V # Test item 5-16- 3-22

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.6% >> pos = 0.039V, neg = 0.043V # Test item 5-16- 3-23

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.600 V, 0.1% >> pos = 0.843V, neg = -0.757V # Test item 5-16- 3-24

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.1% >> pos = 0.031V, neg = 0.028V # Test item 5-16- 3-25

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.799 V, 1.2% >> pos = 0.432V, neg = -0.367V # Test item 5-16- 3-26

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.7% >> pos = 0.023V, neg = 0.025V # Test item 5-16- 3-27

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 0.8% >> pos = 0.149V, neg = -0.101V # Test item 5-16- 3-28

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = 0.020V, neg = 0.021V # Test item 5-16- 3-29

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 7.9% >> pos = 0.070V, neg = -0.028V # Test item 5-16- 3-30

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = 0.034V, neg = 0.034V # Test item 5-16- 3-31

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.134 V, 20.8% >> pos = 1.604V, neg = -1.529V # Test item 5-16- 3-32

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.6% >> pos = 0.007V, neg = 0.009V # Test item 5-16- 3-33

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.573 V, 17.1% >> pos = 0.795V, neg = -0.777V # Test item 5-16- 3-34

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.0% >> pos = -0.003V, neg = -0.002V # Test item 5-16- 3-35

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.787 V, 15.7% >> pos = 0.391V, neg = -0.396V # Test item 5-16- 3-36

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = -0.011V, neg = -0.012V # Test item 5-16- 3-37

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 6.9% >> pos = 0.113V, neg = -0.136V # Test item 5-16- 3-38

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = -0.015V, neg = -0.015V # Test item 5-16- 3-39

T AMCA: MESE 2305: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 4.8% >> pos = 0.035V, neg = -0.064V # Test item 5-16- 3-40

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = 0.078V, neg = 0.077V # Test item 6-16- 3- 1

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.145 V, 17.1% >> pos = 1.650V, neg = -1.496V # Test item 6-16- 3- 2

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.045V, neg = 0.045V # Test item 6-16- 3- 3

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.574 V, 16.0% >> pos = 0.834V, neg = -0.740V # Test item 6-16- 3- 4

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.4% >> pos = 0.033V, neg = 0.035V # Test item 6-16- 3- 5

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.789 V, 13.4% >> pos = 0.426V, neg = -0.363V # Test item 6-16- 3- 6

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% >> pos = 0.023V, neg = 0.021V # Test item 6-16- 3- 7

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 11.2% >> pos = 0.146V, neg = -0.101V # Test item 6-16- 3- 8

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.019V, neg = 0.020V # Test item 6-16- 3- 9

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.6% >> pos = 0.069V, neg = -0.031V # Test item 6-16- 3-10

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = 0.045V, neg = 0.044V # Test item 6-16- 3-11

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.216 V, 4.9% >> pos = 1.652V, neg = -1.564V # Test item 6-16- 3-12

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.022V, neg = 0.022V # Test item 6-16- 3-13

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.584 V, 10.2% >> pos = 0.806V, neg = -0.777V # Test item 6-16- 3-14

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = 0.002V, neg = 0.001V # Test item 6-16- 3-15

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.806 V, 7.8% >> pos = 0.402V, neg = -0.404V # Test item 6-16- 3-16

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = -0.006V, neg = -0.007V # Test item 6-16- 3-17

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 12.1% >> pos = 0.116V, neg = -0.131V # Test item 6-16- 3-18

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = -0.011V, neg = -0.011V # Test item 6-16- 3-19

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.096 V, 18.6% >> pos = 0.039V, neg = -0.058V # Test item 6-16- 3-20

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.074V, neg = 0.074V # Test item 6-16- 3-21

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.152 V, 14.9% >> pos = 1.654V, neg = -1.498V # Test item 6-16- 3-22

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.044V, neg = 0.045V # Test item 6-16- 3-23

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.572 V, 17.6% >> pos = 0.832V, neg = -0.740V # Test item 6-16- 3-24

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% >> pos = 0.033V, neg = 0.031V # Test item 6-16- 3-25

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.790 V, 12.3% >> pos = 0.428V, neg = -0.362V # Test item 6-16- 3-26

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.2% >> pos = 0.023V, neg = 0.016V # Test item 6-16- 3-27

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 6.7% >> pos = 0.147V, neg = -0.101V # Test item 6-16- 3-28

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.019V, neg = 0.020V # Test item 6-16- 3-29

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 4.6% >> pos = 0.069V, neg = -0.030V # Test item 6-16- 3-30

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.4% >> pos = 0.045V, neg = 0.048V # Test item 6-16- 3-31

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.208 V, 2.6% >> pos = 1.652V, neg = -1.557V # Test item 6-16- 3-32

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% >> pos = 0.017V, neg = 0.015V # Test item 6-16- 3-33

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.586 V, 8.6% >> pos = 0.808V, neg = -0.778V # Test item 6-16- 3-34

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = 0.001V, neg = 0.003V # Test item 6-16- 3-35

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.793 V, 8.6% >> pos = 0.400V, neg = -0.393V # Test item 6-16- 3-36

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = -0.007V, neg = -0.006V # Test item 6-16- 3-37

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 5.0% >> pos = 0.119V, neg = -0.130V # Test item 6-16- 3-38

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% >> pos = -0.010V, neg = -0.010V # Test item 6-16- 3-39

T AMCA: MESE 2306: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 2.1% >> pos = 0.039V, neg = -0.060V # Test item 6-16- 3-40

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.087V, neg = 0.087V # Test item 7-16- 3- 1

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.198 V, 0.5% >> pos = 1.685V, neg = -1.513V # Test item 7-16- 3- 2

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.005 V, 4.9% >> pos = 0.047V, neg = 0.052V # Test item 7-16- 3- 3

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.594 V, 3.7% >> pos = 0.845V, neg = -0.749V # Test item 7-16- 3- 4

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.3% >> pos = 0.030V, neg = 0.033V # Test item 7-16- 3- 5

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.797 V, 3.3% >> pos = 0.431V, neg = -0.367V # Test item 7-16- 3- 6

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.022V, neg = 0.023V # Test item 7-16- 3- 7

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 5.0% >> pos = 0.148V, neg = -0.103V # Test item 7-16- 3- 8

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = 0.018V, neg = 0.018V # Test item 7-16- 3- 9

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.7% >> pos = 0.067V, neg = -0.032V # Test item 7-16- 3-10

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.008 V, 8.3% >> pos = 0.048V, neg = 0.057V # Test item 7-16- 3-11

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.275 V, 23.3% >> pos = 1.692V, neg = -1.582V # Test item 7-16- 3-12

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.3% >> pos = 0.027V, neg = 0.025V # Test item 7-16- 3-13

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.613 V, 8.4% >> pos = 0.828V, neg = -0.785V # Test item 7-16- 3-14

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.003V, neg = 0.003V # Test item 7-16- 3-15

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.816 V, 19.7% >> pos = 0.405V, neg = -0.411V # Test item 7-16- 3-16

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% >> pos = -0.004V, neg = -0.006V # Test item 7-16- 3-17

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 5.9% >> pos = 0.120V, neg = -0.132V # Test item 7-16- 3-18

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.3% >> pos = -0.011V, neg = -0.009V # Test item 7-16- 3-19

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 10.2% >> pos = 0.040V, neg = -0.058V # Test item 7-16- 3-20

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.005 V, 5.4% >> pos = 0.078V, neg = 0.083V # Test item 7-16- 3-21

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.201 V, 0.4% >> pos = 1.683V, neg = -1.519V # Test item 7-16- 3-22

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = 0.049V, neg = 0.048V # Test item 7-16- 3-23

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.596 V, 2.6% >> pos = 0.848V, neg = -0.747V # Test item 7-16- 3-24

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.7% >> pos = 0.032V, neg = 0.030V # Test item 7-16- 3-25

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.798 V, 2.2% >> pos = 0.432V, neg = -0.367V # Test item 7-16- 3-26

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.4% >> pos = 0.022V, neg = 0.019V # Test item 7-16- 3-27

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.252 V, 6.0% >> pos = 0.149V, neg = -0.103V # Test item 7-16- 3-28

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = 0.019V, neg = 0.018V # Test item 7-16- 3-29

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.2% >> pos = 0.068V, neg = -0.032V # Test item 7-16- 3-30

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.009 V, 9.1% >> pos = 0.049V, neg = 0.058V # Test item 7-16- 3-31

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.264 V, 20.1% >> pos = 1.686V, neg = -1.578V # Test item 7-16- 3-32

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.7% >> pos = 0.023V, neg = 0.019V # Test item 7-16- 3-33

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.608 V, 5.2% >> pos = 0.824V, neg = -0.784V # Test item 7-16- 3-34

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 4.3% >> pos = 0.001V, neg = 0.006V # Test item 7-16- 3-35

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.806 V, 8.1% >> pos = 0.406V, neg = -0.400V # Test item 7-16- 3-36

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = -0.006V, neg = -0.006V # Test item 7-16- 3-37

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.254 V, 14.5% >> pos = 0.122V, neg = -0.131V # Test item 7-16- 3-38

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.4% >> pos = -0.012V, neg = -0.010V # Test item 7-16- 3-39

T AMCA: MESE 2307: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 2.6% >> pos = 0.040V, neg = -0.060V # Test item 7-16- 3-40

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.2% >> pos = 0.071V, neg = 0.074V # Test item 8-16- 3- 1

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.233 V, 10.4% >> pos = 1.692V, neg = -1.541V # Test item 8-16- 3- 2

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.007 V, 6.8% >> pos = 0.041V, neg = 0.048V # Test item 8-16- 3- 3

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.597 V, 1.8% >> pos = 0.841V, neg = -0.756V # Test item 8-16- 3- 4

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.3% >> pos = 0.028V, neg = 0.026V # Test item 8-16- 3- 5

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.800 V, 0.4% >> pos = 0.430V, neg = -0.370V # Test item 8-16- 3- 6

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.022V, neg = 0.022V # Test item 8-16- 3- 7

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 0.4% >> pos = 0.147V, neg = -0.103V # Test item 8-16- 3- 8

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.5% >> pos = 0.019V, neg = 0.018V # Test item 8-16- 3- 9

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.101 V, 5.2% >> pos = 0.069V, neg = -0.032V # Test item 8-16- 3-10

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.045V, neg = 0.045V # Test item 8-16- 3-11

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.155 V, 14.1% >> pos = 1.620V, neg = -1.535V # Test item 8-16- 3-12

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.9% >> pos = 0.020V, neg = 0.022V # Test item 8-16- 3-13

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.583 V, 10.7% >> pos = 0.809V, neg = -0.774V # Test item 8-16- 3-14

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = -0.001V, neg = -0.002V # Test item 8-16- 3-15

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.792 V, 10.4% >> pos = 0.395V, neg = -0.396V # Test item 8-16- 3-16

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.007V, neg = -0.007V # Test item 8-16- 3-17

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 9.9% >> pos = 0.116V, neg = -0.131V # Test item 8-16- 3-18

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.8% >> pos = -0.011V, neg = -0.009V # Test item 8-16- 3-19

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 3.3% >> pos = 0.040V, neg = -0.060V # Test item 8-16- 3-20

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.073V, neg = 0.073V # Test item 8-16- 3-21

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.243 V, 13.6% >> pos = 1.694V, neg = -1.549V # Test item 8-16- 3-22

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.8% >> pos = 0.048V, neg = 0.041V # Test item 8-16- 3-23

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.594 V, 3.9% >> pos = 0.841V, neg = -0.753V # Test item 8-16- 3-24

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.027V, neg = 0.027V # Test item 8-16- 3-25

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.801 V, 1.0% >> pos = 0.429V, neg = -0.372V # Test item 8-16- 3-26

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.021V, neg = 0.022V # Test item 8-16- 3-27

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 1.2% >> pos = 0.147V, neg = -0.103V # Test item 8-16- 3-28

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.5% >> pos = 0.018V, neg = 0.017V # Test item 8-16- 3-29

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.1% >> pos = 0.068V, neg = -0.032V # Test item 8-16- 3-30

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = 0.042V, neg = 0.042V # Test item 8-16- 3-31

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.159 V, 12.8% >> pos = 1.621V, neg = -1.538V # Test item 8-16- 3-32

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.6% >> pos = 0.013V, neg = 0.014V # Test item 8-16- 3-33

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.577 V, 14.4% >> pos = 0.805V, neg = -0.772V # Test item 8-16- 3-34

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% >> pos = 0.002V, neg = -0.000V # Test item 8-16- 3-35

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.790 V, 12.5% >> pos = 0.394V, neg = -0.396V # Test item 8-16- 3-36

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = -0.007V, neg = -0.007V # Test item 8-16- 3-37

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 6.6% >> pos = 0.117V, neg = -0.131V # Test item 8-16- 3-38

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = -0.010V, neg = -0.010V # Test item 8-16- 3-39

T AMCA: MESE 2308: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 6.1% >> pos = 0.038V, neg = -0.060V # Test item 8-16- 3-40

T AMCA: MESE 2301: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.785 V, 15.1% >> POS = 0.817V, NEG = 0.032V # Test item 1-16- 4- 1

T AMCA: MESE 2301: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.766 V, 34.4% >> POS = 0.800V, NEG = 0.034V # Test item 1-16- 4- 2

T AMCA: MESE 2301: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.746 V, 53.5% >> POS = 0.770V, NEG = 0.024V # Test item 1-16- 4- 3

T AMCA: MESE 2301: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.800 V, 0.1% >> POS = 0.837V, NEG = 0.037V # Test item 1-16- 4- 4

T AMCA: MESE 2302: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.781 V, 19.4% >> POS = 0.820V, NEG = 0.039V # Test item 2-16- 4- 1

T AMCA: MESE 2302: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.764 V, 35.5% >> POS = 0.802V, NEG = 0.038V # Test item 2-16- 4- 2

T AMCA: MESE 2302: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.747 V, 53.3% >> POS = 0.777V, NEG = 0.030V # Test item 2-16- 4- 3

T AMCA: MESE 2302: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.800 V, 0.3% >> POS = 0.842V, NEG = 0.041V # Test item 2-16- 4- 4

T AMCA: MESE 2303: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.774 V, 25.9% >> POS = 0.808V, NEG = 0.034V # Test item 3-16- 4- 1

T AMCA: MESE 2303: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.753 V, 47.0% >> POS = 0.786V, NEG = 0.033V # Test item 3-16- 4- 2

T AMCA: MESE 2303: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.734 V, 65.5% >> POS = 0.761V, NEG = 0.027V # Test item 3-16- 4- 3

T AMCA: MESE 2303: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.785 V, 15.5% >> POS = 0.825V, NEG = 0.041V # Test item 3-16- 4- 4

T AMCA: MESE 2304: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.772 V, 28.3% >> POS = 0.804V, NEG = 0.033V # Test item 4-16- 4- 1

T AMCA: MESE 2304: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.755 V, 45.0% >> POS = 0.788V, NEG = 0.033V # Test item 4-16- 4- 2

T AMCA: MESE 2304: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.737 V, 63.0% >> POS = 0.759V, NEG = 0.022V # Test item 4-16- 4- 3

T AMCA: MESE 2304: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.789 V, 10.6% >> POS = 0.826V, NEG = 0.037V # Test item 4-16- 4- 4

T AMCA: MESE 2305: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.786 V, 14.2% >> POS = 0.826V, NEG = 0.040V # Test item 5-16- 4- 1

T AMCA: MESE 2305: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.769 V, 31.3% >> POS = 0.809V, NEG = 0.041V # Test item 5-16- 4- 2

T AMCA: MESE 2305: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.749 V, 50.9% >> POS = 0.778V, NEG = 0.029V # Test item 5-16- 4- 3

T AMCA: MESE 2305: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.800 V, 0.1% >> POS = 0.844V, NEG = 0.044V # Test item 5-16- 4- 4

T AMCA: MESE 2306: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.772 V, 27.6% >> POS = 0.814V, NEG = 0.041V # Test item 6-16- 4- 1

T AMCA: MESE 2306: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.752 V, 47.8% >> POS = 0.793V, NEG = 0.041V # Test item 6-16- 4- 2

T AMCA: MESE 2306: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.736 V, 64.1% >> POS = 0.767V, NEG = 0.031V # Test item 6-16- 4- 3

T AMCA: MESE 2306: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.787 V, 13.4% >> POS = 0.832V, NEG = 0.046V # Test item 6-16- 4- 4

T AMCA: MESE 2307: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.776 V, 24.3% >> POS = 0.821V, NEG = 0.046V # Test item 7-16- 4- 1

T AMCA: MESE 2307: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.771 V, 29.4% >> POS = 0.819V, NEG = 0.048V # Test item 7-16- 4- 2

T AMCA: MESE 2307: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.746 V, 53.9% >> POS = 0.781V, NEG = 0.034V # Test item 7-16- 4- 3

T AMCA: MESE 2307: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.796 V, 3.6% >> POS = 0.845V, NEG = 0.049V # Test item 7-16- 4- 4

T AMCA: MESE 2308: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.785 V, 14.8% >> POS = 0.825V, NEG = 0.039V # Test item 8-16- 4- 1

T AMCA: MESE 2308: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.765 V, 35.2% >> POS = 0.804V, NEG = 0.039V # Test item 8-16- 4- 2

T AMCA: MESE 2308: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.749 V, 51.0% >> POS = 0.779V, NEG = 0.030V # Test item 8-16- 4- 3

T AMCA: MESE 2308: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.798 V, 1.8% >> POS = 0.841V, NEG = 0.043V # Test item 8-16- 4- 4

T AMCA: MESE 2301: Signal\_Input/Output\_relay\_test\_Resistance\_A+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5018.028 Ohm, 1.2% >> vOffset = -0.060V, vMeas = 2.449V, iForce = 0.500mA # Test item 1-20- 1- 1

T AMCA: MESE 2301: Signal\_Input/Output\_relay\_test\_Resistance\_A-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5017.776 Ohm, 1.2% >> vOffset = -0.060V, vMeas = 2.449V, iForce = 0.500mA # Test item 1-20- 1- 2

T AMCA: MESE 2301: Signal\_Input/Output\_relay\_test\_Resistance\_AA+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1002.045 Ohm, 2.0% >> vOffset = -0.007V, vMeas = 0.494V, iForce = 0.500mA # Test item 1-20- 1- 3

T AMCA: MESE 2301: Signal\_Input/Output\_relay\_test\_Resistance\_AA-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1003.304 Ohm, 3.3% >> vOffset = -0.007V, vMeas = 0.495V, iForce = 0.500mA # Test item 1-20- 1- 4

T AMCA: MESE 2302: Signal\_Input/Output\_relay\_test\_Resistance\_B+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5025.078 Ohm, 0.2% >> vOffset = -0.066V, vMeas = 2.447V, iForce = 0.500mA # Test item 2-20- 1- 1

T AMCA: MESE 2302: Signal\_Input/Output\_relay\_test\_Resistance\_B-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5025.833 Ohm, 0.4% >> vOffset = -0.066V, vMeas = 2.447V, iForce = 0.500mA # Test item 2-20- 1- 2

T AMCA: MESE 2302: Signal\_Input/Output\_relay\_test\_Resistance\_BB+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1004.562 Ohm, 4.6% >> vOffset = -0.010V, vMeas = 0.492V, iForce = 0.500mA # Test item 2-20- 1- 3

T AMCA: MESE 2302: Signal\_Input/Output\_relay\_test\_Resistance\_BB-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1004.562 Ohm, 4.6% >> vOffset = -0.010V, vMeas = 0.493V, iForce = 0.500mA # Test item 2-20- 1- 4

T AMCA: MESE 2303: Signal\_Input/Output\_relay\_test\_Resistance\_C+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5027.092 Ohm, 0.6% >> vOffset = -0.066V, vMeas = 2.448V, iForce = 0.500mA # Test item 3-20- 1- 1

T AMCA: MESE 2303: Signal\_Input/Output\_relay\_test\_Resistance\_C-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5028.351 Ohm, 0.9% >> vOffset = -0.066V, vMeas = 2.448V, iForce = 0.500mA # Test item 3-20- 1- 2

T AMCA: MESE 2303: Signal\_Input/Output\_relay\_test\_Resistance\_CC+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1006.577 Ohm, 6.6% >> vOffset = -0.011V, vMeas = 0.493V, iForce = 0.500mA # Test item 3-20- 1- 3

T AMCA: MESE 2303: Signal\_Input/Output\_relay\_test\_Resistance\_CC-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1002.548 Ohm, 2.5% >> vOffset = -0.010V, vMeas = 0.491V, iForce = 0.500mA # Test item 3-20- 1- 4

T AMCA: MESE 2304: Signal\_Input/Output\_relay\_test\_Resistance\_D+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5023.819 Ohm, 0.0% >> vOffset = -0.063V, vMeas = 2.449V, iForce = 0.500mA # Test item 4-20- 1- 1

T AMCA: MESE 2304: Signal\_Input/Output\_relay\_test\_Resistance\_D-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5022.057 Ohm, 0.4% >> vOffset = -0.063V, vMeas = 2.448V, iForce = 0.500mA # Test item 4-20- 1- 2

T AMCA: MESE 2304: Signal\_Input/Output\_relay\_test\_Resistance\_DD+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1002.296 Ohm, 2.3% >> vOffset = -0.008V, vMeas = 0.493V, iForce = 0.500mA # Test item 4-20- 1- 3

T AMCA: MESE 2304: Signal\_Input/Output\_relay\_test\_Resistance\_DD-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1001.541 Ohm, 1.5% >> vOffset = -0.008V, vMeas = 0.493V, iForce = 0.500mA # Test item 4-20- 1- 4

T AMCA: MESE 2305: Signal\_Input/Output\_relay\_test\_Resistance\_E+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5025.330 Ohm, 0.3% >> vOffset = -0.082V, vMeas = 2.430V, iForce = 0.500mA # Test item 5-20- 1- 1

T AMCA: MESE 2305: Signal\_Input/Output\_relay\_test\_Resistance\_E-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5026.840 Ohm, 0.6% >> vOffset = -0.083V, vMeas = 2.431V, iForce = 0.500mA # Test item 5-20- 1- 2

T AMCA: MESE 2305: Signal\_Input/Output\_relay\_test\_Resistance\_EE+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5027.092 Ohm, 0.6% >> vOffset = -0.082V, vMeas = 2.431V, iForce = 0.500mA # Test item 5-20- 1- 3

T AMCA: MESE 2305: Signal\_Input/Output\_relay\_test\_Resistance\_EE-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5026.588 Ohm, 0.5% >> vOffset = -0.083V, vMeas = 2.431V, iForce = 0.500mA # Test item 5-20- 1- 4

T AMCA: MESE 2306: Signal\_Input/Output\_relay\_test\_Resistance\_F+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5012.489 Ohm, 2.3% >> vOffset = -0.068V, vMeas = 2.438V, iForce = 0.500mA # Test item 6-20- 1- 1

T AMCA: MESE 2306: Signal\_Input/Output\_relay\_test\_Resistance\_F-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5013.748 Ohm, 2.0% >> vOffset = -0.069V, vMeas = 2.438V, iForce = 0.500mA # Test item 6-20- 1- 2

T AMCA: MESE 2306: Signal\_Input/Output\_relay\_test\_Resistance\_FF+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5014.755 Ohm, 1.8% >> vOffset = -0.069V, vMeas = 2.438V, iForce = 0.500mA # Test item 6-20- 1- 3

T AMCA: MESE 2306: Signal\_Input/Output\_relay\_test\_Resistance\_FF-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5014.252 Ohm, 1.9% >> vOffset = -0.069V, vMeas = 2.438V, iForce = 0.500mA # Test item 6-20- 1- 4

T AMCA: MESE 2307: Signal\_Input/Output\_relay\_test\_Resistance\_G+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5020.042 Ohm, 0.8% >> vOffset = -0.064V, vMeas = 2.446V, iForce = 0.500mA # Test item 7-20- 1- 1

T AMCA: MESE 2307: Signal\_Input/Output\_relay\_test\_Resistance\_G-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5018.532 Ohm, 1.1% >> vOffset = -0.064V, vMeas = 2.446V, iForce = 0.500mA # Test item 7-20- 1- 2

T AMCA: MESE 2307: Signal\_Input/Output\_relay\_test\_Resistance\_GG+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5019.539 Ohm, 0.9% >> vOffset = -0.064V, vMeas = 2.446V, iForce = 0.500mA # Test item 7-20- 1- 3

T AMCA: MESE 2307: Signal\_Input/Output\_relay\_test\_Resistance\_GG-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5019.539 Ohm, 0.9% >> vOffset = -0.064V, vMeas = 2.446V, iForce = 0.500mA # Test item 7-20- 1- 4

T AMCA: MESE 2308: Signal\_Input/Output\_relay\_test\_Resistance\_H+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5030.365 Ohm, 1.3% >> vOffset = -0.063V, vMeas = 2.452V, iForce = 0.500mA # Test item 8-20- 1- 1

T AMCA: MESE 2308: Signal\_Input/Output\_relay\_test\_Resistance\_H-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5031.875 Ohm, 1.6% >> vOffset = -0.063V, vMeas = 2.453V, iForce = 0.500mA # Test item 8-20- 1- 2

T AMCA: MESE 2308: Signal\_Input/Output\_relay\_test\_Resistance\_HH+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5031.120 Ohm, 1.4% >> vOffset = -0.063V, vMeas = 2.452V, iForce = 0.500mA # Test item 8-20- 1- 3

T AMCA: MESE 2308: Signal\_Input/Output\_relay\_test\_Resistance\_HH-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5029.861 Ohm, 1.2% >> vOffset = -0.063V, vMeas = 2.452V, iForce = 0.500mA # Test item 8-20- 1- 4

T AMCA: MCE 2300: Common\_trigger\_input\_relay\_test\_Common\_Trigger\_1: R\_exp = 470.000 Ohm, [369.000 ... 571.000 Ohm] : R\_meas = 469.593 Ohm, 0.4% >> vMeas = 1.408V, vOffset = -0.001V, usedUnit = 1 # Test item 0-21- 1- 1

T AMCA: MCE 2300: Common\_trigger\_input\_relay\_test\_Common\_Trigger\_2: R\_exp = 470.000 Ohm, [369.000 ... 571.000 Ohm] : R\_meas = 469.971 Ohm, 0.0% >> vMeas = 1.410V, vOffset = -0.000V, usedUnit = 1 # Test item 0-21- 1- 2

T AMCA: MCE 2310: Unit\_FPGA\_configuration\_test\_Unit\_1: exp = passed: meas = passed # Test item 1- 1- 1- 1

T AMCA: MCE 2310: Unit\_FPGA\_configuration\_test\_Unit\_2: exp = passed: meas = passed # Test item 2- 1- 1- 1

T AMCA: MCE 2310: Unit\_FPGA\_configuration\_test\_Unit\_3: exp = passed: meas = passed # Test item 3- 1- 1- 1

T AMCA: MCE 2310: Unit\_FPGA\_configuration\_test\_Unit\_4: exp = passed: meas = passed # Test item 4- 1- 1- 1

T AMCA: MCE 2310: Unit\_FPGA\_configuration\_test\_Unit\_5: exp = passed: meas = passed # Test item 5- 1- 1- 1

T AMCA: MCE 2310: Unit\_FPGA\_configuration\_test\_Unit\_6: exp = passed: meas = passed # Test item 6- 1- 1- 1

T AMCA: MCE 2310: Unit\_FPGA\_configuration\_test\_Unit\_7: exp = passed: meas = passed # Test item 7- 1- 1- 1

T AMCA: MCE 2310: Unit\_FPGA\_configuration\_test\_Unit\_8: exp = passed: meas = passed # Test item 8- 1- 1- 1

T AMCA: MCE 2310: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Data\_Bus\_Test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 0- 1- 2- 1

T AMCA: MCE 2310: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Data\_Bus\_Test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 1- 2- 2

T AMCA: MCE 2310: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 1- 2- 3

T AMCA: MESE 2311: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 1- 8- 1

T AMCA: MESE 2312: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 1- 8- 1

T AMCA: MESE 2313: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 1- 8- 1

T AMCA: MESE 2314: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 1- 8- 1

T AMCA: MESE 2315: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 1- 8- 1

T AMCA: MESE 2316: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 1- 8- 1

T AMCA: MESE 2317: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 1- 8- 1

T AMCA: MESE 2318: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 1- 8- 1

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_DGND\_B\_(U10144): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.020 V, 3.9% # Test item 0- 1- 3- 1

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_(U10145): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.018 V, 3.6% # Test item 1- 1- 3- 2

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_IF\_(U10145): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.035 V, 7.0% # Test item 2- 1- 3- 3

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_(U811): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.006 V, 1.2% # Test item 3- 1- 3- 4

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.979 V, 1.5% # Test item 0- 1- 3- 5

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 0.990 V, 19.3% # Test item 1- 1- 3- 6

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_B: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.997 V, 17.9% # Test item 2- 1- 3- 7

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_B: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.029 V, 18.3% # Test item 3- 1- 3- 8

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5VD\_B: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.986 V, 26.6% # Test item 4- 1- 3- 9

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_B: V\_exp = 1.020 V, [0.918 ... 1.122 V] : V\_meas = 1.036 V, 15.2% # Test item 5- 1- 3-10

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_TEMP\_BUSIF: V\_exp = 0.750 V, [0.500 ... 1.000 V] : V\_meas = 0.632 V, 47.0% # Test item 6- 1- 3-11

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5.5VA: V\_exp = 1.050 V, [0.945 ... 1.155 V] : V\_meas = 1.054 V, 4.0% # Test item 7- 1- 3-12

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5.5VA: V\_exp = -1.140 V, [-1.254 ... -1.026 V] : V\_meas = -1.118 V, 19.5% # Test item 8- 1- 3-13

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+10VA: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 1.007 V, 7.2% # Test item 9- 1- 3-14

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.2VA\_IF: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.996 V, 3.7% # Test item 10- 1- 3-15

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U13: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.984 V, 5.6% # Test item 11- 1- 3-16

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U24: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.983 V, 7.6% # Test item 12- 1- 3-17

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U57: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.984 V, 5.6% # Test item 13- 1- 3-18

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U68: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.990 V, 0.5% # Test item 14- 1- 3-19

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U13: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.013 V, 22.7% # Test item 15- 1- 3-20

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U24: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.009 V, 18.7% # Test item 16- 1- 3-21

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U57: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.013 V, 22.7% # Test item 17- 1- 3-22

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U68: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 1.016 V, 26.8% # Test item 18- 1- 3-23

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_F\_TG: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 0.997 V, 41.3% # Test item 19- 1- 3-24

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.5VD\_F\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.988 V, 12.0% # Test item 20- 1- 3-25

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_F: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.003 V, 35.6% # Test item 21- 1- 3-26

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.5VD\_F: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.978 V, 22.0% # Test item 22- 1- 3-27

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5VD\_F: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.951 V, 9.4% # Test item 23- 1- 3-28

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5VA\_F: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.956 V, 4.4% # Test item 24- 1- 3-29

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5VA\_F: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.961 V, 1.3% # Test item 25- 1- 3-30

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VA\_F: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.005 V, 33.9% # Test item 26- 1- 3-31

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.966 V, 14.3% # Test item 27- 1- 3-32

T AMCA: MCE 2310: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_IF\_IF2: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.974 V, 26.0% # Test item 28- 1- 3-33

T AMCA: MCE 2310: Unit\_bus\_data\_line\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 0- 1- 4- 1

T AMCA: MCE 2310: Unit\_bus\_data\_line\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 1- 4- 2

T AMCA: MCE 2310: Unit\_bus\_address/control\_line\_test\_WE\_line\_and\_bit\_0\_of\_addr.\_bus: Reg\_exp = 0x00030002: Reg\_meas = 0x00030002 # Test item 0- 1- 5- 1

T AMCA: MCE 2310: Unit\_bus\_address/control\_line\_test\_OE\_line\_\_unit\_select\_line\_for\_unit\_1--4\_and\_bit\_1\_of\_addr.\_bus: Reg\_exp = 0x0002F004: Reg\_meas = 0x0002F004 # Test item 1- 1- 5- 2

T AMCA: MCE 2310: Unit\_bus\_address/control\_line\_test\_Unit\_select\_line\_for\_unit\_5--8\_and\_bit\_2\_of\_addr.\_bus: Reg\_exp = 0x00020F08: Reg\_meas = 0x00020F08 # Test item 2- 1- 5- 3

T AMCA: MCE 2310: Unit\_bus\_address/control\_line\_test\_Reset: Reg\_exp = 0x00060000: Reg\_meas = 0x00060000 # Test item 3- 1- 5- 4

T AMCA: MCE 2310: Analog\_interface\_module\_EEPROM\_access\_test\_Analog\_Interface\_Module: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 0- 1- 6- 1

T AMCA: MCE 2310: Front\_module\_serial\_bus\_test\_1k\_Ohm\_Rref\_connect\_relay\_on\_test: Reg\_exp = 0x00000003: Reg\_meas = 0x00000003 # Test item 0- 1- 7- 1

T AMCA: MCE 2310: Temperature\_sensor\_test\_START: V\_exp = 0.912 V, [0.450 ... 1.375 V] : V\_meas = 0.794 V, 25.6% >> degree = 33.760degree # Test item 0- 2- 3- 1

T AMCA: MCE 2310: MCLK\_test\_START: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.014 V, 14.0% >> D\_MCLK\_DC = 0.917V, D\_MCLK\_DC\* = 0.931V # Test item 0- 2- 4- 1

T AMCA: MCE 2310: MCLK\_test\_STOP: V\_exp = 0.300 V, [0.200 ... 19.980 V] : V\_meas = 0.334 V, 0.2% >> D\_MCLK\_DC = 0.758V, D\_MCLK\_DC\* = 1.092V # Test item 0- 2- 4- 2

T AMCA: MCE 2310: DC\_rail\_path\_test: R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1000.750 Ohm, 0.8% # Test item 0- 2- 8- 1

T AMCA: MCE 2310: DC\_rail\_path\_test\_GND\_connection\_switch\_On: R\_exp = 5.000 Ohm, [0.000 ... 10.000 Ohm] : R\_meas = 0.917 Ohm, 81.7% # Test item 0- 2- 8- 2

T AMCA: MESE 2311: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 3- 1- 1

T AMCA: MESE 2312: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 3- 1- 1

T AMCA: MESE 2313: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 3- 1- 1

T AMCA: MESE 2314: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 3- 1- 1

T AMCA: MESE 2315: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 3- 1- 1

T AMCA: MESE 2316: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 3- 1- 1

T AMCA: MESE 2317: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 3- 1- 1

T AMCA: MESE 2318: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 3- 1- 1

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.993 V, 12.8% # Test item 1- 3- 2- 1

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.989 V, 8.7% # Test item 1- 3- 2- 2

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.034 V, 23.3% # Test item 1- 3- 2- 3

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.001 V, 8.9% # Test item 1- 3- 2- 4

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.977 V, 3.1% # Test item 1- 3- 2- 5

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.981 V, 19.0% # Test item 1- 3- 2- 6

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.969 V, 9.1% # Test item 1- 3- 2- 7

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.952 V, 8.1% # Test item 1- 3- 2- 8

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.026 V, 46.4% # Test item 2- 3- 2- 1

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.028 V, 48.5% # Test item 2- 3- 2- 2

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.065 V, 54.0% # Test item 2- 3- 2- 3

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.010 V, 0.0% # Test item 2- 3- 2- 4

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.978 V, 2.0% # Test item 2- 3- 2- 5

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.981 V, 19.0% # Test item 2- 3- 2- 6

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.979 V, 19.5% # Test item 2- 3- 2- 7

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.951 V, 9.1% # Test item 2- 3- 2- 8

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.026 V, 46.4% # Test item 3- 3- 2- 1

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.027 V, 47.4% # Test item 3- 3- 2- 2

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.063 V, 52.0% # Test item 3- 3- 2- 3

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.005 V, 5.0% # Test item 3- 3- 2- 4

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.975 V, 5.1% # Test item 3- 3- 2- 5

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.975 V, 25.0% # Test item 3- 3- 2- 6

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.978 V, 18.5% # Test item 3- 3- 2- 7

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.950 V, 10.2% # Test item 3- 3- 2- 8

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.988 V, 7.7% # Test item 4- 3- 2- 1

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.004 V, 24.0% # Test item 4- 3- 2- 2

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.045 V, 34.2% # Test item 4- 3- 2- 3

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.002 V, 7.9% # Test item 4- 3- 2- 4

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.976 V, 4.1% # Test item 4- 3- 2- 5

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.980 V, 20.0% # Test item 4- 3- 2- 6

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.963 V, 2.9% # Test item 4- 3- 2- 7

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.956 V, 3.9% # Test item 4- 3- 2- 8

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.033 V, 53.6% # Test item 5- 3- 2- 1

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.036 V, 56.6% # Test item 5- 3- 2- 2

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.075 V, 63.9% # Test item 5- 3- 2- 3

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.004 V, 5.9% # Test item 5- 3- 2- 4

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.966 V, 14.3% # Test item 5- 3- 2- 5

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.977 V, 23.0% # Test item 5- 3- 2- 6

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.980 V, 20.6% # Test item 5- 3- 2- 7

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.938 V, 22.7% # Test item 5- 3- 2- 8

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.015 V, 35.2% # Test item 6- 3- 2- 1

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.033 V, 53.6% # Test item 6- 3- 2- 2

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.072 V, 60.9% # Test item 6- 3- 2- 3

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.008 V, 2.0% # Test item 6- 3- 2- 4

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.981 V, 1.0% # Test item 6- 3- 2- 5

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.982 V, 18.0% # Test item 6- 3- 2- 6

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.970 V, 10.2% # Test item 6- 3- 2- 7

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.991 V, 32.6% # Test item 6- 3- 2- 8

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.028 V, 48.5% # Test item 7- 3- 2- 1

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.030 V, 50.5% # Test item 7- 3- 2- 2

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.073 V, 61.9% # Test item 7- 3- 2- 3

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.013 V, 3.0% # Test item 7- 3- 2- 4

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.977 V, 3.1% # Test item 7- 3- 2- 5

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.985 V, 15.0% # Test item 7- 3- 2- 6

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.979 V, 19.5% # Test item 7- 3- 2- 7

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.946 V, 14.3% # Test item 7- 3- 2- 8

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.024 V, 44.4% # Test item 8- 3- 2- 1

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.035 V, 55.6% # Test item 8- 3- 2- 2

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.071 V, 59.9% # Test item 8- 3- 2- 3

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.004 V, 5.9% # Test item 8- 3- 2- 4

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.974 V, 6.1% # Test item 8- 3- 2- 5

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.982 V, 18.0% # Test item 8- 3- 2- 6

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.983 V, 23.7% # Test item 8- 3- 2- 7

T AMCA: MCE 2310: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.942 V, 18.5% # Test item 8- 3- 2- 8

T AMCA: MESE 2311: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 3- 3- 1

T AMCA: MESE 2312: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 3- 3- 1

T AMCA: MESE 2313: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 3- 3- 1

T AMCA: MESE 2314: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 3- 3- 1

T AMCA: MESE 2315: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 3- 3- 1

T AMCA: MESE 2316: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 3- 3- 1

T AMCA: MESE 2317: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 3- 3- 1

T AMCA: MESE 2318: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 3- 3- 1

T AMCA: MESE 2311: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 1- 3- 4- 1

T AMCA: MESE 2311: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 3- 4- 2

T AMCA: MESE 2312: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 2- 3- 4- 1

T AMCA: MESE 2312: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 2- 3- 4- 2

T AMCA: MESE 2313: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 3- 3- 4- 1

T AMCA: MESE 2313: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 3- 3- 4- 2

T AMCA: MESE 2314: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 4- 3- 4- 1

T AMCA: MESE 2314: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 4- 3- 4- 2

T AMCA: MESE 2315: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 5- 3- 4- 1

T AMCA: MESE 2315: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 5- 3- 4- 2

T AMCA: MESE 2316: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 6- 3- 4- 1

T AMCA: MESE 2316: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 6- 3- 4- 2

T AMCA: MESE 2317: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 7- 3- 4- 1

T AMCA: MESE 2317: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 7- 3- 4- 2

T AMCA: MESE 2318: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 8- 3- 4- 1

T AMCA: MESE 2318: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 8- 3- 4- 2

T AMCA: MESE 2311: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 3- 5- 1

T AMCA: MESE 2312: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 3- 5- 1

T AMCA: MESE 2313: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 3- 5- 1

T AMCA: MESE 2314: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 3- 5- 1

T AMCA: MESE 2315: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 3- 5- 1

T AMCA: MESE 2316: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 3- 5- 1

T AMCA: MESE 2317: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 3- 5- 1

T AMCA: MESE 2318: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 3- 5- 1

T AMCA: MESE 2311: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 3- 5- 2

T AMCA: MESE 2312: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 3- 5- 2

T AMCA: MESE 2313: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 3- 5- 2

T AMCA: MESE 2314: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 3- 5- 2

T AMCA: MESE 2315: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 3- 5- 2

T AMCA: MESE 2316: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 3- 5- 2

T AMCA: MESE 2317: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 3- 5- 2

T AMCA: MESE 2318: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 3- 5- 2

T AMCA: MESE 2311: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 3- 5- 3

T AMCA: MESE 2312: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 3- 5- 3

T AMCA: MESE 2313: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 3- 5- 3

T AMCA: MESE 2314: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 3- 5- 3

T AMCA: MESE 2315: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 3- 5- 3

T AMCA: MESE 2316: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 3- 5- 3

T AMCA: MESE 2317: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 3- 5- 3

T AMCA: MESE 2318: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 3- 5- 3

T AMCA: MESE 2311: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 3- 5- 4

T AMCA: MESE 2312: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 3- 5- 4

T AMCA: MESE 2313: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 3- 5- 4

T AMCA: MESE 2314: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 3- 5- 4

T AMCA: MESE 2315: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 3- 5- 4

T AMCA: MESE 2316: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 3- 5- 4

T AMCA: MESE 2317: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 3- 5- 4

T AMCA: MESE 2318: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 3- 5- 4

T AMCA: MESE 2311: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 1- 3- 6- 1

T AMCA: MESE 2312: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 2- 3- 6- 1

T AMCA: MESE 2313: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 3- 3- 6- 1

T AMCA: MESE 2314: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 4- 3- 6- 1

T AMCA: MESE 2315: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 5- 3- 6- 1

T AMCA: MESE 2316: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 6- 3- 6- 1

T AMCA: MESE 2317: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 7- 3- 6- 1

T AMCA: MESE 2318: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 8- 3- 6- 1

T AMCA: MESE 2311: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 1- 3- 6- 2

T AMCA: MESE 2312: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 2- 3- 6- 2

T AMCA: MESE 2313: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 3- 3- 6- 2

T AMCA: MESE 2314: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 4- 3- 6- 2

T AMCA: MESE 2315: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 5- 3- 6- 2

T AMCA: MESE 2316: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 6- 3- 6- 2

T AMCA: MESE 2317: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 7- 3- 6- 2

T AMCA: MESE 2318: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 8- 3- 6- 2

T AMCA: MESE 2311: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 3- 7- 1

T AMCA: MESE 2312: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 3- 7- 1

T AMCA: MESE 2313: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 3- 7- 1

T AMCA: MESE 2314: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 3- 7- 1

T AMCA: MESE 2315: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 3- 7- 1

T AMCA: MESE 2316: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 3- 7- 1

T AMCA: MESE 2317: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 3- 7- 1

T AMCA: MESE 2318: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 3- 7- 1

T AMCA: MESE 2311: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 1-22- 1- 1

T AMCA: MESE 2311: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 1-22- 1- 2

T AMCA: MESE 2312: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 2-22- 1- 1

T AMCA: MESE 2312: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 2-22- 1- 2

T AMCA: MESE 2313: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 3-22- 1- 1

T AMCA: MESE 2313: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 3-22- 1- 2

T AMCA: MESE 2314: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 4-22- 1- 1

T AMCA: MESE 2314: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 4-22- 1- 2

T AMCA: MESE 2315: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 5-22- 1- 1

T AMCA: MESE 2315: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 5-22- 1- 2

T AMCA: MESE 2316: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 6-22- 1- 1

T AMCA: MESE 2316: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 6-22- 1- 2

T AMCA: MESE 2317: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 7-22- 1- 1

T AMCA: MESE 2317: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 7-22- 1- 2

T AMCA: MESE 2318: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 8-22- 1- 1

T AMCA: MESE 2318: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 8-22- 1- 2

T AMCA: MESE 2311: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.900 V, 10.5% # Test item 1- 4- 1- 1

T AMCA: MESE 2311: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.842 V, 9.9% # Test item 1- 4- 1- 2

T AMCA: MESE 2312: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.904 V, 11.9% # Test item 2- 4- 1- 1

T AMCA: MESE 2312: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.837 V, 11.4% # Test item 2- 4- 1- 2

T AMCA: MESE 2313: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.901 V, 10.6% # Test item 3- 4- 1- 1

T AMCA: MESE 2313: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.841 V, 10.2% # Test item 3- 4- 1- 2

T AMCA: MESE 2314: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.899 V, 10.2% # Test item 4- 4- 1- 1

T AMCA: MESE 2314: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.843 V, 9.2% # Test item 4- 4- 1- 2

T AMCA: MESE 2315: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.900 V, 10.5% # Test item 5- 4- 1- 1

T AMCA: MESE 2315: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.840 V, 10.4% # Test item 5- 4- 1- 2

T AMCA: MESE 2316: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.899 V, 10.0% # Test item 6- 4- 1- 1

T AMCA: MESE 2316: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.841 V, 10.2% # Test item 6- 4- 1- 2

T AMCA: MESE 2317: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.901 V, 10.6% # Test item 7- 4- 1- 1

T AMCA: MESE 2317: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.839 V, 10.6% # Test item 7- 4- 1- 2

T AMCA: MESE 2318: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.905 V, 12.1% # Test item 8- 4- 1- 1

T AMCA: MESE 2318: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.836 V, 11.8% # Test item 8- 4- 1- 2

T AMCA: MESE 2311: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.026 V, 8.5% # Test item 1- 4- 2- 1

T AMCA: MESE 2311: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.031 V, 31.2% # Test item 1- 4- 2- 2

T AMCA: MESE 2311: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.963 V, 12.4% # Test item 1- 4- 2- 3

T AMCA: MESE 2312: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.030 V, 10.1% # Test item 2- 4- 2- 1

T AMCA: MESE 2312: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.036 V, 36.3% # Test item 2- 4- 2- 2

T AMCA: MESE 2312: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.957 V, 14.2% # Test item 2- 4- 2- 3

T AMCA: MESE 2313: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.025 V, 8.4% # Test item 3- 4- 2- 1

T AMCA: MESE 2313: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.030 V, 30.0% # Test item 3- 4- 2- 2

T AMCA: MESE 2313: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.965 V, 11.5% # Test item 3- 4- 2- 3

T AMCA: MESE 2314: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.025 V, 8.3% # Test item 4- 4- 2- 1

T AMCA: MESE 2314: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.031 V, 30.7% # Test item 4- 4- 2- 2

T AMCA: MESE 2314: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.963 V, 12.2% # Test item 4- 4- 2- 3

T AMCA: MESE 2315: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.026 V, 8.8% # Test item 5- 4- 2- 1

T AMCA: MESE 2315: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.034 V, 34.0% # Test item 5- 4- 2- 2

T AMCA: MESE 2315: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.958 V, 13.9% # Test item 5- 4- 2- 3

T AMCA: MESE 2316: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.026 V, 8.5% # Test item 6- 4- 2- 1

T AMCA: MESE 2316: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.031 V, 31.0% # Test item 6- 4- 2- 2

T AMCA: MESE 2316: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.963 V, 12.3% # Test item 6- 4- 2- 3

T AMCA: MESE 2317: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.025 V, 8.3% # Test item 7- 4- 2- 1

T AMCA: MESE 2317: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.030 V, 30.2% # Test item 7- 4- 2- 2

T AMCA: MESE 2317: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.964 V, 11.8% # Test item 7- 4- 2- 3

T AMCA: MESE 2311: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.889 mA, 37.1% # Test item 1- 4- 3- 1

T AMCA: MESE 2311: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.010 mA, 6.6% # Test item 1- 4- 3- 2

T AMCA: MESE 2311: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.832 mA, 37.4% # Test item 1- 4- 3- 3

T AMCA: MESE 2311: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.007 mA, 4.9% # Test item 1- 4- 3- 4

T AMCA: MESE 2312: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.895 mA, 35.0% # Test item 2- 4- 3- 1

T AMCA: MESE 2312: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.013 mA, 8.9% # Test item 2- 4- 3- 2

T AMCA: MESE 2312: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.825 mA, 38.8% # Test item 2- 4- 3- 3

T AMCA: MESE 2312: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.007 mA, 4.4% # Test item 2- 4- 3- 4

T AMCA: MESE 2313: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.888 mA, 37.2% # Test item 3- 4- 3- 1

T AMCA: MESE 2313: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.007 mA, 4.9% # Test item 3- 4- 3- 2

T AMCA: MESE 2313: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.833 mA, 37.1% # Test item 3- 4- 3- 3

T AMCA: MESE 2313: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.009 mA, 6.0% # Test item 3- 4- 3- 4

T AMCA: MESE 2314: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.892 mA, 36.1% # Test item 4- 4- 3- 1

T AMCA: MESE 2314: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.013 mA, 8.8% # Test item 4- 4- 3- 2

T AMCA: MESE 2314: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.832 mA, 37.4% # Test item 4- 4- 3- 3

T AMCA: MESE 2314: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.006 mA, 4.1% # Test item 4- 4- 3- 4

T AMCA: MESE 2315: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.889 mA, 36.8% # Test item 5- 4- 3- 1

T AMCA: MESE 2315: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.013 mA, 8.3% # Test item 5- 4- 3- 2

T AMCA: MESE 2315: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.826 mA, 38.8% # Test item 5- 4- 3- 3

T AMCA: MESE 2315: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.005 mA, 3.2% # Test item 5- 4- 3- 4

T AMCA: MESE 2316: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.890 mA, 36.8% # Test item 6- 4- 3- 1

T AMCA: MESE 2316: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.012 mA, 7.8% # Test item 6- 4- 3- 2

T AMCA: MESE 2316: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.830 mA, 37.9% # Test item 6- 4- 3- 3

T AMCA: MESE 2316: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.005 mA, 3.6% # Test item 6- 4- 3- 4

T AMCA: MESE 2317: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.889 mA, 37.0% # Test item 7- 4- 3- 1

T AMCA: MESE 2317: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.009 mA, 6.0% # Test item 7- 4- 3- 2

T AMCA: MESE 2317: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.832 mA, 37.3% # Test item 7- 4- 3- 3

T AMCA: MESE 2317: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.009 mA, 6.2% # Test item 7- 4- 3- 4

T AMCA: MESE 2318: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.895 mA, 34.9% # Test item 8- 4- 3- 1

T AMCA: MESE 2318: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.012 mA, 8.1% # Test item 8- 4- 3- 2

T AMCA: MESE 2318: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.825 mA, 39.0% # Test item 8- 4- 3- 3

T AMCA: MESE 2318: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.006 mA, 4.1% # Test item 8- 4- 3- 4

T AMCA: MESE 2311: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.977 V, 7.8% # Test item 1- 4- 4- 1

T AMCA: MESE 2311: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.273 V, 3.4% # Test item 1- 4- 4- 2

T AMCA: MESE 2311: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.022 V, 4.9% # Test item 1- 4- 4- 3

T AMCA: MESE 2311: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.275 V, 11.7% # Test item 1- 4- 4- 4

T AMCA: MESE 2312: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.973 V, 8.9% # Test item 2- 4- 4- 1

T AMCA: MESE 2312: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.273 V, 5.2% # Test item 2- 4- 4- 2

T AMCA: MESE 2312: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.022 V, 4.9% # Test item 2- 4- 4- 3

T AMCA: MESE 2312: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.274 V, 5.7% # Test item 2- 4- 4- 4

T AMCA: MESE 2313: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.977 V, 7.8% # Test item 3- 4- 4- 1

T AMCA: MESE 2313: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.272 V, 1.7% # Test item 3- 4- 4- 2

T AMCA: MESE 2313: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.025 V, 5.6% # Test item 3- 4- 4- 3

T AMCA: MESE 2313: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.275 V, 12.2% # Test item 3- 4- 4- 4

T AMCA: MESE 2314: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.979 V, 6.9% # Test item 4- 4- 4- 1

T AMCA: MESE 2314: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.273 V, 3.4% # Test item 4- 4- 4- 2

T AMCA: MESE 2314: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.020 V, 4.5% # Test item 4- 4- 4- 3

T AMCA: MESE 2314: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.273 V, 5.2% # Test item 4- 4- 4- 4

T AMCA: MESE 2315: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.979 V, 6.9% # Test item 5- 4- 4- 1

T AMCA: MESE 2315: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.275 V, 12.6% # Test item 5- 4- 4- 2

T AMCA: MESE 2315: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.019 V, 4.2% # Test item 5- 4- 4- 3

T AMCA: MESE 2315: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.271 V, 3.1% # Test item 5- 4- 4- 4

T AMCA: MESE 2316: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.979 V, 7.1% # Test item 6- 4- 4- 1

T AMCA: MESE 2316: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.273 V, 5.2% # Test item 6- 4- 4- 2

T AMCA: MESE 2316: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.021 V, 4.7% # Test item 6- 4- 4- 3

T AMCA: MESE 2316: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.273 V, 3.8% # Test item 6- 4- 4- 4

T AMCA: MESE 2317: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.976 V, 8.0% # Test item 7- 4- 4- 1

T AMCA: MESE 2317: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.272 V, 0.3% # Test item 7- 4- 4- 2

T AMCA: MESE 2317: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.024 V, 5.4% # Test item 7- 4- 4- 3

T AMCA: MESE 2317: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.275 V, 11.2% # Test item 7- 4- 4- 4

T AMCA: MESE 2318: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.973 V, 9.0% # Test item 8- 4- 4- 1

T AMCA: MESE 2318: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.273 V, 3.8% # Test item 8- 4- 4- 2

T AMCA: MESE 2318: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.025 V, 5.5% # Test item 8- 4- 4- 3

T AMCA: MESE 2318: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.274 V, 8.5% # Test item 8- 4- 4- 4

T AMCA: MESE 2311: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.991 kOhm, 8.7% # Test item 1- 4- 5- 1

T AMCA: MESE 2312: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.990 kOhm, 9.6% # Test item 2- 4- 5- 1

T AMCA: MESE 2313: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.992 kOhm, 8.5% # Test item 3- 4- 5- 1

T AMCA: MESE 2314: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.992 kOhm, 7.5% # Test item 4- 4- 5- 1

T AMCA: MESE 2315: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.992 kOhm, 7.6% # Test item 5- 4- 5- 1

T AMCA: MESE 2316: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.992 kOhm, 7.6% # Test item 6- 4- 5- 1

T AMCA: MESE 2317: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.991 kOhm, 8.9% # Test item 7- 4- 5- 1

T AMCA: MESE 2318: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.990 kOhm, 9.6% # Test item 8- 4- 5- 1

T AMCA: MCE 2310: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_1: R\_exp = 10220.000 Ohm, [9198.000 ... 11242.000 Ohm] : R\_meas = 9982.261 Ohm, 23.3% >> vMeas = 2.750V, vOffset = -0.244V, usedUnit = 1 # Test item 0- 5- 1- 1

T AMCA: MCE 2310: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_1\_(with\_GND): R\_exp = 59.000 Ohm, [49.000 ... 69.000 Ohm] : R\_meas = 58.998 Ohm, 0.0% >> vMeas = 0.179V, vOffset = 0.002V, usedUnit = 1 # Test item 0- 5- 1- 2

T AMCA: MCE 2310: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_2: R\_exp = 10220.000 Ohm, [9198.000 ... 11242.000 Ohm] : R\_meas = 9988.136 Ohm, 22.7% >> vMeas = 2.752V, vOffset = -0.244V, usedUnit = 1 # Test item 0- 5- 1- 3

T AMCA: MCE 2310: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_2\_(with\_GND): R\_exp = 59.000 Ohm, [49.000 ... 69.000 Ohm] : R\_meas = 58.998 Ohm, 0.0% >> vMeas = 0.179V, vOffset = 0.002V, usedUnit = 1 # Test item 0- 5- 1- 4

T AMCA: MCE 2310: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_\_0.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 1

T AMCA: MCE 2310: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_\_1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 2

T AMCA: MCE 2310: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_-1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 3

T AMCA: MCE 2310: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_\_0.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 4

T AMCA: MCE 2310: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_\_1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 5

T AMCA: MCE 2310: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_-1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 6

T AMCA: MESE 2311: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 0.9% >> vOffset = -0.023V # Test item 1- 2- 9- 1

T AMCA: MESE 2311: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 17.6% >> vOffset = 0.002V # Test item 1- 2- 9- 2

T AMCA: MESE 2312: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.497 V, 1.7% >> vOffset = -0.024V # Test item 2- 2- 9- 1

T AMCA: MESE 2312: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 21.4% >> vOffset = 0.002V # Test item 2- 2- 9- 2

T AMCA: MESE 2313: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 1.0% >> vOffset = -0.023V # Test item 3- 2- 9- 1

T AMCA: MESE 2313: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 22.7% >> vOffset = -0.000V # Test item 3- 2- 9- 2

T AMCA: MESE 2314: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 0.7% >> vOffset = -0.021V # Test item 4- 2- 9- 1

T AMCA: MESE 2314: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 17.6% >> vOffset = 0.003V # Test item 4- 2- 9- 2

T AMCA: MESE 2315: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.498 V, 1.3% >> vOffset = -0.018V # Test item 5- 2- 9- 1

T AMCA: MESE 2315: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 20.1% >> vOffset = 0.008V # Test item 5- 2- 9- 2

T AMCA: MESE 2316: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 1.0% >> vOffset = -0.020V # Test item 6- 2- 9- 1

T AMCA: MESE 2316: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 22.7% >> vOffset = 0.006V # Test item 6- 2- 9- 2

T AMCA: MESE 2317: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.498 V, 1.3% >> vOffset = -0.022V # Test item 7- 2- 9- 1

T AMCA: MESE 2317: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 18.9% >> vOffset = 0.004V # Test item 7- 2- 9- 2

T AMCA: MESE 2318: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.497 V, 1.9% >> vOffset = -0.024V # Test item 8- 2- 9- 1

T AMCA: MESE 2318: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 18.9% >> vOffset = 0.005V # Test item 8- 2- 9- 2

T AMCA: MESE 2311: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 1- 7- 1- 1

T AMCA: MESE 2312: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 2- 7- 1- 1

T AMCA: MESE 2313: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 3- 7- 1- 1

T AMCA: MESE 2314: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 4- 7- 1- 1

T AMCA: MESE 2315: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 5- 7- 1- 1

T AMCA: MESE 2316: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 6- 7- 1- 1

T AMCA: MESE 2317: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 7- 7- 1- 1

T AMCA: MESE 2318: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 8- 7- 1- 1

T AMCA: MESE 2311: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 1- 7- 1- 2

T AMCA: MESE 2312: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 2- 7- 1- 2

T AMCA: MESE 2313: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 3- 7- 1- 2

T AMCA: MESE 2314: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 4- 7- 1- 2

T AMCA: MESE 2315: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 5- 7- 1- 2

T AMCA: MESE 2316: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 6- 7- 1- 2

T AMCA: MESE 2317: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 7- 7- 1- 2

T AMCA: MESE 2318: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 8- 7- 1- 2

T AMCA: MESE 2311: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 1- 7- 2- 1

T AMCA: MESE 2312: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 2- 7- 2- 1

T AMCA: MESE 2313: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 3- 7- 2- 1

T AMCA: MESE 2314: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 4- 7- 2- 1

T AMCA: MESE 2315: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 5- 7- 2- 1

T AMCA: MESE 2316: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 6- 7- 2- 1

T AMCA: MESE 2317: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 7- 7- 2- 1

T AMCA: MESE 2318: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 8- 7- 2- 1

T AMCA: MESE 2311: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 7- 3- 1

T AMCA: MESE 2311: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 7- 3- 2

T AMCA: MESE 2312: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 7- 3- 1

T AMCA: MESE 2312: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 7- 3- 2

T AMCA: MESE 2313: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 7- 3- 1

T AMCA: MESE 2313: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 7- 3- 2

T AMCA: MESE 2314: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 7- 3- 1

T AMCA: MESE 2314: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 7- 3- 2

T AMCA: MESE 2315: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 7- 3- 1

T AMCA: MESE 2315: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 7- 3- 2

T AMCA: MESE 2316: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 7- 3- 1

T AMCA: MESE 2316: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 7- 3- 2

T AMCA: MESE 2317: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 7- 3- 1

T AMCA: MESE 2317: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 7- 3- 2

T AMCA: MESE 2318: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 7- 3- 1

T AMCA: MESE 2318: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 7- 3- 2

T AMCA: MESE 2311: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 1- 8- 1- 1

T AMCA: MESE 2311: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 1- 8- 1- 2

T AMCA: MESE 2311: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 1- 8- 1- 3

T AMCA: MESE 2311: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 4

T AMCA: MESE 2311: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 1- 8- 1- 5

T AMCA: MESE 2311: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 6

T AMCA: MESE 2311: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 1- 8- 1- 7

T AMCA: MESE 2311: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 8

T AMCA: MESE 2311: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 9

T AMCA: MESE 2312: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 2- 8- 1- 1

T AMCA: MESE 2312: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 2- 8- 1- 2

T AMCA: MESE 2312: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 2- 8- 1- 3

T AMCA: MESE 2312: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 4

T AMCA: MESE 2312: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 2- 8- 1- 5

T AMCA: MESE 2312: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 6

T AMCA: MESE 2312: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 2- 8- 1- 7

T AMCA: MESE 2312: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 8

T AMCA: MESE 2312: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 9

T AMCA: MESE 2313: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 3- 8- 1- 1

T AMCA: MESE 2313: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 3- 8- 1- 2

T AMCA: MESE 2313: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 3- 8- 1- 3

T AMCA: MESE 2313: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 4

T AMCA: MESE 2313: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 3- 8- 1- 5

T AMCA: MESE 2313: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 6

T AMCA: MESE 2313: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 3- 8- 1- 7

T AMCA: MESE 2313: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 8

T AMCA: MESE 2313: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 9

T AMCA: MESE 2314: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 4- 8- 1- 1

T AMCA: MESE 2314: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 4- 8- 1- 2

T AMCA: MESE 2314: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 4- 8- 1- 3

T AMCA: MESE 2314: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 4

T AMCA: MESE 2314: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 4- 8- 1- 5

T AMCA: MESE 2314: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 6

T AMCA: MESE 2314: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 4- 8- 1- 7

T AMCA: MESE 2314: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 8

T AMCA: MESE 2314: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 9

T AMCA: MESE 2315: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 5- 8- 1- 1

T AMCA: MESE 2315: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 5- 8- 1- 2

T AMCA: MESE 2315: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 5- 8- 1- 3

T AMCA: MESE 2315: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 4

T AMCA: MESE 2315: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 5- 8- 1- 5

T AMCA: MESE 2315: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 6

T AMCA: MESE 2315: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 5- 8- 1- 7

T AMCA: MESE 2315: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 8

T AMCA: MESE 2315: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 9

T AMCA: MESE 2316: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 6- 8- 1- 1

T AMCA: MESE 2316: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 6- 8- 1- 2

T AMCA: MESE 2316: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 6- 8- 1- 3

T AMCA: MESE 2316: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 4

T AMCA: MESE 2316: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 6- 8- 1- 5

T AMCA: MESE 2316: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 6

T AMCA: MESE 2316: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 6- 8- 1- 7

T AMCA: MESE 2316: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 8

T AMCA: MESE 2316: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 9

T AMCA: MESE 2317: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 7- 8- 1- 1

T AMCA: MESE 2317: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 7- 8- 1- 2

T AMCA: MESE 2317: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 7- 8- 1- 3

T AMCA: MESE 2317: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 4

T AMCA: MESE 2317: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 7- 8- 1- 5

T AMCA: MESE 2317: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 6

T AMCA: MESE 2317: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 7- 8- 1- 7

T AMCA: MESE 2317: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 8

T AMCA: MESE 2317: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 9

T AMCA: MESE 2318: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 8- 8- 1- 1

T AMCA: MESE 2318: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 8- 8- 1- 2

T AMCA: MESE 2318: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 8- 8- 1- 3

T AMCA: MESE 2318: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 4

T AMCA: MESE 2318: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 8- 8- 1- 5

T AMCA: MESE 2318: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 6

T AMCA: MESE 2318: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 8- 8- 1- 7

T AMCA: MESE 2318: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 8

T AMCA: MESE 2318: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 9

T AMCA: MESE 2311: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 1, masterModule = 1, slaveUnit = 3, slaveModule = 1 # Test item 1- 8- 3- 1

T AMCA: MESE 2311: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 1, masterModule = 1, slaveUnit = 3, slaveModule = 1 # Test item 1- 8- 3- 2

T AMCA: MESE 2313: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 3, masterModule = 1, slaveUnit = 2, slaveModule = 2 # Test item 3- 8- 3- 3

T AMCA: MESE 2313: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 3, masterModule = 1, slaveUnit = 2, slaveModule = 2 # Test item 3- 8- 3- 4

T AMCA: MESE 2312: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 2, masterModule = 2, slaveUnit = 4, slaveModule = 2 # Test item 2- 8- 3- 5

T AMCA: MESE 2312: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 2, masterModule = 2, slaveUnit = 4, slaveModule = 2 # Test item 2- 8- 3- 6

T AMCA: MESE 2314: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 4, masterModule = 2, slaveUnit = 5, slaveModule = 3 # Test item 4- 8- 3- 7

T AMCA: MESE 2314: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 4, masterModule = 2, slaveUnit = 5, slaveModule = 3 # Test item 4- 8- 3- 8

T AMCA: MESE 2315: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 5, masterModule = 3, slaveUnit = 7, slaveModule = 3 # Test item 5- 8- 3- 9

T AMCA: MESE 2315: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 5, masterModule = 3, slaveUnit = 7, slaveModule = 3 # Test item 5- 8- 3-10

T AMCA: MESE 2317: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 7, masterModule = 3, slaveUnit = 8, slaveModule = 4 # Test item 7- 8- 3-11

T AMCA: MESE 2317: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 7, masterModule = 3, slaveUnit = 8, slaveModule = 4 # Test item 7- 8- 3-12

T AMCA: MESE 2318: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 8, masterModule = 4, slaveUnit = 6, slaveModule = 4 # Test item 8- 8- 3-13

T AMCA: MESE 2318: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 8, masterModule = 4, slaveUnit = 6, slaveModule = 4 # Test item 8- 8- 3-14

T AMCA: MESE 2316: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 6, masterModule = 4, slaveUnit = 1, slaveModule = 1 # Test item 6- 8- 3-15

T AMCA: MESE 2316: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 6, masterModule = 4, slaveUnit = 1, slaveModule = 1 # Test item 6- 8- 3-16

T AMCA: MESE 2311: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.598 ns, 20.1% >> short = 58584, long = 30242 # Test item 1- 8- 6- 1

T AMCA: MESE 2312: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.818 ns, 9.1% >> short = 58641, long = 29925 # Test item 2- 8- 6- 1

T AMCA: MESE 2313: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.925 ns, 3.7% >> short = 58226, long = 29659 # Test item 3- 8- 6- 1

T AMCA: MESE 2314: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.597 ns, 20.1% >> short = 59159, long = 30396 # Test item 4- 8- 6- 1

T AMCA: MESE 2315: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.950 ns, 2.5% >> short = 59394, long = 29922 # Test item 5- 8- 6- 1

T AMCA: MESE 2316: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.839 ns, 8.1% >> short = 58657, long = 29899 # Test item 6- 8- 6- 1

T AMCA: MESE 2317: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.721 ns, 14.0% >> short = 60025, long = 30431 # Test item 7- 8- 6- 1

T AMCA: MESE 2318: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.671 ns, 16.4% >> short = 58686, long = 30158 # Test item 8- 8- 6- 1

T AMCA: MESE 2311: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 1- 8- 7- 1

T AMCA: MESE 2311: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18415, 39.6% # Test item 1- 8- 7- 2

T AMCA: MESE 2311: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x000047EF: Reg\_meas = 0x000047EF # Test item 1- 8- 7- 3

T AMCA: MESE 2311: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 8- 7- 4

T AMCA: MESE 2312: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 2- 8- 7- 1

T AMCA: MESE 2312: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18218, 44.5% # Test item 2- 8- 7- 2

T AMCA: MESE 2312: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000472A: Reg\_meas = 0x0000472A # Test item 2- 8- 7- 3

T AMCA: MESE 2312: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 8- 7- 4

T AMCA: MESE 2313: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 3- 8- 7- 1

T AMCA: MESE 2313: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17607, 59.8% # Test item 3- 8- 7- 2

T AMCA: MESE 2313: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x000044C7: Reg\_meas = 0x000044C7 # Test item 3- 8- 7- 3

T AMCA: MESE 2313: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 8- 7- 4

T AMCA: MESE 2314: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 4- 8- 7- 1

T AMCA: MESE 2314: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17698, 57.6% # Test item 4- 8- 7- 2

T AMCA: MESE 2314: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004522: Reg\_meas = 0x00004522 # Test item 4- 8- 7- 3

T AMCA: MESE 2314: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 8- 7- 4

T AMCA: MESE 2315: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 5- 8- 7- 1

T AMCA: MESE 2315: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18516, 37.1% # Test item 5- 8- 7- 2

T AMCA: MESE 2315: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004854: Reg\_meas = 0x00004854 # Test item 5- 8- 7- 3

T AMCA: MESE 2315: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 8- 7- 4

T AMCA: MESE 2316: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 6- 8- 7- 1

T AMCA: MESE 2316: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17540, 61.5% # Test item 6- 8- 7- 2

T AMCA: MESE 2316: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004484: Reg\_meas = 0x00004484 # Test item 6- 8- 7- 3

T AMCA: MESE 2316: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 8- 7- 4

T AMCA: MESE 2317: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 7- 8- 7- 1

T AMCA: MESE 2317: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17943, 51.4% # Test item 7- 8- 7- 2

T AMCA: MESE 2317: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004617: Reg\_meas = 0x00004617 # Test item 7- 8- 7- 3

T AMCA: MESE 2317: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 8- 7- 4

T AMCA: MESE 2318: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 8- 8- 7- 1

T AMCA: MESE 2318: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18100, 47.5% # Test item 8- 8- 7- 2

T AMCA: MESE 2318: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x000046B4: Reg\_meas = 0x000046B4 # Test item 8- 8- 7- 3

T AMCA: MESE 2318: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 8- 7- 4

T AMCA: MESE 2311: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 9- 1- 1

T AMCA: MESE 2311: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 9- 1- 2

T AMCA: MESE 2311: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 3

T AMCA: MESE 2311: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 4

T AMCA: MESE 2311: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 5

T AMCA: MESE 2311: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 6

T AMCA: MESE 2311: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 7

T AMCA: MESE 2311: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 1- 9- 1- 8

T AMCA: MESE 2311: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 9

T AMCA: MESE 2311: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1-10

T AMCA: MESE 2311: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 1- 9- 1-11

T AMCA: MESE 2311: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 1- 9- 1-12

T AMCA: MESE 2311: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 1- 9- 1-13

T AMCA: MESE 2311: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 9- 1-14

T AMCA: MESE 2312: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 9- 1- 1

T AMCA: MESE 2312: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 9- 1- 2

T AMCA: MESE 2312: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 3

T AMCA: MESE 2312: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 4

T AMCA: MESE 2312: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 5

T AMCA: MESE 2312: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 6

T AMCA: MESE 2312: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 7

T AMCA: MESE 2312: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 2- 9- 1- 8

T AMCA: MESE 2312: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 9

T AMCA: MESE 2312: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1-10

T AMCA: MESE 2312: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 2- 9- 1-11

T AMCA: MESE 2312: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 2- 9- 1-12

T AMCA: MESE 2312: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 2- 9- 1-13

T AMCA: MESE 2312: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 9- 1-14

T AMCA: MESE 2313: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 9- 1- 1

T AMCA: MESE 2313: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 9- 1- 2

T AMCA: MESE 2313: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 3

T AMCA: MESE 2313: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 4

T AMCA: MESE 2313: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 5

T AMCA: MESE 2313: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 6

T AMCA: MESE 2313: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 7

T AMCA: MESE 2313: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 3- 9- 1- 8

T AMCA: MESE 2313: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 9

T AMCA: MESE 2313: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1-10

T AMCA: MESE 2313: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 3- 9- 1-11

T AMCA: MESE 2313: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 3- 9- 1-12

T AMCA: MESE 2313: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 3- 9- 1-13

T AMCA: MESE 2313: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 9- 1-14

T AMCA: MESE 2314: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 9- 1- 1

T AMCA: MESE 2314: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 9- 1- 2

T AMCA: MESE 2314: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 3

T AMCA: MESE 2314: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 4

T AMCA: MESE 2314: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 5

T AMCA: MESE 2314: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 6

T AMCA: MESE 2314: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 7

T AMCA: MESE 2314: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 4- 9- 1- 8

T AMCA: MESE 2314: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 9

T AMCA: MESE 2314: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1-10

T AMCA: MESE 2314: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 4- 9- 1-11

T AMCA: MESE 2314: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 4- 9- 1-12

T AMCA: MESE 2314: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 4- 9- 1-13

T AMCA: MESE 2314: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 9- 1-14

T AMCA: MESE 2315: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 9- 1- 1

T AMCA: MESE 2315: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 9- 1- 2

T AMCA: MESE 2315: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 3

T AMCA: MESE 2315: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 4

T AMCA: MESE 2315: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 5

T AMCA: MESE 2315: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 6

T AMCA: MESE 2315: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 7

T AMCA: MESE 2315: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 5- 9- 1- 8

T AMCA: MESE 2315: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 9

T AMCA: MESE 2315: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1-10

T AMCA: MESE 2315: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 5- 9- 1-11

T AMCA: MESE 2315: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 5- 9- 1-12

T AMCA: MESE 2315: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 5- 9- 1-13

T AMCA: MESE 2315: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 9- 1-14

T AMCA: MESE 2316: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 9- 1- 1

T AMCA: MESE 2316: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 9- 1- 2

T AMCA: MESE 2316: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 3

T AMCA: MESE 2316: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 4

T AMCA: MESE 2316: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 5

T AMCA: MESE 2316: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 6

T AMCA: MESE 2316: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 7

T AMCA: MESE 2316: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 6- 9- 1- 8

T AMCA: MESE 2316: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 9

T AMCA: MESE 2316: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1-10

T AMCA: MESE 2316: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 6- 9- 1-11

T AMCA: MESE 2316: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 6- 9- 1-12

T AMCA: MESE 2316: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 6- 9- 1-13

T AMCA: MESE 2316: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 9- 1-14

T AMCA: MESE 2317: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 9- 1- 1

T AMCA: MESE 2317: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 9- 1- 2

T AMCA: MESE 2317: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 3

T AMCA: MESE 2317: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 4

T AMCA: MESE 2317: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 5

T AMCA: MESE 2317: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 6

T AMCA: MESE 2317: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 7

T AMCA: MESE 2317: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 7- 9- 1- 8

T AMCA: MESE 2317: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 9

T AMCA: MESE 2317: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1-10

T AMCA: MESE 2317: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 7- 9- 1-11

T AMCA: MESE 2317: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 7- 9- 1-12

T AMCA: MESE 2317: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 7- 9- 1-13

T AMCA: MESE 2317: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 9- 1-14

T AMCA: MESE 2318: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 9- 1- 1

T AMCA: MESE 2318: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 9- 1- 2

T AMCA: MESE 2318: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 3

T AMCA: MESE 2318: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 4

T AMCA: MESE 2318: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 5

T AMCA: MESE 2318: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 6

T AMCA: MESE 2318: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 7

T AMCA: MESE 2318: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 8- 9- 1- 8

T AMCA: MESE 2318: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 9

T AMCA: MESE 2318: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1-10

T AMCA: MESE 2318: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 8- 9- 1-11

T AMCA: MESE 2318: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 8- 9- 1-12

T AMCA: MESE 2318: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 8- 9- 1-13

T AMCA: MESE 2318: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 9- 1-14

T AMCA: MESE 2311: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.954 V, 3.6% # Test item 1-17- 1- 1

T AMCA: MESE 2311: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.950 V, 7.3% # Test item 1-17- 1- 2

T AMCA: MESE 2311: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.963 V, 4.5% # Test item 1-17- 1- 3

T AMCA: MESE 2311: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.957 V, 0.9% # Test item 1-17- 1- 4

T AMCA: MESE 2311: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.060 V, 8.9% # Test item 1-17- 1- 5

T AMCA: MESE 2311: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.981 V, 1.8% # Test item 1-17- 1- 6

T AMCA: MESE 2311: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.971 V, 1.8% # Test item 1-17- 1- 7

T AMCA: MESE 2311: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.909 V, 5.5% # Test item 1-17- 1- 8

T AMCA: MESE 2311: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.973 V, 5.5% # Test item 1-17- 1- 9

T AMCA: MESE 2311: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.983 V, 9.1% # Test item 1-17- 1-10

T AMCA: MESE 2311: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.968 V, 7.3% # Test item 1-17- 1-11

T AMCA: MESE 2312: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.947 V, 10.0% # Test item 2-17- 1- 1

T AMCA: MESE 2312: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.956 V, 1.8% # Test item 2-17- 1- 2

T AMCA: MESE 2312: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.953 V, 4.5% # Test item 2-17- 1- 3

T AMCA: MESE 2312: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.957 V, 0.9% # Test item 2-17- 1- 4

T AMCA: MESE 2312: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.059 V, 10.0% # Test item 2-17- 1- 5

T AMCA: MESE 2312: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.978 V, 0.9% # Test item 2-17- 1- 6

T AMCA: MESE 2312: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.974 V, 0.9% # Test item 2-17- 1- 7

T AMCA: MESE 2312: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.914 V, 7.8% # Test item 2-17- 1- 8

T AMCA: MESE 2312: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.978 V, 0.9% # Test item 2-17- 1- 9

T AMCA: MESE 2312: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.994 V, 0.9% # Test item 2-17- 1-10

T AMCA: MESE 2312: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.967 V, 8.2% # Test item 2-17- 1-11

T AMCA: MESE 2313: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.951 V, 6.4% # Test item 3-17- 1- 1

T AMCA: MESE 2313: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.949 V, 8.2% # Test item 3-17- 1- 2

T AMCA: MESE 2313: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.956 V, 1.8% # Test item 3-17- 1- 3

T AMCA: MESE 2313: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.955 V, 2.7% # Test item 3-17- 1- 4

T AMCA: MESE 2313: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.064 V, 3.6% # Test item 3-17- 1- 5

T AMCA: MESE 2313: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.987 V, 7.3% # Test item 3-17- 1- 6

T AMCA: MESE 2313: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.964 V, 8.2% # Test item 3-17- 1- 7

T AMCA: MESE 2313: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.646 V, 12.2% # Test item 3-17- 1- 8

T AMCA: MESE 2313: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.974 V, 4.5% # Test item 3-17- 1- 9

T AMCA: MESE 2313: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.713 V, 13.6% # Test item 3-17- 1-10

T AMCA: MESE 2314: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.947 V, 10.0% # Test item 4-17- 1- 1

T AMCA: MESE 2314: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.954 V, 3.6% # Test item 4-17- 1- 2

T AMCA: MESE 2314: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.958 V, 0.0% # Test item 4-17- 1- 3

T AMCA: MESE 2314: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.952 V, 5.5% # Test item 4-17- 1- 4

T AMCA: MESE 2314: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.057 V, 10.0% # Test item 4-17- 1- 5

T AMCA: MESE 2314: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.981 V, 1.8% # Test item 4-17- 1- 6

T AMCA: MESE 2314: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.970 V, 2.7% # Test item 4-17- 1- 7

T AMCA: MESE 2314: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.651 V, 16.8% # Test item 4-17- 1- 8

T AMCA: MESE 2314: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.974 V, 4.5% # Test item 4-17- 1- 9

T AMCA: MESE 2314: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.716 V, 10.9% # Test item 4-17- 1-10

T AMCA: MESE 2315: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.947 V, 10.0% # Test item 5-17- 1- 1

T AMCA: MESE 2315: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.954 V, 3.6% # Test item 5-17- 1- 2

T AMCA: MESE 2315: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.961 V, 2.7% # Test item 5-17- 1- 3

T AMCA: MESE 2315: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.964 V, 5.5% # Test item 5-17- 1- 4

T AMCA: MESE 2315: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.058 V, 11.1% # Test item 5-17- 1- 5

T AMCA: MESE 2315: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.978 V, 0.9% # Test item 5-17- 1- 6

T AMCA: MESE 2315: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.964 V, 8.2% # Test item 5-17- 1- 7

T AMCA: MESE 2315: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.886 V, 10.5% # Test item 5-17- 1- 8

T AMCA: MESE 2315: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.973 V, 5.5% # Test item 5-17- 1- 9

T AMCA: MESE 2315: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.986 V, 6.4% # Test item 5-17- 1-10

T AMCA: MESE 2315: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.964 V, 10.9% # Test item 5-17- 1-11

T AMCA: MESE 2316: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.955 V, 2.7% # Test item 6-17- 1- 1

T AMCA: MESE 2316: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.952 V, 5.5% # Test item 6-17- 1- 2

T AMCA: MESE 2316: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.956 V, 1.8% # Test item 6-17- 1- 3

T AMCA: MESE 2316: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.954 V, 3.6% # Test item 6-17- 1- 4

T AMCA: MESE 2316: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.066 V, 1.8% # Test item 6-17- 1- 5

T AMCA: MESE 2316: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.979 V, 0.0% # Test item 6-17- 1- 6

T AMCA: MESE 2316: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.971 V, 1.8% # Test item 6-17- 1- 7

T AMCA: MESE 2316: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.644 V, 10.4% # Test item 6-17- 1- 8

T AMCA: MESE 2316: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.980 V, 0.9% # Test item 6-17- 1- 9

T AMCA: MESE 2316: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.714 V, 12.7% # Test item 6-17- 1-10

T AMCA: MESE 2317: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.956 V, 1.8% # Test item 7-17- 1- 1

T AMCA: MESE 2317: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.956 V, 1.8% # Test item 7-17- 1- 2

T AMCA: MESE 2317: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.960 V, 1.8% # Test item 7-17- 1- 3

T AMCA: MESE 2317: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.960 V, 1.8% # Test item 7-17- 1- 4

T AMCA: MESE 2317: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.065 V, 2.7% # Test item 7-17- 1- 5

T AMCA: MESE 2317: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.987 V, 7.3% # Test item 7-17- 1- 6

T AMCA: MESE 2317: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.968 V, 4.5% # Test item 7-17- 1- 7

T AMCA: MESE 2317: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.644 V, 10.4% # Test item 7-17- 1- 8

T AMCA: MESE 2317: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.976 V, 2.7% # Test item 7-17- 1- 9

T AMCA: MESE 2317: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.712 V, 14.5% # Test item 7-17- 1-10

T AMCA: MESE 2318: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.956 V, 1.8% # Test item 8-17- 1- 1

T AMCA: MESE 2318: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.958 V, 0.0% # Test item 8-17- 1- 2

T AMCA: MESE 2318: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.953 V, 4.5% # Test item 8-17- 1- 3

T AMCA: MESE 2318: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.956 V, 1.8% # Test item 8-17- 1- 4

T AMCA: MESE 2318: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.061 V, 7.8% # Test item 8-17- 1- 5

T AMCA: MESE 2318: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.982 V, 2.7% # Test item 8-17- 1- 6

T AMCA: MESE 2318: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.964 V, 8.2% # Test item 8-17- 1- 7

T AMCA: MESE 2318: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.891 V, 5.9% # Test item 8-17- 1- 8

T AMCA: MESE 2318: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.974 V, 4.5% # Test item 8-17- 1- 9

T AMCA: MESE 2318: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.987 V, 5.5% # Test item 8-17- 1-10

T AMCA: MESE 2318: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.965 V, 10.0% # Test item 8-17- 1-11

T AMCA: MESE 2311: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.005 V, 1.5% # Test item 1-15- 1- 1

T AMCA: MESE 2311: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 4.3% # Test item 1-15- 1- 2

T AMCA: MESE 2311: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.018 V, 6.0% # Test item 1-15- 1- 3

T AMCA: MESE 2311: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.004 V, 1.4% # Test item 1-15- 1- 4

T AMCA: MESE 2311: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 4.4% # Test item 1-15- 1- 5

T AMCA: MESE 2311: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.018 V, 6.0% # Test item 1-15- 1- 6

T AMCA: MESE 2311: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.004 V, 1.5% # Test item 1-15- 1- 7

T AMCA: MESE 2311: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 4.4% # Test item 1-15- 1- 8

T AMCA: MESE 2311: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.018 V, 6.0% # Test item 1-15- 1- 9

T AMCA: MESE 2311: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.004 V, 1.4% # Test item 1-15- 1-10

T AMCA: MESE 2311: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 4.4% # Test item 1-15- 1-11

T AMCA: MESE 2311: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.018 V, 5.9% # Test item 1-15- 1-12

T AMCA: MESE 2312: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.000 V, 0.1% # Test item 2-15- 1- 1

T AMCA: MESE 2312: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.7% # Test item 2-15- 1- 2

T AMCA: MESE 2312: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.3% # Test item 2-15- 1- 3

T AMCA: MESE 2312: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.000 V, 0.0% # Test item 2-15- 1- 4

T AMCA: MESE 2312: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.8% # Test item 2-15- 1- 5

T AMCA: MESE 2312: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.013 V, 4.3% # Test item 2-15- 1- 6

T AMCA: MESE 2312: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.002 V, 0.7% # Test item 2-15- 1- 7

T AMCA: MESE 2312: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.3% # Test item 2-15- 1- 8

T AMCA: MESE 2312: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.012 V, 4.0% # Test item 2-15- 1- 9

T AMCA: MESE 2312: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.002 V, 0.7% # Test item 2-15- 1-10

T AMCA: MESE 2312: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.4% # Test item 2-15- 1-11

T AMCA: MESE 2312: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.012 V, 4.0% # Test item 2-15- 1-12

T AMCA: MESE 2313: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 2.0% # Test item 3-15- 1- 1

T AMCA: MESE 2313: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.1% # Test item 3-15- 1- 2

T AMCA: MESE 2313: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.015 V, 5.1% # Test item 3-15- 1- 3

T AMCA: MESE 2313: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 1.9% # Test item 3-15- 1- 4

T AMCA: MESE 2313: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.3% # Test item 3-15- 1- 5

T AMCA: MESE 2313: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.015 V, 5.1% # Test item 3-15- 1- 6

T AMCA: MESE 2313: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 2.8% # Test item 3-15- 1- 7

T AMCA: MESE 2313: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% # Test item 3-15- 1- 8

T AMCA: MESE 2313: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.014 V, 4.5% # Test item 3-15- 1- 9

T AMCA: MESE 2313: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 2.8% # Test item 3-15- 1-10

T AMCA: MESE 2313: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% # Test item 3-15- 1-11

T AMCA: MESE 2313: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.014 V, 4.6% # Test item 3-15- 1-12

T AMCA: MESE 2314: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.003 V, 0.9% # Test item 4-15- 1- 1

T AMCA: MESE 2314: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.4% # Test item 4-15- 1- 2

T AMCA: MESE 2314: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.012 V, 4.1% # Test item 4-15- 1- 3

T AMCA: MESE 2314: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.002 V, 0.8% # Test item 4-15- 1- 4

T AMCA: MESE 2314: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.5% # Test item 4-15- 1- 5

T AMCA: MESE 2314: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.012 V, 4.1% # Test item 4-15- 1- 6

T AMCA: MESE 2314: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.001 V, 0.3% # Test item 4-15- 1- 7

T AMCA: MESE 2314: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.4% # Test item 4-15- 1- 8

T AMCA: MESE 2314: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.011 V, 3.8% # Test item 4-15- 1- 9

T AMCA: MESE 2314: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.001 V, 0.2% # Test item 4-15- 1-10

T AMCA: MESE 2314: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.4% # Test item 4-15- 1-11

T AMCA: MESE 2314: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.011 V, 3.7% # Test item 4-15- 1-12

T AMCA: MESE 2315: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.005 V, 1.8% # Test item 5-15- 1- 1

T AMCA: MESE 2315: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.3% # Test item 5-15- 1- 2

T AMCA: MESE 2315: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.5% # Test item 5-15- 1- 3

T AMCA: MESE 2315: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.005 V, 1.7% # Test item 5-15- 1- 4

T AMCA: MESE 2315: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% # Test item 5-15- 1- 5

T AMCA: MESE 2315: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.5% # Test item 5-15- 1- 6

T AMCA: MESE 2315: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.005 V, 1.7% # Test item 5-15- 1- 7

T AMCA: MESE 2315: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% # Test item 5-15- 1- 8

T AMCA: MESE 2315: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.5% # Test item 5-15- 1- 9

T AMCA: MESE 2315: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.005 V, 1.7% # Test item 5-15- 1-10

T AMCA: MESE 2315: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% # Test item 5-15- 1-11

T AMCA: MESE 2315: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.4% # Test item 5-15- 1-12

T AMCA: MESE 2316: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.2% # Test item 6-15- 1- 1

T AMCA: MESE 2316: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.5% # Test item 6-15- 1- 2

T AMCA: MESE 2316: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.5% # Test item 6-15- 1- 3

T AMCA: MESE 2316: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 2.1% # Test item 6-15- 1- 4

T AMCA: MESE 2316: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.5% # Test item 6-15- 1- 5

T AMCA: MESE 2316: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.4% # Test item 6-15- 1- 6

T AMCA: MESE 2316: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.1% # Test item 6-15- 1- 7

T AMCA: MESE 2316: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.5% # Test item 6-15- 1- 8

T AMCA: MESE 2316: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.011 V, 3.8% # Test item 6-15- 1- 9

T AMCA: MESE 2316: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.0% # Test item 6-15- 1-10

T AMCA: MESE 2316: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% # Test item 6-15- 1-11

T AMCA: MESE 2316: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.012 V, 3.8% # Test item 6-15- 1-12

T AMCA: MESE 2317: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.1% # Test item 7-15- 1- 1

T AMCA: MESE 2317: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% # Test item 7-15- 1- 2

T AMCA: MESE 2317: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.3% # Test item 7-15- 1- 3

T AMCA: MESE 2317: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.0% # Test item 7-15- 1- 4

T AMCA: MESE 2317: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% # Test item 7-15- 1- 5

T AMCA: MESE 2317: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.010 V, 3.4% # Test item 7-15- 1- 6

T AMCA: MESE 2317: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.2% # Test item 7-15- 1- 7

T AMCA: MESE 2317: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.1% # Test item 7-15- 1- 8

T AMCA: MESE 2317: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.009 V, 2.8% # Test item 7-15- 1- 9

T AMCA: MESE 2317: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.2% # Test item 7-15- 1-10

T AMCA: MESE 2317: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.0% # Test item 7-15- 1-11

T AMCA: MESE 2317: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.8% # Test item 7-15- 1-12

T AMCA: MESE 2318: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.4% # Test item 8-15- 1- 1

T AMCA: MESE 2318: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% # Test item 8-15- 1- 2

T AMCA: MESE 2318: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.009 V, 2.9% # Test item 8-15- 1- 3

T AMCA: MESE 2318: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.4% # Test item 8-15- 1- 4

T AMCA: MESE 2318: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% # Test item 8-15- 1- 5

T AMCA: MESE 2318: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.009 V, 2.9% # Test item 8-15- 1- 6

T AMCA: MESE 2318: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 2.8% # Test item 8-15- 1- 7

T AMCA: MESE 2318: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.8% # Test item 8-15- 1- 8

T AMCA: MESE 2318: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.009 V, 2.8% # Test item 8-15- 1- 9

T AMCA: MESE 2318: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.8% # Test item 8-15- 1-10

T AMCA: MESE 2318: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.6% # Test item 8-15- 1-11

T AMCA: MESE 2318: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.009 V, 2.9% # Test item 8-15- 1-12

T AMCA: MESE 2311: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.781 MOhm, 14.6% # Test item 1-15- 2- 1

T AMCA: MESE 2311: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.666 MOhm, 22.3% # Test item 1-15- 2- 2

T AMCA: MESE 2311: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.767 MOhm, 15.6% # Test item 1-15- 2- 3

T AMCA: MESE 2311: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.766 MOhm, 15.6% # Test item 1-15- 2- 4

T AMCA: MESE 2312: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.848 MOhm, 10.1% # Test item 2-15- 2- 1

T AMCA: MESE 2312: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.748 MOhm, 16.8% # Test item 2-15- 2- 2

T AMCA: MESE 2312: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.883 MOhm, 7.8% # Test item 2-15- 2- 3

T AMCA: MESE 2312: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.868 MOhm, 8.8% # Test item 2-15- 2- 4

T AMCA: MESE 2313: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.604 MOhm, 26.4% # Test item 3-15- 2- 1

T AMCA: MESE 2313: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.668 MOhm, 22.2% # Test item 3-15- 2- 2

T AMCA: MESE 2313: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.839 MOhm, 10.7% # Test item 3-15- 2- 3

T AMCA: MESE 2313: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.768 MOhm, 15.4% # Test item 3-15- 2- 4

T AMCA: MESE 2314: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.800 MOhm, 13.3% # Test item 4-15- 2- 1

T AMCA: MESE 2314: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.818 MOhm, 12.2% # Test item 4-15- 2- 2

T AMCA: MESE 2314: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.857 MOhm, 9.5% # Test item 4-15- 2- 3

T AMCA: MESE 2314: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.745 MOhm, 17.0% # Test item 4-15- 2- 4

T AMCA: MESE 2315: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.798 MOhm, 13.4% # Test item 5-15- 2- 1

T AMCA: MESE 2315: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.733 MOhm, 17.8% # Test item 5-15- 2- 2

T AMCA: MESE 2315: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.742 MOhm, 17.2% # Test item 5-15- 2- 3

T AMCA: MESE 2315: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.818 MOhm, 12.2% # Test item 5-15- 2- 4

T AMCA: MESE 2316: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.777 MOhm, 14.8% # Test item 6-15- 2- 1

T AMCA: MESE 2316: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.922 MOhm, 5.2% # Test item 6-15- 2- 2

T AMCA: MESE 2316: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.898 MOhm, 6.8% # Test item 6-15- 2- 3

T AMCA: MESE 2316: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.772 MOhm, 15.2% # Test item 6-15- 2- 4

T AMCA: MESE 2317: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.922 MOhm, 5.2% # Test item 7-15- 2- 1

T AMCA: MESE 2317: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.812 MOhm, 12.6% # Test item 7-15- 2- 2

T AMCA: MESE 2317: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.789 MOhm, 14.1% # Test item 7-15- 2- 3

T AMCA: MESE 2317: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.780 MOhm, 14.7% # Test item 7-15- 2- 4

T AMCA: MESE 2318: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.765 MOhm, 15.7% # Test item 8-15- 2- 1

T AMCA: MESE 2318: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.696 MOhm, 20.3% # Test item 8-15- 2- 2

T AMCA: MESE 2318: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.694 MOhm, 20.4% # Test item 8-15- 2- 3

T AMCA: MESE 2318: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.806 MOhm, 12.9% # Test item 8-15- 2- 4

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.035V, neg = 0.035V # Test item 1-15- 3- 1

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.696 V, 21.7% >> pos = 2.383V, neg = -2.313V # Test item 1-15- 3- 2

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.035V, neg = 0.035V # Test item 1-15- 3- 3

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.7% >> pos = 0.816V, neg = -0.746V # Test item 1-15- 3- 4

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.035V, neg = 0.035V # Test item 1-15- 3- 5

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 22.2% >> pos = 0.133V, neg = -0.063V # Test item 1-15- 3- 6

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.035V, neg = -0.035V # Test item 1-15- 3- 7

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.696 V, 21.6% >> pos = 2.313V, neg = -2.383V # Test item 1-15- 3- 8

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.035V, neg = -0.035V # Test item 1-15- 3- 9

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.6% >> pos = 0.746V, neg = -0.816V # Test item 1-15- 3-10

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.035V, neg = -0.035V # Test item 1-15- 3-11

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 22.2% >> pos = 0.062V, neg = -0.133V # Test item 1-15- 3-12

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.035V, neg = 0.035V # Test item 1-15- 3-13

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.695 V, 21.9% >> pos = 2.383V, neg = -2.312V # Test item 1-15- 3-14

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.035V, neg = 0.035V # Test item 1-15- 3-15

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.9% >> pos = 0.816V, neg = -0.745V # Test item 1-15- 3-16

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.035V, neg = 0.035V # Test item 1-15- 3-17

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 22.7% >> pos = 0.133V, neg = -0.062V # Test item 1-15- 3-18

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.035V, neg = -0.035V # Test item 1-15- 3-19

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.696 V, 21.7% >> pos = 2.312V, neg = -2.384V # Test item 1-15- 3-20

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.035V, neg = -0.035V # Test item 1-15- 3-21

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.8% >> pos = 0.745V, neg = -0.816V # Test item 1-15- 3-22

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.036V, neg = -0.036V # Test item 1-15- 3-23

T AMCA: MESE 2311: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.195 V, 22.6% >> pos = 0.062V, neg = -0.133V # Test item 1-15- 3-24

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.040V, neg = 0.040V # Test item 2-15- 3- 1

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.702 V, 20.4% >> pos = 2.391V, neg = -2.311V # Test item 2-15- 3- 2

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.039V, neg = 0.039V # Test item 2-15- 3- 3

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.2% >> pos = 0.822V, neg = -0.743V # Test item 2-15- 3- 4

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.040V, neg = 0.040V # Test item 2-15- 3- 5

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.9% >> pos = 0.138V, neg = -0.058V # Test item 2-15- 3- 6

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.040V, neg = -0.039V # Test item 2-15- 3- 7

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.3% >> pos = 2.312V, neg = -2.391V # Test item 2-15- 3- 8

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.039V, neg = -0.040V # Test item 2-15- 3- 9

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.2% >> pos = 0.743V, neg = -0.822V # Test item 2-15- 3-10

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.040V, neg = -0.040V # Test item 2-15- 3-11

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.8% >> pos = 0.058V, neg = -0.138V # Test item 2-15- 3-12

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.038V, neg = 0.038V # Test item 2-15- 3-13

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.702 V, 20.3% >> pos = 2.390V, neg = -2.313V # Test item 2-15- 3-14

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.038V, neg = 0.038V # Test item 2-15- 3-15

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.2% >> pos = 0.821V, neg = -0.744V # Test item 2-15- 3-16

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.038V, neg = 0.038V # Test item 2-15- 3-17

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.9% >> pos = 0.136V, neg = -0.060V # Test item 2-15- 3-18

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.038V, neg = -0.038V # Test item 2-15- 3-19

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.702 V, 20.4% >> pos = 2.312V, neg = -2.389V # Test item 2-15- 3-20

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.038V, neg = -0.038V # Test item 2-15- 3-21

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.2% >> pos = 0.744V, neg = -0.821V # Test item 2-15- 3-22

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.038V, neg = -0.038V # Test item 2-15- 3-23

T AMCA: MESE 2312: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.9% >> pos = 0.059V, neg = -0.136V # Test item 2-15- 3-24

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.032V, neg = 0.032V # Test item 3-15- 3- 1

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.719 V, 16.9% >> pos = 2.392V, neg = -2.327V # Test item 3-15- 3- 2

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.032V, neg = 0.032V # Test item 3-15- 3- 3

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.570 V, 18.8% >> pos = 0.817V, neg = -0.753V # Test item 3-15- 3- 4

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.032V, neg = 0.032V # Test item 3-15- 3- 5

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 17.8% >> pos = 0.130V, neg = -0.066V # Test item 3-15- 3- 6

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.032V, neg = -0.032V # Test item 3-15- 3- 7

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.718 V, 17.1% >> pos = 2.327V, neg = -2.391V # Test item 3-15- 3- 8

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.032V, neg = -0.032V # Test item 3-15- 3- 9

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.570 V, 18.8% >> pos = 0.753V, neg = -0.817V # Test item 3-15- 3-10

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.032V, neg = -0.032V # Test item 3-15- 3-11

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 17.7% >> pos = 0.066V, neg = -0.130V # Test item 3-15- 3-12

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.030V, neg = 0.030V # Test item 3-15- 3-13

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.717 V, 17.3% >> pos = 2.388V, neg = -2.328V # Test item 3-15- 3-14

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.030V, neg = 0.030V # Test item 3-15- 3-15

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.569 V, 19.1% >> pos = 0.815V, neg = -0.755V # Test item 3-15- 3-16

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.030V, neg = 0.030V # Test item 3-15- 3-17

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.1% >> pos = 0.128V, neg = -0.068V # Test item 3-15- 3-18

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.030V, neg = -0.030V # Test item 3-15- 3-19

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.718 V, 17.1% >> pos = 2.329V, neg = -2.389V # Test item 3-15- 3-20

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.030V, neg = -0.030V # Test item 3-15- 3-21

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.570 V, 19.0% >> pos = 0.755V, neg = -0.815V # Test item 3-15- 3-22

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.030V, neg = -0.030V # Test item 3-15- 3-23

T AMCA: MESE 2313: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.0% >> pos = 0.068V, neg = -0.128V # Test item 3-15- 3-24

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 4-15- 3- 1

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.728 V, 14.9% >> pos = 2.397V, neg = -2.331V # Test item 4-15- 3- 2

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 4-15- 3- 3

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.573 V, 16.8% >> pos = 0.819V, neg = -0.754V # Test item 4-15- 3- 4

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 4-15- 3- 5

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.197 V, 15.7% >> pos = 0.131V, neg = -0.066V # Test item 4-15- 3- 6

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 4-15- 3- 7

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.728 V, 15.0% >> pos = 2.331V, neg = -2.397V # Test item 4-15- 3- 8

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 4-15- 3- 9

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.573 V, 16.8% >> pos = 0.754V, neg = -0.820V # Test item 4-15- 3-10

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 4-15- 3-11

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.197 V, 15.9% >> pos = 0.065V, neg = -0.131V # Test item 4-15- 3-12

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 4-15- 3-13

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.728 V, 15.1% >> pos = 2.397V, neg = -2.330V # Test item 4-15- 3-14

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 4-15- 3-15

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.573 V, 17.0% >> pos = 0.820V, neg = -0.753V # Test item 4-15- 3-16

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 4-15- 3-17

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.197 V, 15.8% >> pos = 0.132V, neg = -0.065V # Test item 4-15- 3-18

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 4-15- 3-19

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.728 V, 15.0% >> pos = 2.331V, neg = -2.397V # Test item 4-15- 3-20

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 4-15- 3-21

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.573 V, 17.0% >> pos = 0.753V, neg = -0.820V # Test item 4-15- 3-22

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 4-15- 3-23

T AMCA: MESE 2314: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.197 V, 15.7% >> pos = 0.065V, neg = -0.132V # Test item 4-15- 3-24

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.032V, neg = 0.032V # Test item 5-15- 3- 1

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.696 V, 21.6% >> pos = 2.380V, neg = -2.316V # Test item 5-15- 3- 2

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.032V, neg = 0.032V # Test item 5-15- 3- 3

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.7% >> pos = 0.813V, neg = -0.749V # Test item 5-15- 3- 4

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.032V, neg = 0.032V # Test item 5-15- 3- 5

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 22.5% >> pos = 0.130V, neg = -0.065V # Test item 5-15- 3- 6

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.032V, neg = -0.032V # Test item 5-15- 3- 7

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.696 V, 21.6% >> pos = 2.316V, neg = -2.380V # Test item 5-15- 3- 8

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.032V, neg = -0.032V # Test item 5-15- 3- 9

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.7% >> pos = 0.749V, neg = -0.813V # Test item 5-15- 3-10

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.032V, neg = -0.032V # Test item 5-15- 3-11

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 22.5% >> pos = 0.065V, neg = -0.130V # Test item 5-15- 3-12

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.032V, neg = 0.032V # Test item 5-15- 3-13

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.696 V, 21.7% >> pos = 2.380V, neg = -2.316V # Test item 5-15- 3-14

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.032V, neg = 0.032V # Test item 5-15- 3-15

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.562 V, 23.5% >> pos = 0.814V, neg = -0.749V # Test item 5-15- 3-16

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.032V, neg = 0.032V # Test item 5-15- 3-17

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 22.1% >> pos = 0.130V, neg = -0.065V # Test item 5-15- 3-18

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.032V, neg = -0.032V # Test item 5-15- 3-19

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.698 V, 21.3% >> pos = 2.316V, neg = -2.381V # Test item 5-15- 3-20

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.032V, neg = -0.032V # Test item 5-15- 3-21

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.563 V, 23.3% >> pos = 0.749V, neg = -0.814V # Test item 5-15- 3-22

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.032V, neg = -0.032V # Test item 5-15- 3-23

T AMCA: MESE 2315: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 22.0% >> pos = 0.065V, neg = -0.130V # Test item 5-15- 3-24

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.030V, neg = 0.030V # Test item 6-15- 3- 1

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.715 V, 17.8% >> pos = 2.388V, neg = -2.327V # Test item 6-15- 3- 2

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.030V, neg = 0.030V # Test item 6-15- 3- 3

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 20.2% >> pos = 0.814V, neg = -0.754V # Test item 6-15- 3- 4

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.030V, neg = 0.030V # Test item 6-15- 3- 5

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.4% >> pos = 0.128V, neg = -0.068V # Test item 6-15- 3- 6

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.030V, neg = -0.030V # Test item 6-15- 3- 7

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.715 V, 17.8% >> pos = 2.327V, neg = -2.388V # Test item 6-15- 3- 8

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.030V, neg = -0.030V # Test item 6-15- 3- 9

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 20.2% >> pos = 0.754V, neg = -0.814V # Test item 6-15- 3-10

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.030V, neg = -0.030V # Test item 6-15- 3-11

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.4% >> pos = 0.068V, neg = -0.128V # Test item 6-15- 3-12

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.029V, neg = 0.029V # Test item 6-15- 3-13

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.715 V, 17.8% >> pos = 2.387V, neg = -2.328V # Test item 6-15- 3-14

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.029V, neg = 0.029V # Test item 6-15- 3-15

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 19.9% >> pos = 0.814V, neg = -0.755V # Test item 6-15- 3-16

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.029V, neg = 0.029V # Test item 6-15- 3-17

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.4% >> pos = 0.128V, neg = -0.069V # Test item 6-15- 3-18

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.029V, neg = -0.029V # Test item 6-15- 3-19

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.714 V, 17.9% >> pos = 2.328V, neg = -2.387V # Test item 6-15- 3-20

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.029V, neg = -0.029V # Test item 6-15- 3-21

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 19.9% >> pos = 0.755V, neg = -0.814V # Test item 6-15- 3-22

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.029V, neg = -0.029V # Test item 6-15- 3-23

T AMCA: MESE 2316: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.4% >> pos = 0.069V, neg = -0.128V # Test item 6-15- 3-24

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.028V, neg = 0.028V # Test item 7-15- 3- 1

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.699 V, 21.1% >> pos = 2.378V, neg = -2.321V # Test item 7-15- 3- 2

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.028V, neg = 0.028V # Test item 7-15- 3- 3

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.563 V, 23.0% >> pos = 0.810V, neg = -0.754V # Test item 7-15- 3- 4

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.028V, neg = 0.028V # Test item 7-15- 3- 5

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.8% >> pos = 0.126V, neg = -0.070V # Test item 7-15- 3- 6

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.028V, neg = -0.028V # Test item 7-15- 3- 7

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.700 V, 20.9% >> pos = 2.322V, neg = -2.378V # Test item 7-15- 3- 8

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.028V, neg = -0.028V # Test item 7-15- 3- 9

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.563 V, 23.0% >> pos = 0.754V, neg = -0.810V # Test item 7-15- 3-10

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.028V, neg = -0.028V # Test item 7-15- 3-11

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.8% >> pos = 0.070V, neg = -0.126V # Test item 7-15- 3-12

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.027V, neg = 0.027V # Test item 7-15- 3-13

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.700 V, 20.9% >> pos = 2.377V, neg = -2.323V # Test item 7-15- 3-14

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.027V, neg = 0.027V # Test item 7-15- 3-15

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.563 V, 22.8% >> pos = 0.809V, neg = -0.755V # Test item 7-15- 3-16

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.027V, neg = 0.027V # Test item 7-15- 3-17

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.7% >> pos = 0.125V, neg = -0.071V # Test item 7-15- 3-18

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 7-15- 3-19

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.699 V, 21.0% >> pos = 2.323V, neg = -2.377V # Test item 7-15- 3-20

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 7-15- 3-21

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.563 V, 22.9% >> pos = 0.755V, neg = -0.809V # Test item 7-15- 3-22

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 7-15- 3-23

T AMCA: MESE 2317: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.8% >> pos = 0.071V, neg = -0.125V # Test item 7-15- 3-24

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 8-15- 3- 1

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.716 V, 17.5% >> pos = 2.391V, neg = -2.325V # Test item 8-15- 3- 2

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 8-15- 3- 3

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.569 V, 19.4% >> pos = 0.818V, neg = -0.751V # Test item 8-15- 3- 4

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 8-15- 3- 5

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 17.8% >> pos = 0.132V, neg = -0.065V # Test item 8-15- 3- 6

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 8-15- 3- 7

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.717 V, 17.2% >> pos = 2.325V, neg = -2.392V # Test item 8-15- 3- 8

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 8-15- 3- 9

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.569 V, 19.3% >> pos = 0.751V, neg = -0.818V # Test item 8-15- 3-10

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 8-15- 3-11

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 17.8% >> pos = 0.065V, neg = -0.132V # Test item 8-15- 3-12

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 8-15- 3-13

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.716 V, 17.6% >> pos = 2.391V, neg = -2.325V # Test item 8-15- 3-14

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 8-15- 3-15

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.569 V, 19.5% >> pos = 0.817V, neg = -0.752V # Test item 8-15- 3-16

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.033V, neg = 0.033V # Test item 8-15- 3-17

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.0% >> pos = 0.131V, neg = -0.065V # Test item 8-15- 3-18

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 8-15- 3-19

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.716 V, 17.6% >> pos = 2.325V, neg = -2.391V # Test item 8-15- 3-20

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 8-15- 3-21

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.569 V, 19.5% >> pos = 0.752V, neg = -0.817V # Test item 8-15- 3-22

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.033V, neg = -0.033V # Test item 8-15- 3-23

T AMCA: MESE 2318: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.1% >> pos = 0.065V, neg = -0.131V # Test item 8-15- 3-24

T AMCA: MESE 2311: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.5% # Test item 1-16- 1- 1

T AMCA: MESE 2311: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.6% # Test item 1-16- 1- 2

T AMCA: MESE 2311: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.994 V, 2.0% # Test item 1-16- 1- 3

T AMCA: MESE 2311: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.4% # Test item 1-16- 1- 4

T AMCA: MESE 2311: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.6% # Test item 1-16- 1- 5

T AMCA: MESE 2311: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.994 V, 2.0% # Test item 1-16- 1- 6

T AMCA: MESE 2311: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.3% # Test item 1-16- 1- 7

T AMCA: MESE 2311: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 8.4% # Test item 1-16- 1- 8

T AMCA: MESE 2311: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.994 V, 2.1% # Test item 1-16- 1- 9

T AMCA: MESE 2311: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.011 V, 3.5% # Test item 1-16- 1-10

T AMCA: MESE 2311: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.8% # Test item 1-16- 1-11

T AMCA: MESE 2311: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.994 V, 2.0% # Test item 1-16- 1-12

T AMCA: MESE 2312: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.8% # Test item 2-16- 1- 1

T AMCA: MESE 2312: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.1% # Test item 2-16- 1- 2

T AMCA: MESE 2312: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.991 V, 2.9% # Test item 2-16- 1- 3

T AMCA: MESE 2312: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.8% # Test item 2-16- 1- 4

T AMCA: MESE 2312: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.4% # Test item 2-16- 1- 5

T AMCA: MESE 2312: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.991 V, 3.0% # Test item 2-16- 1- 6

T AMCA: MESE 2312: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.8% # Test item 2-16- 1- 7

T AMCA: MESE 2312: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.1% # Test item 2-16- 1- 8

T AMCA: MESE 2312: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.991 V, 2.9% # Test item 2-16- 1- 9

T AMCA: MESE 2312: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.8% # Test item 2-16- 1-10

T AMCA: MESE 2312: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.1% # Test item 2-16- 1-11

T AMCA: MESE 2312: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.991 V, 3.0% # Test item 2-16- 1-12

T AMCA: MESE 2313: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.4% # Test item 3-16- 1- 1

T AMCA: MESE 2313: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.2% # Test item 3-16- 1- 2

T AMCA: MESE 2313: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.996 V, 1.3% # Test item 3-16- 1- 3

T AMCA: MESE 2313: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.4% # Test item 3-16- 1- 4

T AMCA: MESE 2313: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.9% # Test item 3-16- 1- 5

T AMCA: MESE 2313: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.996 V, 1.2% # Test item 3-16- 1- 6

T AMCA: MESE 2313: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.5% # Test item 3-16- 1- 7

T AMCA: MESE 2313: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.9% # Test item 3-16- 1- 8

T AMCA: MESE 2313: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.996 V, 1.3% # Test item 3-16- 1- 9

T AMCA: MESE 2313: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.4% # Test item 3-16- 1-10

T AMCA: MESE 2313: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.1% # Test item 3-16- 1-11

T AMCA: MESE 2313: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.996 V, 1.3% # Test item 3-16- 1-12

T AMCA: MESE 2314: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.3% # Test item 4-16- 1- 1

T AMCA: MESE 2314: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.4% # Test item 4-16- 1- 2

T AMCA: MESE 2314: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.992 V, 2.7% # Test item 4-16- 1- 3

T AMCA: MESE 2314: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.4% # Test item 4-16- 1- 4

T AMCA: MESE 2314: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.6% # Test item 4-16- 1- 5

T AMCA: MESE 2314: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.992 V, 2.7% # Test item 4-16- 1- 6

T AMCA: MESE 2314: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.5% # Test item 4-16- 1- 7

T AMCA: MESE 2314: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.4% # Test item 4-16- 1- 8

T AMCA: MESE 2314: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.992 V, 2.7% # Test item 4-16- 1- 9

T AMCA: MESE 2314: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.4% # Test item 4-16- 1-10

T AMCA: MESE 2314: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.6% # Test item 4-16- 1-11

T AMCA: MESE 2314: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.992 V, 2.7% # Test item 4-16- 1-12

T AMCA: MESE 2315: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.4% # Test item 5-16- 1- 1

T AMCA: MESE 2315: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 12.0% # Test item 5-16- 1- 2

T AMCA: MESE 2315: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.987 V, 4.3% # Test item 5-16- 1- 3

T AMCA: MESE 2315: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.4% # Test item 5-16- 1- 4

T AMCA: MESE 2315: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 12.5% # Test item 5-16- 1- 5

T AMCA: MESE 2315: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.987 V, 4.4% # Test item 5-16- 1- 6

T AMCA: MESE 2315: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.4% # Test item 5-16- 1- 7

T AMCA: MESE 2315: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 12.1% # Test item 5-16- 1- 8

T AMCA: MESE 2315: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.987 V, 4.3% # Test item 5-16- 1- 9

T AMCA: MESE 2315: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.4% # Test item 5-16- 1-10

T AMCA: MESE 2315: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 12.5% # Test item 5-16- 1-11

T AMCA: MESE 2315: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.987 V, 4.4% # Test item 5-16- 1-12

T AMCA: MESE 2316: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.3% # Test item 6-16- 1- 1

T AMCA: MESE 2316: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 12.1% # Test item 6-16- 1- 2

T AMCA: MESE 2316: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.990 V, 3.4% # Test item 6-16- 1- 3

T AMCA: MESE 2316: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.3% # Test item 6-16- 1- 4

T AMCA: MESE 2316: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 12.2% # Test item 6-16- 1- 5

T AMCA: MESE 2316: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.990 V, 3.4% # Test item 6-16- 1- 6

T AMCA: MESE 2316: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.3% # Test item 6-16- 1- 7

T AMCA: MESE 2316: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 12.0% # Test item 6-16- 1- 8

T AMCA: MESE 2316: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.990 V, 3.4% # Test item 6-16- 1- 9

T AMCA: MESE 2316: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.3% # Test item 6-16- 1-10

T AMCA: MESE 2316: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.012 V, 12.2% # Test item 6-16- 1-11

T AMCA: MESE 2316: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.990 V, 3.5% # Test item 6-16- 1-12

T AMCA: MESE 2317: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.0% # Test item 7-16- 1- 1

T AMCA: MESE 2317: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.013 V, 13.0% # Test item 7-16- 1- 2

T AMCA: MESE 2317: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.990 V, 3.3% # Test item 7-16- 1- 3

T AMCA: MESE 2317: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.0% # Test item 7-16- 1- 4

T AMCA: MESE 2317: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.013 V, 13.1% # Test item 7-16- 1- 5

T AMCA: MESE 2317: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.990 V, 3.3% # Test item 7-16- 1- 6

T AMCA: MESE 2317: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.0% # Test item 7-16- 1- 7

T AMCA: MESE 2317: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.013 V, 12.8% # Test item 7-16- 1- 8

T AMCA: MESE 2317: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.990 V, 3.3% # Test item 7-16- 1- 9

T AMCA: MESE 2317: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 5.0% # Test item 7-16- 1-10

T AMCA: MESE 2317: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.013 V, 12.8% # Test item 7-16- 1-11

T AMCA: MESE 2317: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.990 V, 3.4% # Test item 7-16- 1-12

T AMCA: MESE 2318: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.4% # Test item 8-16- 1- 1

T AMCA: MESE 2318: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.6% # Test item 8-16- 1- 2

T AMCA: MESE 2318: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.992 V, 2.8% # Test item 8-16- 1- 3

T AMCA: MESE 2318: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.4% # Test item 8-16- 1- 4

T AMCA: MESE 2318: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.7% # Test item 8-16- 1- 5

T AMCA: MESE 2318: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.992 V, 2.7% # Test item 8-16- 1- 6

T AMCA: MESE 2318: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.3% # Test item 8-16- 1- 7

T AMCA: MESE 2318: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.8% # Test item 8-16- 1- 8

T AMCA: MESE 2318: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.992 V, 2.7% # Test item 8-16- 1- 9

T AMCA: MESE 2318: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.3% # Test item 8-16- 1-10

T AMCA: MESE 2318: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.4% # Test item 8-16- 1-11

T AMCA: MESE 2318: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.992 V, 2.7% # Test item 8-16- 1-12

T AMCA: MESE 2311: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9875.050 Ohm, 12.5% >> MV = 1.781V, offset = -0.194V # Test item 1-16- 2- 1

T AMCA: MESE 2311: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.732 Ohm, 21.9% >> MV = 0.171V, offset = 0.001V # Test item 1-16- 2- 2

T AMCA: MESE 2311: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.186 Ohm, 18.1% >> MV = 0.135V, offset = 0.003V # Test item 1-16- 2- 3

T AMCA: MESE 2311: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9880.715 Ohm, 11.9% >> MV = 1.779V, offset = -0.198V # Test item 1-16- 2- 4

T AMCA: MESE 2311: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.110 Ohm, 15.3% >> MV = 0.172V, offset = 0.001V # Test item 1-16- 2- 5

T AMCA: MESE 2311: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.563 Ohm, 9.7% >> MV = 0.136V, offset = 0.003V # Test item 1-16- 2- 6

T AMCA: MESE 2312: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9855.538 Ohm, 14.4% >> MV = 1.749V, offset = -0.222V # Test item 2-16- 2- 1

T AMCA: MESE 2312: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.648 Ohm, 23.3% >> MV = 0.171V, offset = 0.001V # Test item 2-16- 2- 2

T AMCA: MESE 2312: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.312 Ohm, 15.3% >> MV = 0.136V, offset = 0.003V # Test item 2-16- 2- 3

T AMCA: MESE 2312: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9851.132 Ohm, 14.9% >> MV = 1.750V, offset = -0.221V # Test item 2-16- 2- 4

T AMCA: MESE 2312: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.816 Ohm, 20.4% >> MV = 0.172V, offset = 0.001V # Test item 2-16- 2- 5

T AMCA: MESE 2312: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.353 Ohm, 14.4% >> MV = 0.136V, offset = 0.003V # Test item 2-16- 2- 6

T AMCA: MESE 2313: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9880.085 Ohm, 12.0% >> MV = 1.788V, offset = -0.188V # Test item 3-16- 2- 1

T AMCA: MESE 2313: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.194 Ohm, 13.9% >> MV = 0.170V, offset = -0.001V # Test item 3-16- 2- 2

T AMCA: MESE 2313: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.563 Ohm, 9.7% >> MV = 0.135V, offset = 0.001V # Test item 3-16- 2- 3

T AMCA: MESE 2313: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9879.456 Ohm, 12.1% >> MV = 1.783V, offset = -0.193V # Test item 3-16- 2- 4

T AMCA: MESE 2313: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.942 Ohm, 18.2% >> MV = 0.170V, offset = -0.001V # Test item 3-16- 2- 5

T AMCA: MESE 2313: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.521 Ohm, 10.6% >> MV = 0.135V, offset = 0.001V # Test item 3-16- 2- 6

T AMCA: MESE 2314: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9887.639 Ohm, 11.2% >> MV = 1.797V, offset = -0.181V # Test item 4-16- 2- 1

T AMCA: MESE 2314: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.606 Ohm, 24.0% >> MV = 0.172V, offset = 0.002V # Test item 4-16- 2- 2

T AMCA: MESE 2314: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.270 Ohm, 16.2% >> MV = 0.136V, offset = 0.004V # Test item 4-16- 2- 3

T AMCA: MESE 2314: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9887.639 Ohm, 11.2% >> MV = 1.794V, offset = -0.183V # Test item 4-16- 2- 4

T AMCA: MESE 2314: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.816 Ohm, 20.4% >> MV = 0.173V, offset = 0.002V # Test item 4-16- 2- 5

T AMCA: MESE 2314: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.312 Ohm, 15.3% >> MV = 0.137V, offset = 0.004V # Test item 4-16- 2- 6

T AMCA: MESE 2315: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9875.680 Ohm, 12.4% >> MV = 1.792V, offset = -0.183V # Test item 5-16- 2- 1

T AMCA: MESE 2315: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.774 Ohm, 21.1% >> MV = 0.177V, offset = 0.006V # Test item 5-16- 2- 2

T AMCA: MESE 2315: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.228 Ohm, 17.2% >> MV = 0.140V, offset = 0.008V # Test item 5-16- 2- 3

T AMCA: MESE 2315: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9885.750 Ohm, 11.4% >> MV = 1.792V, offset = -0.185V # Test item 5-16- 2- 4

T AMCA: MESE 2315: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.404 Ohm, 10.3% >> MV = 0.179V, offset = 0.007V # Test item 5-16- 2- 5

T AMCA: MESE 2315: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.815 Ohm, 4.1% >> MV = 0.142V, offset = 0.008V # Test item 5-16- 2- 6

T AMCA: MESE 2316: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9871.273 Ohm, 12.9% >> MV = 1.797V, offset = -0.178V # Test item 6-16- 2- 1

T AMCA: MESE 2316: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.152 Ohm, 14.6% >> MV = 0.176V, offset = 0.005V # Test item 6-16- 2- 2

T AMCA: MESE 2316: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.563 Ohm, 9.7% >> MV = 0.140V, offset = 0.006V # Test item 6-16- 2- 3

T AMCA: MESE 2316: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9872.532 Ohm, 12.7% >> MV = 1.797V, offset = -0.177V # Test item 6-16- 2- 4

T AMCA: MESE 2316: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.942 Ohm, 18.2% >> MV = 0.175V, offset = 0.005V # Test item 6-16- 2- 5

T AMCA: MESE 2316: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.395 Ohm, 13.4% >> MV = 0.139V, offset = 0.006V # Test item 6-16- 2- 6

T AMCA: MESE 2317: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9848.614 Ohm, 15.1% >> MV = 1.784V, offset = -0.186V # Test item 7-16- 2- 1

T AMCA: MESE 2317: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.152 Ohm, 14.6% >> MV = 0.174V, offset = 0.003V # Test item 7-16- 2- 2

T AMCA: MESE 2317: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.731 Ohm, 6.0% >> MV = 0.139V, offset = 0.005V # Test item 7-16- 2- 3

T AMCA: MESE 2317: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9844.838 Ohm, 15.5% >> MV = 1.783V, offset = -0.186V # Test item 7-16- 2- 4

T AMCA: MESE 2317: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.404 Ohm, 10.3% >> MV = 0.175V, offset = 0.002V # Test item 7-16- 2- 5

T AMCA: MESE 2317: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.815 Ohm, 4.1% >> MV = 0.139V, offset = 0.005V # Test item 7-16- 2- 6

T AMCA: MESE 2318: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9842.319 Ohm, 15.8% >> MV = 1.756V, offset = -0.213V # Test item 8-16- 2- 1

T AMCA: MESE 2318: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.732 Ohm, 21.9% >> MV = 0.174V, offset = 0.003V # Test item 8-16- 2- 2

T AMCA: MESE 2318: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.353 Ohm, 14.4% >> MV = 0.138V, offset = 0.005V # Test item 8-16- 2- 3

T AMCA: MESE 2318: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9846.097 Ohm, 15.4% >> MV = 1.752V, offset = -0.218V # Test item 8-16- 2- 4

T AMCA: MESE 2318: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.900 Ohm, 19.0% >> MV = 0.174V, offset = 0.003V # Test item 8-16- 2- 5

T AMCA: MESE 2318: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.395 Ohm, 13.4% >> MV = 0.138V, offset = 0.005V # Test item 8-16- 2- 6

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = 0.076V, neg = 0.076V # Test item 1-16- 3- 1

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.237 V, 11.7% >> pos = 1.696V, neg = -1.541V # Test item 1-16- 3- 2

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = 0.053V, neg = 0.054V # Test item 1-16- 3- 3

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.592 V, 4.8% >> pos = 0.851V, neg = -0.741V # Test item 1-16- 3- 4

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.043V, neg = 0.043V # Test item 1-16- 3- 5

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.800 V, 0.4% >> pos = 0.443V, neg = -0.357V # Test item 1-16- 3- 6

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = 0.035V, neg = 0.034V # Test item 1-16- 3- 7

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 7.6% >> pos = 0.158V, neg = -0.090V # Test item 1-16- 3- 8

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.032V, neg = 0.032V # Test item 1-16- 3- 9

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 3.7% >> pos = 0.082V, neg = -0.017V # Test item 1-16- 3-10

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.018V, neg = 0.018V # Test item 1-16- 3-11

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.152 V, 14.9% >> pos = 1.599V, neg = -1.554V # Test item 1-16- 3-12

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = 0.010V, neg = 0.011V # Test item 1-16- 3-13

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.570 V, 18.9% >> pos = 0.781V, neg = -0.789V # Test item 1-16- 3-14

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = -0.014V, neg = -0.014V # Test item 1-16- 3-15

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.784 V, 19.7% >> pos = 0.379V, neg = -0.405V # Test item 1-16- 3-16

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = -0.021V, neg = -0.022V # Test item 1-16- 3-17

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.247 V, 12.0% >> pos = 0.102V, neg = -0.145V # Test item 1-16- 3-18

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.022V, neg = -0.022V # Test item 1-16- 3-19

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 5.2% >> pos = 0.025V, neg = -0.074V # Test item 1-16- 3-20

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = 0.079V, neg = 0.078V # Test item 1-16- 3-21

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.237 V, 11.7% >> pos = 1.701V, neg = -1.536V # Test item 1-16- 3-22

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.6% >> pos = 0.054V, neg = 0.052V # Test item 1-16- 3-23

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.595 V, 3.0% >> pos = 0.851V, neg = -0.745V # Test item 1-16- 3-24

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.042V, neg = 0.042V # Test item 1-16- 3-25

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.798 V, 2.8% >> pos = 0.444V, neg = -0.354V # Test item 1-16- 3-26

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.035V, neg = 0.035V # Test item 1-16- 3-27

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 6.4% >> pos = 0.159V, neg = -0.090V # Test item 1-16- 3-28

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.032V, neg = 0.031V # Test item 1-16- 3-29

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 2.0% >> pos = 0.082V, neg = -0.018V # Test item 1-16- 3-30

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.0% >> pos = 0.022V, neg = 0.019V # Test item 1-16- 3-31

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.151 V, 15.5% >> pos = 1.592V, neg = -1.558V # Test item 1-16- 3-32

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = -0.002V, neg = -0.002V # Test item 1-16- 3-33

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.571 V, 17.9% >> pos = 0.783V, neg = -0.789V # Test item 1-16- 3-34

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.5% >> pos = -0.013V, neg = -0.015V # Test item 1-16- 3-35

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.786 V, 17.3% >> pos = 0.380V, neg = -0.407V # Test item 1-16- 3-36

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = -0.022V, neg = -0.023V # Test item 1-16- 3-37

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 7.2% >> pos = 0.102V, neg = -0.146V # Test item 1-16- 3-38

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = -0.025V, neg = -0.024V # Test item 1-16- 3-39

T AMCA: MESE 2311: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 4.6% >> pos = 0.025V, neg = -0.074V # Test item 1-16- 3-40

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 4.4% >> pos = 0.098V, neg = 0.103V # Test item 2-16- 3- 1

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.240 V, 12.6% >> pos = 1.722V, neg = -1.519V # Test item 2-16- 3- 2

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.4% >> pos = 0.066V, neg = 0.068V # Test item 2-16- 3- 3

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.602 V, 1.3% >> pos = 0.867V, neg = -0.735V # Test item 2-16- 3- 4

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.051V, neg = 0.050V # Test item 2-16- 3- 5

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.801 V, 0.6% >> pos = 0.451V, neg = -0.349V # Test item 2-16- 3- 6

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.7% >> pos = 0.041V, neg = 0.044V # Test item 2-16- 3- 7

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 3.2% >> pos = 0.167V, neg = -0.083V # Test item 2-16- 3- 8

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.4% >> pos = 0.038V, neg = 0.039V # Test item 2-16- 3- 9

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.094 V, 31.0% >> pos = 0.088V, neg = -0.005V # Test item 2-16- 3-10

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.005 V, 5.1% >> pos = 0.029V, neg = 0.034V # Test item 2-16- 3-11

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.160 V, 12.5% >> pos = 1.614V, neg = -1.546V # Test item 2-16- 3-12

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.4% >> pos = 0.016V, neg = 0.019V # Test item 2-16- 3-13

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.578 V, 14.0% >> pos = 0.789V, neg = -0.788V # Test item 2-16- 3-14

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.4% >> pos = -0.016V, neg = -0.014V # Test item 2-16- 3-15

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.790 V, 12.7% >> pos = 0.381V, neg = -0.409V # Test item 2-16- 3-16

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = -0.024V, neg = -0.025V # Test item 2-16- 3-17

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 0.1% >> pos = 0.100V, neg = -0.150V # Test item 2-16- 3-18

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.011 V, 11.1% >> pos = -0.018V, neg = -0.029V # Test item 2-16- 3-19

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 24.1% >> pos = 0.022V, neg = -0.083V # Test item 2-16- 3-20

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.100V, neg = 0.100V # Test item 2-16- 3-21

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.243 V, 13.6% >> pos = 1.722V, neg = -1.522V # Test item 2-16- 3-22

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.0% >> pos = 0.066V, neg = 0.064V # Test item 2-16- 3-23

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.601 V, 0.4% >> pos = 0.866V, neg = -0.734V # Test item 2-16- 3-24

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = 0.052V, neg = 0.052V # Test item 2-16- 3-25

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.801 V, 1.0% >> pos = 0.452V, neg = -0.349V # Test item 2-16- 3-26

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.041V, neg = 0.041V # Test item 2-16- 3-27

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 2.1% >> pos = 0.167V, neg = -0.083V # Test item 2-16- 3-28

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.039V, neg = 0.039V # Test item 2-16- 3-29

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.6% >> pos = 0.087V, neg = -0.013V # Test item 2-16- 3-30

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.6% >> pos = 0.035V, neg = 0.029V # Test item 2-16- 3-31

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.170 V, 9.4% >> pos = 1.615V, neg = -1.555V # Test item 2-16- 3-32

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.5% >> pos = -0.002V, neg = 0.001V # Test item 2-16- 3-33

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.593 V, 4.5% >> pos = 0.791V, neg = -0.801V # Test item 2-16- 3-34

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.0% >> pos = -0.014V, neg = -0.016V # Test item 2-16- 3-35

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.792 V, 10.4% >> pos = 0.381V, neg = -0.411V # Test item 2-16- 3-36

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.3% >> pos = -0.025V, neg = -0.025V # Test item 2-16- 3-37

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 9.4% >> pos = 0.099V, neg = -0.148V # Test item 2-16- 3-38

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.2% >> pos = -0.029V, neg = -0.028V # Test item 2-16- 3-39

T AMCA: MESE 2312: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 2.2% >> pos = 0.022V, neg = -0.078V # Test item 2-16- 3-40

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.010 V, 10.1% >> pos = 0.048V, neg = 0.058V # Test item 3-16- 3- 1

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.170 V, 9.3% >> pos = 1.638V, neg = -1.532V # Test item 3-16- 3- 2

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.041V, neg = 0.041V # Test item 3-16- 3- 3

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.579 V, 12.9% >> pos = 0.830V, neg = -0.749V # Test item 3-16- 3- 4

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.0% >> pos = 0.036V, neg = 0.034V # Test item 3-16- 3- 5

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.787 V, 16.0% >> pos = 0.427V, neg = -0.360V # Test item 3-16- 3- 6

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.032V, neg = 0.032V # Test item 3-16- 3- 7

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 5.0% >> pos = 0.156V, neg = -0.092V # Test item 3-16- 3- 8

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = 0.031V, neg = 0.030V # Test item 3-16- 3- 9

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 11.7% >> pos = 0.080V, neg = -0.018V # Test item 3-16- 3-10

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = -0.005V, neg = -0.005V # Test item 3-16- 3-11

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.223 V, 7.3% >> pos = 1.606V, neg = -1.617V # Test item 3-16- 3-12

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = -0.003V, neg = -0.003V # Test item 3-16- 3-13

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.592 V, 4.9% >> pos = 0.777V, neg = -0.815V # Test item 3-16- 3-14

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.6% >> pos = -0.025V, neg = -0.023V # Test item 3-16- 3-15

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.803 V, 4.0% >> pos = 0.379V, neg = -0.424V # Test item 3-16- 3-16

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = -0.027V, neg = -0.025V # Test item 3-16- 3-17

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 5.8% >> pos = 0.099V, neg = -0.150V # Test item 3-16- 3-18

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = -0.028V, neg = -0.029V # Test item 3-16- 3-19

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.101 V, 2.6% >> pos = 0.022V, neg = -0.078V # Test item 3-16- 3-20

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.051V, neg = 0.051V # Test item 3-16- 3-21

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.166 V, 10.7% >> pos = 1.634V, neg = -1.532V # Test item 3-16- 3-22

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = 0.038V, neg = 0.039V # Test item 3-16- 3-23

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.575 V, 15.4% >> pos = 0.829V, neg = -0.747V # Test item 3-16- 3-24

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = 0.032V, neg = 0.033V # Test item 3-16- 3-25

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.790 V, 12.8% >> pos = 0.430V, neg = -0.360V # Test item 3-16- 3-26

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.031V, neg = 0.031V # Test item 3-16- 3-27

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 3.7% >> pos = 0.158V, neg = -0.093V # Test item 3-16- 3-28

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = 0.030V, neg = 0.030V # Test item 3-16- 3-29

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.101 V, 4.9% >> pos = 0.082V, neg = -0.019V # Test item 3-16- 3-30

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.8% >> pos = -0.012V, neg = -0.010V # Test item 3-16- 3-31

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.218 V, 5.8% >> pos = 1.603V, neg = -1.615V # Test item 3-16- 3-32

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.3% >> pos = -0.018V, neg = -0.017V # Test item 3-16- 3-33

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.593 V, 4.4% >> pos = 0.777V, neg = -0.815V # Test item 3-16- 3-34

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.8% >> pos = -0.024V, neg = -0.027V # Test item 3-16- 3-35

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.794 V, 7.4% >> pos = 0.370V, neg = -0.424V # Test item 3-16- 3-36

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = -0.027V, neg = -0.027V # Test item 3-16- 3-37

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 0.1% >> pos = 0.098V, neg = -0.152V # Test item 3-16- 3-38

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = -0.028V, neg = -0.028V # Test item 3-16- 3-39

T AMCA: MESE 2313: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.0% >> pos = 0.021V, neg = -0.079V # Test item 3-16- 3-40

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.7% >> pos = 0.058V, neg = 0.060V # Test item 4-16- 3- 1

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.206 V, 1.7% >> pos = 1.663V, neg = -1.542V # Test item 4-16- 3- 2

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.3% >> pos = 0.043V, neg = 0.045V # Test item 4-16- 3- 3

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.591 V, 5.5% >> pos = 0.839V, neg = -0.752V # Test item 4-16- 3- 4

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.5% >> pos = 0.033V, neg = 0.033V # Test item 4-16- 3- 5

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.798 V, 2.7% >> pos = 0.434V, neg = -0.364V # Test item 4-16- 3- 6

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = 0.031V, neg = 0.031V # Test item 4-16- 3- 7

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 5.1% >> pos = 0.156V, neg = -0.093V # Test item 4-16- 3- 8

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.3% >> pos = 0.028V, neg = 0.029V # Test item 4-16- 3- 9

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.4% >> pos = 0.078V, neg = -0.022V # Test item 4-16- 3-10

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.6% >> pos = 0.006V, neg = 0.003V # Test item 4-16- 3-11

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.223 V, 7.2% >> pos = 1.616V, neg = -1.607V # Test item 4-16- 3-12

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.5% >> pos = 0.003V, neg = 0.001V # Test item 4-16- 3-13

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.596 V, 2.4% >> pos = 0.787V, neg = -0.809V # Test item 4-16- 3-14

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = -0.020V, neg = -0.019V # Test item 4-16- 3-15

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.796 V, 4.7% >> pos = 0.378V, neg = -0.418V # Test item 4-16- 3-16

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = -0.023V, neg = -0.023V # Test item 4-16- 3-17

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 7.0% >> pos = 0.101V, neg = -0.148V # Test item 4-16- 3-18

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = -0.026V, neg = -0.025V # Test item 4-16- 3-19

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.095 V, 24.8% >> pos = 0.024V, neg = -0.071V # Test item 4-16- 3-20

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.3% >> pos = 0.060V, neg = 0.056V # Test item 4-16- 3-21

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.202 V, 0.6% >> pos = 1.661V, neg = -1.540V # Test item 4-16- 3-22

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.7% >> pos = 0.044V, neg = 0.042V # Test item 4-16- 3-23

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.588 V, 7.3% >> pos = 0.837V, neg = -0.751V # Test item 4-16- 3-24

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.034V, neg = 0.034V # Test item 4-16- 3-25

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.796 V, 5.5% >> pos = 0.431V, neg = -0.365V # Test item 4-16- 3-26

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.031V, neg = 0.031V # Test item 4-16- 3-27

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 0.7% >> pos = 0.156V, neg = -0.094V # Test item 4-16- 3-28

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.028V, neg = 0.029V # Test item 4-16- 3-29

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 3.8% >> pos = 0.078V, neg = -0.021V # Test item 4-16- 3-30

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.3% >> pos = 0.009V, neg = 0.007V # Test item 4-16- 3-31

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.222 V, 7.0% >> pos = 1.614V, neg = -1.608V # Test item 4-16- 3-32

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.3% >> pos = -0.012V, neg = -0.010V # Test item 4-16- 3-33

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.594 V, 3.9% >> pos = 0.785V, neg = -0.809V # Test item 4-16- 3-34

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = -0.020V, neg = -0.021V # Test item 4-16- 3-35

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.801 V, 1.6% >> pos = 0.381V, neg = -0.421V # Test item 4-16- 3-36

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = -0.023V, neg = -0.023V # Test item 4-16- 3-37

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 2.3% >> pos = 0.102V, neg = -0.148V # Test item 4-16- 3-38

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = -0.025V, neg = -0.025V # Test item 4-16- 3-39

T AMCA: MESE 2314: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 3.7% >> pos = 0.025V, neg = -0.074V # Test item 4-16- 3-40

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.4% >> pos = 0.092V, neg = 0.093V # Test item 5-16- 3- 1

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.243 V, 13.5% >> pos = 1.715V, neg = -1.528V # Test item 5-16- 3- 2

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = 0.060V, neg = 0.060V # Test item 5-16- 3- 3

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.602 V, 1.0% >> pos = 0.858V, neg = -0.743V # Test item 5-16- 3- 4

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = 0.042V, neg = 0.041V # Test item 5-16- 3- 5

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.802 V, 3.0% >> pos = 0.444V, neg = -0.359V # Test item 5-16- 3- 6

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = 0.034V, neg = 0.033V # Test item 5-16- 3- 7

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 3.3% >> pos = 0.158V, neg = -0.091V # Test item 5-16- 3- 8

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.030V, neg = 0.030V # Test item 5-16- 3- 9

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.8% >> pos = 0.080V, neg = -0.020V # Test item 5-16- 3-10

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.1% >> pos = 0.046V, neg = 0.039V # Test item 5-16- 3-11

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.168 V, 10.0% >> pos = 1.623V, neg = -1.545V # Test item 5-16- 3-12

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.021V, neg = 0.021V # Test item 5-16- 3-13

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.580 V, 12.4% >> pos = 0.798V, neg = -0.782V # Test item 5-16- 3-14

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 8.2% >> pos = -0.009V, neg = -0.017V # Test item 5-16- 3-15

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.795 V, 6.8% >> pos = 0.390V, neg = -0.404V # Test item 5-16- 3-16

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = -0.018V, neg = -0.018V # Test item 5-16- 3-17

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 7.2% >> pos = 0.106V, neg = -0.142V # Test item 5-16- 3-18

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = -0.022V, neg = -0.022V # Test item 5-16- 3-19

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 2.3% >> pos = 0.028V, neg = -0.071V # Test item 5-16- 3-20

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.006 V, 6.5% >> pos = 0.096V, neg = 0.102V # Test item 5-16- 3-21

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.241 V, 12.7% >> pos = 1.710V, neg = -1.531V # Test item 5-16- 3-22

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = 0.059V, neg = 0.057V # Test item 5-16- 3-23

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.600 V, 0.3% >> pos = 0.857V, neg = -0.743V # Test item 5-16- 3-24

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = 0.044V, neg = 0.043V # Test item 5-16- 3-25

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.799 V, 1.5% >> pos = 0.441V, neg = -0.358V # Test item 5-16- 3-26

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.1% >> pos = 0.034V, neg = 0.032V # Test item 5-16- 3-27

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 0.9% >> pos = 0.159V, neg = -0.090V # Test item 5-16- 3-28

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.5% >> pos = 0.029V, neg = 0.028V # Test item 5-16- 3-29

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 4.7% >> pos = 0.079V, neg = -0.020V # Test item 5-16- 3-30

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 4.4% >> pos = 0.038V, neg = 0.043V # Test item 5-16- 3-31

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.160 V, 12.5% >> pos = 1.619V, neg = -1.540V # Test item 5-16- 3-32

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.5% >> pos = 0.006V, neg = 0.007V # Test item 5-16- 3-33

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.585 V, 9.3% >> pos = 0.801V, neg = -0.784V # Test item 5-16- 3-34

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = -0.008V, neg = -0.008V # Test item 5-16- 3-35

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.791 V, 11.1% >> pos = 0.385V, neg = -0.406V # Test item 5-16- 3-36

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = -0.018V, neg = -0.017V # Test item 5-16- 3-37

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 0.6% >> pos = 0.107V, neg = -0.143V # Test item 5-16- 3-38

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.9% >> pos = -0.022V, neg = -0.021V # Test item 5-16- 3-39

T AMCA: MESE 2315: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 7.6% >> pos = 0.027V, neg = -0.071V # Test item 5-16- 3-40

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.022 V, 22.2% >> pos = 0.085V, neg = 0.063V # Test item 6-16- 3- 1

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.177 V, 7.3% >> pos = 1.671V, neg = -1.506V # Test item 6-16- 3- 2

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% >> pos = 0.054V, neg = 0.052V # Test item 6-16- 3- 3

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.581 V, 12.2% >> pos = 0.843V, neg = -0.737V # Test item 6-16- 3- 4

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.7% >> pos = 0.037V, neg = 0.041V # Test item 6-16- 3- 5

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.791 V, 11.7% >> pos = 0.434V, neg = -0.357V # Test item 6-16- 3- 6

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = 0.032V, neg = 0.031V # Test item 6-16- 3- 7

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 3.9% >> pos = 0.156V, neg = -0.093V # Test item 6-16- 3- 8

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = 0.028V, neg = 0.028V # Test item 6-16- 3- 9

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 7.3% >> pos = 0.076V, neg = -0.022V # Test item 6-16- 3-10

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = 0.029V, neg = 0.030V # Test item 6-16- 3-11

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.226 V, 8.2% >> pos = 1.643V, neg = -1.583V # Test item 6-16- 3-12

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.2% >> pos = 0.016V, neg = 0.013V # Test item 6-16- 3-13

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.591 V, 5.4% >> pos = 0.799V, neg = -0.792V # Test item 6-16- 3-14

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 3.9% >> pos = -0.006V, neg = -0.010V # Test item 6-16- 3-15

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.799 V, 1.2% >> pos = 0.389V, neg = -0.410V # Test item 6-16- 3-16

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.3% >> pos = -0.018V, neg = -0.019V # Test item 6-16- 3-17

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.252 V, 7.0% >> pos = 0.108V, neg = -0.143V # Test item 6-16- 3-18

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.3% >> pos = -0.016V, neg = -0.022V # Test item 6-16- 3-19

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.5% >> pos = 0.029V, neg = -0.072V # Test item 6-16- 3-20

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.4% >> pos = 0.089V, neg = 0.082V # Test item 6-16- 3-21

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.170 V, 9.4% >> pos = 1.665V, neg = -1.504V # Test item 6-16- 3-22

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.2% >> pos = 0.050V, neg = 0.051V # Test item 6-16- 3-23

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.579 V, 13.3% >> pos = 0.843V, neg = -0.736V # Test item 6-16- 3-24

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.3% >> pos = 0.040V, neg = 0.037V # Test item 6-16- 3-25

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.787 V, 16.0% >> pos = 0.430V, neg = -0.357V # Test item 6-16- 3-26

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = 0.032V, neg = 0.031V # Test item 6-16- 3-27

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 1.0% >> pos = 0.156V, neg = -0.095V # Test item 6-16- 3-28

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = 0.027V, neg = 0.028V # Test item 6-16- 3-29

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.098 V, 9.0% >> pos = 0.077V, neg = -0.021V # Test item 6-16- 3-30

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = 0.032V, neg = 0.031V # Test item 6-16- 3-31

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.224 V, 7.5% >> pos = 1.642V, neg = -1.582V # Test item 6-16- 3-32

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.5% >> pos = 0.002V, neg = 0.005V # Test item 6-16- 3-33

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.594 V, 4.0% >> pos = 0.800V, neg = -0.794V # Test item 6-16- 3-34

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.0% >> pos = -0.011V, neg = -0.010V # Test item 6-16- 3-35

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.799 V, 1.6% >> pos = 0.389V, neg = -0.410V # Test item 6-16- 3-36

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.7% >> pos = -0.017V, neg = -0.019V # Test item 6-16- 3-37

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 2.8% >> pos = 0.107V, neg = -0.142V # Test item 6-16- 3-38

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = -0.021V, neg = -0.022V # Test item 6-16- 3-39

T AMCA: MESE 2316: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.106 V, 30.2% >> pos = 0.030V, neg = -0.076V # Test item 6-16- 3-40

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.7% >> pos = 0.080V, neg = 0.082V # Test item 7-16- 3- 1

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.166 V, 10.7% >> pos = 1.659V, neg = -1.507V # Test item 7-16- 3- 2

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.051V, neg = 0.051V # Test item 7-16- 3- 3

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.580 V, 12.7% >> pos = 0.838V, neg = -0.741V # Test item 7-16- 3- 4

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.3% >> pos = 0.038V, neg = 0.031V # Test item 7-16- 3- 5

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.791 V, 10.6% >> pos = 0.434V, neg = -0.357V # Test item 7-16- 3- 6

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.5% >> pos = 0.032V, neg = 0.025V # Test item 7-16- 3- 7

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.250 V, 1.0% >> pos = 0.157V, neg = -0.092V # Test item 7-16- 3- 8

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = 0.029V, neg = 0.028V # Test item 7-16- 3- 9

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.085 V, 75.1% >> pos = 0.079V, neg = -0.006V # Test item 7-16- 3-10

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.6% >> pos = 0.015V, neg = 0.019V # Test item 7-16- 3-11

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.227 V, 8.6% >> pos = 1.636V, neg = -1.591V # Test item 7-16- 3-12

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = 0.011V, neg = 0.011V # Test item 7-16- 3-13

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.591 V, 5.3% >> pos = 0.794V, neg = -0.797V # Test item 7-16- 3-14

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = -0.013V, neg = -0.013V # Test item 7-16- 3-15

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.800 V, 0.6% >> pos = 0.386V, neg = -0.413V # Test item 7-16- 3-16

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.9% >> pos = -0.020V, neg = -0.019V # Test item 7-16- 3-17

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 6.8% >> pos = 0.105V, neg = -0.144V # Test item 7-16- 3-18

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = -0.022V, neg = -0.022V # Test item 7-16- 3-19

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.097 V, 15.3% >> pos = 0.026V, neg = -0.071V # Test item 7-16- 3-20

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.5% >> pos = 0.075V, neg = 0.074V # Test item 7-16- 3-21

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.165 V, 11.0% >> pos = 1.658V, neg = -1.506V # Test item 7-16- 3-22

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.7% >> pos = 0.047V, neg = 0.050V # Test item 7-16- 3-23

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.581 V, 11.8% >> pos = 0.839V, neg = -0.742V # Test item 7-16- 3-24

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.8% >> pos = 0.037V, neg = 0.039V # Test item 7-16- 3-25

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.805 V, 6.4% >> pos = 0.449V, neg = -0.356V # Test item 7-16- 3-26

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.032V, neg = 0.032V # Test item 7-16- 3-27

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 2.4% >> pos = 0.157V, neg = -0.092V # Test item 7-16- 3-28

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.1% >> pos = 0.030V, neg = 0.028V # Test item 7-16- 3-29

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 0.8% >> pos = 0.079V, neg = -0.022V # Test item 7-16- 3-30

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.0% >> pos = 0.021V, neg = 0.024V # Test item 7-16- 3-31

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.224 V, 7.5% >> pos = 1.633V, neg = -1.591V # Test item 7-16- 3-32

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = -0.002V, neg = -0.001V # Test item 7-16- 3-33

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.593 V, 4.6% >> pos = 0.791V, neg = -0.801V # Test item 7-16- 3-34

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.0% >> pos = -0.015V, neg = -0.012V # Test item 7-16- 3-35

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.795 V, 6.3% >> pos = 0.383V, neg = -0.412V # Test item 7-16- 3-36

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.0% >> pos = -0.019V, neg = -0.023V # Test item 7-16- 3-37

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.249 V, 4.6% >> pos = 0.105V, neg = -0.144V # Test item 7-16- 3-38

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.0% >> pos = -0.021V, neg = -0.024V # Test item 7-16- 3-39

T AMCA: MESE 2317: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 6.1% >> pos = 0.027V, neg = -0.072V # Test item 7-16- 3-40

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.7% >> pos = 0.073V, neg = 0.070V # Test item 8-16- 3- 1

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.258 V, 18.2% >> pos = 1.701V, neg = -1.558V # Test item 8-16- 3- 2

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.6% >> pos = 0.051V, neg = 0.049V # Test item 8-16- 3- 3

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.608 V, 4.9% >> pos = 0.853V, neg = -0.755V # Test item 8-16- 3- 4

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.3% >> pos = 0.042V, neg = 0.042V # Test item 8-16- 3- 5

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.803 V, 3.5% >> pos = 0.443V, neg = -0.360V # Test item 8-16- 3- 6

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = 0.035V, neg = 0.034V # Test item 8-16- 3- 7

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.255 V, 18.9% >> pos = 0.164V, neg = -0.090V # Test item 8-16- 3- 8

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = 0.034V, neg = 0.033V # Test item 8-16- 3- 9

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.099 V, 6.2% >> pos = 0.082V, neg = -0.017V # Test item 8-16- 3-10

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = 0.010V, neg = 0.009V # Test item 8-16- 3-11

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.165 V, 10.9% >> pos = 1.595V, neg = -1.571V # Test item 8-16- 3-12

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = 0.005V, neg = 0.005V # Test item 8-16- 3-13

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.586 V, 9.1% >> pos = 0.783V, neg = -0.802V # Test item 8-16- 3-14

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = -0.018V, neg = -0.019V # Test item 8-16- 3-15

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.794 V, 8.1% >> pos = 0.377V, neg = -0.416V # Test item 8-16- 3-16

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.6% >> pos = -0.026V, neg = -0.024V # Test item 8-16- 3-17

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.248 V, 9.8% >> pos = 0.100V, neg = -0.148V # Test item 8-16- 3-18

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = -0.026V, neg = -0.028V # Test item 8-16- 3-19

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.101 V, 7.0% >> pos = 0.024V, neg = -0.078V # Test item 8-16- 3-20

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = 0.072V, neg = 0.073V # Test item 8-16- 3-21

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.258 V, 18.1% >> pos = 1.700V, neg = -1.558V # Test item 8-16- 3-22

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.052V, neg = 0.052V # Test item 8-16- 3-23

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.608 V, 5.3% >> pos = 0.854V, neg = -0.754V # Test item 8-16- 3-24

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.006 V, 6.0% >> pos = 0.039V, neg = 0.045V # Test item 8-16- 3-25

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.805 V, 6.8% >> pos = 0.444V, neg = -0.361V # Test item 8-16- 3-26

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = 0.034V, neg = 0.034V # Test item 8-16- 3-27

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.251 V, 3.4% >> pos = 0.162V, neg = -0.089V # Test item 8-16- 3-28

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.0% >> pos = 0.033V, neg = 0.034V # Test item 8-16- 3-29

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.101 V, 6.8% >> pos = 0.083V, neg = -0.018V # Test item 8-16- 3-30

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 7.9% >> pos = 0.013V, neg = 0.005V # Test item 8-16- 3-31

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.157 V, 13.4% >> pos = 1.587V, neg = -1.570V # Test item 8-16- 3-32

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.8% >> pos = -0.008V, neg = -0.011V # Test item 8-16- 3-33

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.585 V, 9.4% >> pos = 0.784V, neg = -0.801V # Test item 8-16- 3-34

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.8% >> pos = -0.019V, neg = -0.016V # Test item 8-16- 3-35

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.790 V, 12.2% >> pos = 0.376V, neg = -0.414V # Test item 8-16- 3-36

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.2% >> pos = -0.021V, neg = -0.027V # Test item 8-16- 3-37

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.252 V, 6.1% >> pos = 0.100V, neg = -0.151V # Test item 8-16- 3-38

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = -0.027V, neg = -0.027V # Test item 8-16- 3-39

T AMCA: MESE 2318: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.100 V, 1.8% >> pos = 0.023V, neg = -0.077V # Test item 8-16- 3-40

T AMCA: MESE 2311: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.786 V, 14.3% >> POS = 0.834V, NEG = 0.048V # Test item 1-16- 4- 1

T AMCA: MESE 2311: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.766 V, 34.0% >> POS = 0.814V, NEG = 0.048V # Test item 1-16- 4- 2

T AMCA: MESE 2311: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.746 V, 53.7% >> POS = 0.785V, NEG = 0.039V # Test item 1-16- 4- 3

T AMCA: MESE 2311: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.799 V, 0.7% >> POS = 0.852V, NEG = 0.053V # Test item 1-16- 4- 4

T AMCA: MESE 2312: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.784 V, 16.0% >> POS = 0.848V, NEG = 0.064V # Test item 2-16- 4- 1

T AMCA: MESE 2312: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.767 V, 32.6% >> POS = 0.828V, NEG = 0.061V # Test item 2-16- 4- 2

T AMCA: MESE 2312: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.748 V, 52.0% >> POS = 0.799V, NEG = 0.051V # Test item 2-16- 4- 3

T AMCA: MESE 2312: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.800 V, 0.3% >> POS = 0.867V, NEG = 0.067V # Test item 2-16- 4- 4

T AMCA: MESE 2313: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.776 V, 23.7% >> POS = 0.813V, NEG = 0.037V # Test item 3-16- 4- 1

T AMCA: MESE 2313: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.755 V, 45.0% >> POS = 0.790V, NEG = 0.035V # Test item 3-16- 4- 2

T AMCA: MESE 2313: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.738 V, 61.9% >> POS = 0.763V, NEG = 0.025V # Test item 3-16- 4- 3

T AMCA: MESE 2313: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.787 V, 12.9% >> POS = 0.830V, NEG = 0.043V # Test item 3-16- 4- 4

T AMCA: MESE 2314: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.780 V, 20.4% >> POS = 0.819V, NEG = 0.040V # Test item 4-16- 4- 1

T AMCA: MESE 2314: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.765 V, 35.4% >> POS = 0.802V, NEG = 0.037V # Test item 4-16- 4- 2

T AMCA: MESE 2314: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.745 V, 54.9% >> POS = 0.774V, NEG = 0.028V # Test item 4-16- 4- 3

T AMCA: MESE 2314: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.798 V, 2.3% >> POS = 0.841V, NEG = 0.044V # Test item 4-16- 4- 4

T AMCA: MESE 2315: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.788 V, 12.2% >> POS = 0.840V, NEG = 0.052V # Test item 5-16- 4- 1

T AMCA: MESE 2315: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.767 V, 32.6% >> POS = 0.821V, NEG = 0.054V # Test item 5-16- 4- 2

T AMCA: MESE 2315: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.750 V, 50.3% >> POS = 0.793V, NEG = 0.043V # Test item 5-16- 4- 3

T AMCA: MESE 2315: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.802 V, 2.4% >> POS = 0.860V, NEG = 0.057V # Test item 5-16- 4- 4

T AMCA: MESE 2316: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.776 V, 23.9% >> POS = 0.826V, NEG = 0.050V # Test item 6-16- 4- 1

T AMCA: MESE 2316: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.758 V, 41.9% >> POS = 0.805V, NEG = 0.047V # Test item 6-16- 4- 2

T AMCA: MESE 2316: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.738 V, 61.7% >> POS = 0.777V, NEG = 0.039V # Test item 6-16- 4- 3

T AMCA: MESE 2316: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.789 V, 10.7% >> POS = 0.842V, NEG = 0.053V # Test item 6-16- 4- 4

T AMCA: MESE 2317: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.780 V, 20.4% >> POS = 0.824V, NEG = 0.045V # Test item 7-16- 4- 1

T AMCA: MESE 2317: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.759 V, 41.1% >> POS = 0.804V, NEG = 0.045V # Test item 7-16- 4- 2

T AMCA: MESE 2317: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.733 V, 67.2% >> POS = 0.768V, NEG = 0.035V # Test item 7-16- 4- 3

T AMCA: MESE 2317: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.784 V, 15.5% >> POS = 0.835V, NEG = 0.051V # Test item 7-16- 4- 4

T AMCA: MESE 2318: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.791 V, 9.1% >> POS = 0.835V, NEG = 0.044V # Test item 8-16- 4- 1

T AMCA: MESE 2318: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.740 V, 60.5% >> POS = 0.783V, NEG = 0.044V # Test item 8-16- 4- 2

T AMCA: MESE 2318: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.748 V, 52.5% >> POS = 0.784V, NEG = 0.036V # Test item 8-16- 4- 3

T AMCA: MESE 2318: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.802 V, 2.3% >> POS = 0.853V, NEG = 0.051V # Test item 8-16- 4- 4

T AMCA: MESE 2311: Signal\_Input/Output\_relay\_test\_Resistance\_A+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5019.539 Ohm, 0.9% >> vOffset = -0.112V, vMeas = 2.398V, iForce = 0.500mA # Test item 1-20- 1- 1

T AMCA: MESE 2311: Signal\_Input/Output\_relay\_test\_Resistance\_A-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5021.553 Ohm, 0.5% >> vOffset = -0.112V, vMeas = 2.399V, iForce = 0.500mA # Test item 1-20- 1- 2

T AMCA: MESE 2311: Signal\_Input/Output\_relay\_test\_Resistance\_AA+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1001.541 Ohm, 1.5% >> vOffset = -0.019V, vMeas = 0.482V, iForce = 0.500mA # Test item 1-20- 1- 3

T AMCA: MESE 2311: Signal\_Input/Output\_relay\_test\_Resistance\_AA-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1005.821 Ohm, 5.8% >> vOffset = -0.019V, vMeas = 0.484V, iForce = 0.500mA # Test item 1-20- 1- 4

T AMCA: MESE 2312: Signal\_Input/Output\_relay\_test\_Resistance\_B+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5011.986 Ohm, 2.4% >> vOffset = -0.125V, vMeas = 2.381V, iForce = 0.500mA # Test item 2-20- 1- 1

T AMCA: MESE 2312: Signal\_Input/Output\_relay\_test\_Resistance\_B-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5014.252 Ohm, 1.9% >> vOffset = -0.126V, vMeas = 2.381V, iForce = 0.500mA # Test item 2-20- 1- 2

T AMCA: MESE 2312: Signal\_Input/Output\_relay\_test\_Resistance\_BB+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1001.289 Ohm, 1.3% >> vOffset = -0.021V, vMeas = 0.479V, iForce = 0.500mA # Test item 2-20- 1- 3

T AMCA: MESE 2312: Signal\_Input/Output\_relay\_test\_Resistance\_BB-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1003.807 Ohm, 3.8% >> vOffset = -0.022V, vMeas = 0.480V, iForce = 0.500mA # Test item 2-20- 1- 4

T AMCA: MESE 2313: Signal\_Input/Output\_relay\_test\_Resistance\_C+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5026.588 Ohm, 0.5% >> vOffset = -0.113V, vMeas = 2.400V, iForce = 0.500mA # Test item 3-20- 1- 1

T AMCA: MESE 2313: Signal\_Input/Output\_relay\_test\_Resistance\_C-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5022.812 Ohm, 0.2% >> vOffset = -0.112V, vMeas = 2.399V, iForce = 0.500mA # Test item 3-20- 1- 2

T AMCA: MESE 2313: Signal\_Input/Output\_relay\_test\_Resistance\_CC+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1002.800 Ohm, 2.8% >> vOffset = -0.021V, vMeas = 0.481V, iForce = 0.500mA # Test item 3-20- 1- 3

T AMCA: MESE 2313: Signal\_Input/Output\_relay\_test\_Resistance\_CC-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1005.569 Ohm, 5.6% >> vOffset = -0.021V, vMeas = 0.482V, iForce = 0.500mA # Test item 3-20- 1- 4

T AMCA: MESE 2314: Signal\_Input/Output\_relay\_test\_Resistance\_D+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5024.826 Ohm, 0.2% >> vOffset = -0.106V, vMeas = 2.406V, iForce = 0.500mA # Test item 4-20- 1- 1

T AMCA: MESE 2314: Signal\_Input/Output\_relay\_test\_Resistance\_D-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5024.071 Ohm, 0.0% >> vOffset = -0.106V, vMeas = 2.406V, iForce = 0.500mA # Test item 4-20- 1- 2

T AMCA: MESE 2314: Signal\_Input/Output\_relay\_test\_Resistance\_DD+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1007.332 Ohm, 7.3% >> vOffset = -0.017V, vMeas = 0.487V, iForce = 0.500mA # Test item 4-20- 1- 3

T AMCA: MESE 2314: Signal\_Input/Output\_relay\_test\_Resistance\_DD-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1003.304 Ohm, 3.3% >> vOffset = -0.017V, vMeas = 0.485V, iForce = 0.500mA # Test item 4-20- 1- 4

T AMCA: MESE 2315: Signal\_Input/Output\_relay\_test\_Resistance\_E+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5019.287 Ohm, 0.9% >> vOffset = -0.107V, vMeas = 2.403V, iForce = 0.500mA # Test item 5-20- 1- 1

T AMCA: MESE 2315: Signal\_Input/Output\_relay\_test\_Resistance\_E-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5021.049 Ohm, 0.6% >> vOffset = -0.107V, vMeas = 2.403V, iForce = 0.500mA # Test item 5-20- 1- 2

T AMCA: MESE 2315: Signal\_Input/Output\_relay\_test\_Resistance\_EE+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5019.791 Ohm, 0.8% >> vOffset = -0.107V, vMeas = 2.403V, iForce = 0.500mA # Test item 5-20- 1- 3

T AMCA: MESE 2315: Signal\_Input/Output\_relay\_test\_Resistance\_EE-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5018.784 Ohm, 1.0% >> vOffset = -0.107V, vMeas = 2.402V, iForce = 0.500mA # Test item 5-20- 1- 4

T AMCA: MESE 2316: Signal\_Input/Output\_relay\_test\_Resistance\_F+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5019.791 Ohm, 0.8% >> vOffset = -0.105V, vMeas = 2.404V, iForce = 0.500mA # Test item 6-20- 1- 1

T AMCA: MESE 2316: Signal\_Input/Output\_relay\_test\_Resistance\_F-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5020.546 Ohm, 0.7% >> vOffset = -0.106V, vMeas = 2.405V, iForce = 0.500mA # Test item 6-20- 1- 2

T AMCA: MESE 2316: Signal\_Input/Output\_relay\_test\_Resistance\_FF+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5021.301 Ohm, 0.5% >> vOffset = -0.106V, vMeas = 2.405V, iForce = 0.500mA # Test item 6-20- 1- 3

T AMCA: MESE 2316: Signal\_Input/Output\_relay\_test\_Resistance\_FF-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5020.294 Ohm, 0.7% >> vOffset = -0.105V, vMeas = 2.405V, iForce = 0.500mA # Test item 6-20- 1- 4

T AMCA: MESE 2317: Signal\_Input/Output\_relay\_test\_Resistance\_G+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5013.497 Ohm, 2.1% >> vOffset = -0.111V, vMeas = 2.396V, iForce = 0.500mA # Test item 7-20- 1- 1

T AMCA: MESE 2317: Signal\_Input/Output\_relay\_test\_Resistance\_G-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5015.762 Ohm, 1.6% >> vOffset = -0.111V, vMeas = 2.397V, iForce = 0.500mA # Test item 7-20- 1- 2

T AMCA: MESE 2317: Signal\_Input/Output\_relay\_test\_Resistance\_GG+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5017.021 Ohm, 1.4% >> vOffset = -0.111V, vMeas = 2.397V, iForce = 0.500mA # Test item 7-20- 1- 3

T AMCA: MESE 2317: Signal\_Input/Output\_relay\_test\_Resistance\_GG-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5014.000 Ohm, 2.0% >> vOffset = -0.111V, vMeas = 2.396V, iForce = 0.500mA # Test item 7-20- 1- 4

T AMCA: MESE 2318: Signal\_Input/Output\_relay\_test\_Resistance\_H+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5012.741 Ohm, 2.2% >> vOffset = -0.126V, vMeas = 2.380V, iForce = 0.500mA # Test item 8-20- 1- 1

T AMCA: MESE 2318: Signal\_Input/Output\_relay\_test\_Resistance\_H-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5012.993 Ohm, 2.2% >> vOffset = -0.126V, vMeas = 2.381V, iForce = 0.500mA # Test item 8-20- 1- 2

T AMCA: MESE 2318: Signal\_Input/Output\_relay\_test\_Resistance\_HH+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5013.748 Ohm, 2.0% >> vOffset = -0.126V, vMeas = 2.381V, iForce = 0.500mA # Test item 8-20- 1- 3

T AMCA: MESE 2318: Signal\_Input/Output\_relay\_test\_Resistance\_HH-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5013.748 Ohm, 2.0% >> vOffset = -0.126V, vMeas = 2.381V, iForce = 0.500mA # Test item 8-20- 1- 4

T AMCA: MCE 2310: Common\_trigger\_input\_relay\_test\_Common\_Trigger\_1: R\_exp = 470.000 Ohm, [369.000 ... 571.000 Ohm] : R\_meas = 471.230 Ohm, 1.2% >> vMeas = 1.407V, vOffset = -0.007V, usedUnit = 1 # Test item 0-21- 1- 1

T AMCA: MCE 2310: Common\_trigger\_input\_relay\_test\_Common\_Trigger\_2: R\_exp = 470.000 Ohm, [369.000 ... 571.000 Ohm] : R\_meas = 468.712 Ohm, 1.3% >> vMeas = 1.399V, vOffset = -0.007V, usedUnit = 1 # Test item 0-21- 1- 2

T AMCA: MCE 2320: Unit\_FPGA\_configuration\_test\_Unit\_1: exp = passed: meas = passed # Test item 1- 1- 1- 1

T AMCA: MCE 2320: Unit\_FPGA\_configuration\_test\_Unit\_2: exp = passed: meas = passed # Test item 2- 1- 1- 1

T AMCA: MCE 2320: Unit\_FPGA\_configuration\_test\_Unit\_3: exp = passed: meas = passed # Test item 3- 1- 1- 1

T AMCA: MCE 2320: Unit\_FPGA\_configuration\_test\_Unit\_4: exp = passed: meas = passed # Test item 4- 1- 1- 1

T AMCA: MCE 2320: Unit\_FPGA\_configuration\_test\_Unit\_5: exp = passed: meas = passed # Test item 5- 1- 1- 1

T AMCA: MCE 2320: Unit\_FPGA\_configuration\_test\_Unit\_6: exp = passed: meas = passed # Test item 6- 1- 1- 1

T AMCA: MCE 2320: Unit\_FPGA\_configuration\_test\_Unit\_7: exp = passed: meas = passed # Test item 7- 1- 1- 1

T AMCA: MCE 2320: Unit\_FPGA\_configuration\_test\_Unit\_8: exp = passed: meas = passed # Test item 8- 1- 1- 1

T AMCA: MCE 2320: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Data\_Bus\_Test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 0- 1- 2- 1

T AMCA: MCE 2320: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Data\_Bus\_Test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 1- 2- 2

T AMCA: MCE 2320: Analog\_interface\_module\_data\_bus\_and\_reset\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 1- 2- 3

T AMCA: MESE 2321: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 1- 8- 1

T AMCA: MESE 2322: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 1- 8- 1

T AMCA: MESE 2323: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 1- 8- 1

T AMCA: MESE 2324: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 1- 8- 1

T AMCA: MESE 2325: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 1- 8- 1

T AMCA: MESE 2326: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 1- 8- 1

T AMCA: MESE 2327: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 1- 8- 1

T AMCA: MESE 2328: UNIT\_NIOS\_communication\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 1- 8- 1

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_DGND\_B\_(U10144): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.005 V, 0.9% # Test item 0- 1- 3- 1

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_(U10145): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.005 V, 1.1% # Test item 1- 1- 3- 2

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_IF\_(U10145): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.023 V, 4.7% # Test item 2- 1- 3- 3

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_AGND\_(U811): V\_exp = 0.000 V, [-0.500 ... 0.500 V] : V\_meas = -0.003 V, 0.6% # Test item 3- 1- 3- 4

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.970 V, 10.2% # Test item 0- 1- 3- 5

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 0.994 V, 15.8% # Test item 1- 1- 3- 6

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_B: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.982 V, 2.0% # Test item 2- 1- 3- 7

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_B: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.022 V, 11.9% # Test item 3- 1- 3- 8

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5VD\_B: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.976 V, 16.7% # Test item 4- 1- 3- 9

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_B: V\_exp = 1.020 V, [0.918 ... 1.122 V] : V\_meas = 1.034 V, 13.7% # Test item 5- 1- 3-10

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_TEMP\_BUSIF: V\_exp = 0.750 V, [0.500 ... 1.000 V] : V\_meas = 0.625 V, 50.0% # Test item 6- 1- 3-11

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5.5VA: V\_exp = 1.050 V, [0.945 ... 1.155 V] : V\_meas = 1.049 V, 1.4% # Test item 7- 1- 3-12

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5.5VA: V\_exp = -1.140 V, [-1.254 ... -1.026 V] : V\_meas = -1.135 V, 4.8% # Test item 8- 1- 3-13

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+10VA: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.997 V, 3.5% # Test item 9- 1- 3-14

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.2VA\_IF: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.981 V, 18.5% # Test item 10- 1- 3-15

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U13: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.964 V, 26.3% # Test item 11- 1- 3-16

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U24: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.964 V, 26.3% # Test item 12- 1- 3-17

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U57: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.971 V, 19.2% # Test item 13- 1- 3-18

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.25VD\_U68: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.980 V, 10.1% # Test item 14- 1- 3-19

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U13: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.995 V, 5.1% # Test item 15- 1- 3-20

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U24: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.997 V, 7.1% # Test item 16- 1- 3-21

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U57: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.991 V, 1.0% # Test item 17- 1- 3-22

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+1.5VD\_U68: V\_exp = 0.990 V, [0.891 ... 1.089 V] : V\_meas = 0.998 V, 8.1% # Test item 18- 1- 3-23

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_F\_TG: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.004 V, 34.6% # Test item 19- 1- 3-24

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.5VD\_F\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.985 V, 15.0% # Test item 20- 1- 3-25

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VD\_F: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.000 V, 38.5% # Test item 21- 1- 3-26

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+4.5VD\_F: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.986 V, 14.0% # Test item 22- 1- 3-27

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5VD\_F: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.953 V, 7.3% # Test item 23- 1- 3-28

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+5VA\_F: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.962 V, 1.6% # Test item 24- 1- 3-29

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_-5VA\_F: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.961 V, 1.6% # Test item 25- 1- 3-30

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+2.5VA\_F: V\_exp = 1.040 V, [0.936 ... 1.144 V] : V\_meas = 1.011 V, 28.4% # Test item 26- 1- 3-31

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.972 V, 8.2% # Test item 27- 1- 3-32

T AMCA: MCE 2320: Analog\_interface\_module\_regulated\_power\_supply\_voltage\_test\_+3.3VD\_IF\_IF2: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.967 V, 33.0% # Test item 28- 1- 3-33

T AMCA: MCE 2320: Unit\_bus\_data\_line\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 0- 1- 4- 1

T AMCA: MCE 2320: Unit\_bus\_data\_line\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 1- 4- 2

T AMCA: MCE 2320: Unit\_bus\_address/control\_line\_test\_WE\_line\_and\_bit\_0\_of\_addr.\_bus: Reg\_exp = 0x00030002: Reg\_meas = 0x00030002 # Test item 0- 1- 5- 1

T AMCA: MCE 2320: Unit\_bus\_address/control\_line\_test\_OE\_line\_\_unit\_select\_line\_for\_unit\_1--4\_and\_bit\_1\_of\_addr.\_bus: Reg\_exp = 0x0002F004: Reg\_meas = 0x0002F004 # Test item 1- 1- 5- 2

T AMCA: MCE 2320: Unit\_bus\_address/control\_line\_test\_Unit\_select\_line\_for\_unit\_5--8\_and\_bit\_2\_of\_addr.\_bus: Reg\_exp = 0x00020F08: Reg\_meas = 0x00020F08 # Test item 2- 1- 5- 3

T AMCA: MCE 2320: Unit\_bus\_address/control\_line\_test\_Reset: Reg\_exp = 0x00060000: Reg\_meas = 0x00060000 # Test item 3- 1- 5- 4

T AMCA: MCE 2320: Analog\_interface\_module\_EEPROM\_access\_test\_Analog\_Interface\_Module: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 0- 1- 6- 1

T AMCA: MCE 2320: Front\_module\_serial\_bus\_test\_1k\_Ohm\_Rref\_connect\_relay\_on\_test: Reg\_exp = 0x00000003: Reg\_meas = 0x00000003 # Test item 0- 1- 7- 1

T AMCA: MCE 2320: Temperature\_sensor\_test\_START: V\_exp = 0.912 V, [0.450 ... 1.375 V] : V\_meas = 0.807 V, 22.8% >> degree = 34.280degree # Test item 0- 2- 3- 1

T AMCA: MCE 2320: MCLK\_test\_START: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.014 V, 14.0% >> D\_MCLK\_DC = 0.915V, D\_MCLK\_DC\* = 0.929V # Test item 0- 2- 4- 1

T AMCA: MCE 2320: MCLK\_test\_STOP: V\_exp = 0.300 V, [0.200 ... 19.980 V] : V\_meas = 0.332 V, 0.2% >> D\_MCLK\_DC = 0.759V, D\_MCLK\_DC\* = 1.091V # Test item 0- 2- 4- 2

T AMCA: MCE 2320: DC\_rail\_path\_test: R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1001.000 Ohm, 1.0% # Test item 0- 2- 8- 1

T AMCA: MCE 2320: DC\_rail\_path\_test\_GND\_connection\_switch\_On: R\_exp = 5.000 Ohm, [0.000 ... 10.000 Ohm] : R\_meas = 0.833 Ohm, 83.3% # Test item 0- 2- 8- 2

T AMCA: MESE 2321: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 3- 1- 1

T AMCA: MESE 2322: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 3- 1- 1

T AMCA: MESE 2323: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 3- 1- 1

T AMCA: MESE 2324: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 3- 1- 1

T AMCA: MESE 2325: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 3- 1- 1

T AMCA: MESE 2326: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 3- 1- 1

T AMCA: MESE 2327: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 3- 1- 1

T AMCA: MESE 2328: Unit\_EEPROM\_access\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 3- 1- 1

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.989 V, 8.9% # Test item 1- 3- 2- 1

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.986 V, 5.9% # Test item 1- 3- 2- 2

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.024 V, 13.6% # Test item 1- 3- 2- 3

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.009 V, 1.0% # Test item 1- 3- 2- 4

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.977 V, 3.1% # Test item 1- 3- 2- 5

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.977 V, 23.0% # Test item 1- 3- 2- 6

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.948 V, 12.8% # Test item 1- 3- 2- 7

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_1\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.976 V, 16.9% # Test item 1- 3- 2- 8

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.004 V, 24.2% # Test item 2- 3- 2- 1

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.012 V, 32.4% # Test item 2- 3- 2- 2

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.054 V, 43.3% # Test item 2- 3- 2- 3

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.020 V, 9.9% # Test item 2- 3- 2- 4

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.976 V, 4.1% # Test item 2- 3- 2- 5

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.983 V, 17.0% # Test item 2- 3- 2- 6

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.951 V, 9.6% # Test item 2- 3- 2- 7

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_2\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.970 V, 10.7% # Test item 2- 3- 2- 8

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.011 V, 31.4% # Test item 3- 3- 2- 1

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.014 V, 34.4% # Test item 3- 3- 2- 2

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.053 V, 42.3% # Test item 3- 3- 2- 3

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.007 V, 3.0% # Test item 3- 3- 2- 4

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.972 V, 8.2% # Test item 3- 3- 2- 5

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.974 V, 26.0% # Test item 3- 3- 2- 6

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.956 V, 4.4% # Test item 3- 3- 2- 7

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_3\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.975 V, 15.9% # Test item 3- 3- 2- 8

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.981 V, 0.8% # Test item 4- 3- 2- 1

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.994 V, 14.0% # Test item 4- 3- 2- 2

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.028 V, 17.6% # Test item 4- 3- 2- 3

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.004 V, 5.9% # Test item 4- 3- 2- 4

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.982 V, 2.0% # Test item 4- 3- 2- 5

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.975 V, 25.0% # Test item 4- 3- 2- 6

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.963 V, 2.9% # Test item 4- 3- 2- 7

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_4\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.969 V, 9.6% # Test item 4- 3- 2- 8

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.012 V, 32.4% # Test item 5- 3- 2- 1

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.021 V, 41.6% # Test item 5- 3- 2- 2

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.051 V, 40.3% # Test item 5- 3- 2- 3

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.015 V, 5.0% # Test item 5- 3- 2- 4

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.972 V, 8.2% # Test item 5- 3- 2- 5

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.981 V, 19.0% # Test item 5- 3- 2- 6

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.957 V, 3.4% # Test item 5- 3- 2- 7

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_5\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.958 V, 1.8% # Test item 5- 3- 2- 8

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.005 V, 25.3% # Test item 6- 3- 2- 1

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.021 V, 41.6% # Test item 6- 3- 2- 2

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.053 V, 42.3% # Test item 6- 3- 2- 3

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.009 V, 1.0% # Test item 6- 3- 2- 4

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.976 V, 4.1% # Test item 6- 3- 2- 5

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.982 V, 18.0% # Test item 6- 3- 2- 6

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.960 V, 0.3% # Test item 6- 3- 2- 7

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_6\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.995 V, 36.7% # Test item 6- 3- 2- 8

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.019 V, 39.5% # Test item 7- 3- 2- 1

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.014 V, 34.4% # Test item 7- 3- 2- 2

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.050 V, 39.4% # Test item 7- 3- 2- 3

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.005 V, 5.0% # Test item 7- 3- 2- 4

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.968 V, 12.2% # Test item 7- 3- 2- 5

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.974 V, 26.0% # Test item 7- 3- 2- 6

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.958 V, 2.3% # Test item 7- 3- 2- 7

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_7\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.973 V, 13.8% # Test item 7- 3- 2- 8

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VA\_IF: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.016 V, 36.5% # Test item 8- 3- 2- 1

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VA\_CLK: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 1.012 V, 32.4% # Test item 8- 3- 2- 2

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+2.5VA\_IF: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.059 V, 48.3% # Test item 8- 3- 2- 3

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+2.5VD\_TG: V\_exp = 1.010 V, [0.909 ... 1.111 V] : V\_meas = 1.007 V, 3.0% # Test item 8- 3- 2- 4

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+3.3VD\_TG: V\_exp = 0.980 V, [0.882 ... 1.078 V] : V\_meas = 0.979 V, 1.0% # Test item 8- 3- 2- 5

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+4.5VD\_TG: V\_exp = 1.000 V, [0.900 ... 1.100 V] : V\_meas = 0.975 V, 25.0% # Test item 8- 3- 2- 6

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_+5VA: V\_exp = 0.960 V, [0.864 ... 1.056 V] : V\_meas = 0.962 V, 1.8% # Test item 8- 3- 2- 7

T AMCA: MCE 2320: Unit\_Regulated\_power\_supply\_voltage\_test\_Unit\_8\_-5VA: V\_exp = -0.960 V, [-1.056 ... -0.864 V] : V\_meas = -0.960 V, 0.3% # Test item 8- 3- 2- 8

T AMCA: MESE 2321: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 3- 3- 1

T AMCA: MESE 2322: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 3- 3- 1

T AMCA: MESE 2323: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 3- 3- 1

T AMCA: MESE 2324: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 3- 3- 1

T AMCA: MESE 2325: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 3- 3- 1

T AMCA: MESE 2326: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 3- 3- 1

T AMCA: MESE 2327: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 3- 3- 1

T AMCA: MESE 2328: FPGA\_configuration\_ROM\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 3- 3- 1

T AMCA: MESE 2321: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 1- 3- 4- 1

T AMCA: MESE 2321: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 1- 3- 4- 2

T AMCA: MESE 2322: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 2- 3- 4- 1

T AMCA: MESE 2322: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 2- 3- 4- 2

T AMCA: MESE 2323: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 3- 3- 4- 1

T AMCA: MESE 2323: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 3- 3- 4- 2

T AMCA: MESE 2324: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 4- 3- 4- 1

T AMCA: MESE 2324: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 4- 3- 4- 2

T AMCA: MESE 2325: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 5- 3- 4- 1

T AMCA: MESE 2325: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 5- 3- 4- 2

T AMCA: MESE 2326: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 6- 3- 4- 1

T AMCA: MESE 2326: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 6- 3- 4- 2

T AMCA: MESE 2327: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 7- 3- 4- 1

T AMCA: MESE 2327: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 7- 3- 4- 2

T AMCA: MESE 2328: SEQ\_FPGA\_access\_test\_01..01\_test: Reg\_exp = 0x55555555: Reg\_meas = 0x55555555 # Test item 8- 3- 4- 1

T AMCA: MESE 2328: SEQ\_FPGA\_access\_test\_10..10\_test: Reg\_exp = 0xAAAAAAAA: Reg\_meas = 0xAAAAAAAA # Test item 8- 3- 4- 2

T AMCA: MESE 2321: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 3- 5- 1

T AMCA: MESE 2322: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 3- 5- 1

T AMCA: MESE 2323: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 3- 5- 1

T AMCA: MESE 2324: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 3- 5- 1

T AMCA: MESE 2325: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 3- 5- 1

T AMCA: MESE 2326: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 3- 5- 1

T AMCA: MESE 2327: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 3- 5- 1

T AMCA: MESE 2328: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 3- 5- 1

T AMCA: MESE 2321: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 3- 5- 2

T AMCA: MESE 2322: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 3- 5- 2

T AMCA: MESE 2323: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 3- 5- 2

T AMCA: MESE 2324: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 3- 5- 2

T AMCA: MESE 2325: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 3- 5- 2

T AMCA: MESE 2326: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 3- 5- 2

T AMCA: MESE 2327: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 3- 5- 2

T AMCA: MESE 2328: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 3- 5- 2

T AMCA: MESE 2321: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 3- 5- 3

T AMCA: MESE 2322: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 3- 5- 3

T AMCA: MESE 2323: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 3- 5- 3

T AMCA: MESE 2324: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 3- 5- 3

T AMCA: MESE 2325: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 3- 5- 3

T AMCA: MESE 2326: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 3- 5- 3

T AMCA: MESE 2327: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 3- 5- 3

T AMCA: MESE 2328: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 3- 5- 3

T AMCA: MESE 2321: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 3- 5- 4

T AMCA: MESE 2322: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 3- 5- 4

T AMCA: MESE 2323: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 3- 5- 4

T AMCA: MESE 2324: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 3- 5- 4

T AMCA: MESE 2325: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 3- 5- 4

T AMCA: MESE 2326: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 3- 5- 4

T AMCA: MESE 2327: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 3- 5- 4

T AMCA: MESE 2328: Nios\_loopback\_test\_Diag\_loopback\_register\_write\_+\_read\_with\_BFIFO\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 3- 5- 4

T AMCA: MESE 2321: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 1- 3- 6- 1

T AMCA: MESE 2322: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 2- 3- 6- 1

T AMCA: MESE 2323: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 3- 3- 6- 1

T AMCA: MESE 2324: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 4- 3- 6- 1

T AMCA: MESE 2325: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 5- 3- 6- 1

T AMCA: MESE 2326: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 6- 3- 6- 1

T AMCA: MESE 2327: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 7- 3- 6- 1

T AMCA: MESE 2328: IFCHIP\_access\_test\_Write\_and\_read\_test: Reg\_exp = 0x00000045: Reg\_meas = 0x00000045 # Test item 8- 3- 6- 1

T AMCA: MESE 2321: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 1- 3- 6- 2

T AMCA: MESE 2322: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 2- 3- 6- 2

T AMCA: MESE 2323: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 3- 3- 6- 2

T AMCA: MESE 2324: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 4- 3- 6- 2

T AMCA: MESE 2325: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 5- 3- 6- 2

T AMCA: MESE 2326: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 6- 3- 6- 2

T AMCA: MESE 2327: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 7- 3- 6- 2

T AMCA: MESE 2328: IFCHIP\_access\_test\_Reset\_test: Reg\_exp = 0x00000041: Reg\_meas = 0x00000041 # Test item 8- 3- 6- 2

T AMCA: MESE 2321: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 3- 7- 1

T AMCA: MESE 2322: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 3- 7- 1

T AMCA: MESE 2323: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 3- 7- 1

T AMCA: MESE 2324: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 3- 7- 1

T AMCA: MESE 2325: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 3- 7- 1

T AMCA: MESE 2326: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 3- 7- 1

T AMCA: MESE 2327: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 3- 7- 1

T AMCA: MESE 2328: Precision\_ADC\_access\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 3- 7- 1

T AMCA: MESE 2321: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 1-22- 1- 1

T AMCA: MESE 2321: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 1-22- 1- 2

T AMCA: MESE 2322: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 2-22- 1- 1

T AMCA: MESE 2322: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 2-22- 1- 2

T AMCA: MESE 2323: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 3-22- 1- 1

T AMCA: MESE 2323: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 3-22- 1- 2

T AMCA: MESE 2324: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 4-22- 1- 1

T AMCA: MESE 2324: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 4-22- 1- 2

T AMCA: MESE 2325: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 5-22- 1- 1

T AMCA: MESE 2325: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 5-22- 1- 2

T AMCA: MESE 2326: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 6-22- 1- 1

T AMCA: MESE 2326: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 6-22- 1- 2

T AMCA: MESE 2327: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 7-22- 1- 1

T AMCA: MESE 2327: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 7-22- 1- 2

T AMCA: MESE 2328: Unit\_JTAG\_test\_IR\_chain\_test: exp = 40: meas = 40 # Test item 8-22- 1- 1

T AMCA: MESE 2328: Unit\_JTAG\_test\_DR\_chain\_test: exp = 4: meas = 4 # Test item 8-22- 1- 2

T AMCA: MESE 2321: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.898 V, 9.7% # Test item 1- 4- 1- 1

T AMCA: MESE 2321: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.840 V, 10.5% # Test item 1- 4- 1- 2

T AMCA: MESE 2322: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.899 V, 10.2% # Test item 2- 4- 1- 1

T AMCA: MESE 2322: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.841 V, 10.0% # Test item 2- 4- 1- 2

T AMCA: MESE 2323: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.899 V, 10.3% # Test item 3- 4- 1- 1

T AMCA: MESE 2323: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.841 V, 10.3% # Test item 3- 4- 1- 2

T AMCA: MESE 2324: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.898 V, 9.8% # Test item 4- 4- 1- 1

T AMCA: MESE 2324: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.840 V, 10.4% # Test item 4- 4- 1- 2

T AMCA: MESE 2325: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.900 V, 10.4% # Test item 5- 4- 1- 1

T AMCA: MESE 2325: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.841 V, 10.3% # Test item 5- 4- 1- 2

T AMCA: MESE 2326: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.901 V, 10.7% # Test item 6- 4- 1- 1

T AMCA: MESE 2326: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.838 V, 11.1% # Test item 6- 4- 1- 2

T AMCA: MESE 2327: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.900 V, 10.5% # Test item 7- 4- 1- 1

T AMCA: MESE 2327: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.841 V, 10.0% # Test item 7- 4- 1- 2

T AMCA: MESE 2328: Precision\_DAC\_test\_\_3.0\_V\_test\_code\_0x0000dc00: V\_exp = 2.870 V, [2.583 ... 3.157 V] : V\_meas = 2.900 V, 10.5% # Test item 8- 4- 1- 1

T AMCA: MESE 2328: Precision\_DAC\_test\_-3.0\_V\_test\_code\_0x00002362: V\_exp = -2.870 V, [-3.157 ... -2.583 V] : V\_meas = -2.839 V, 10.9% # Test item 8- 4- 1- 2

T AMCA: MESE 2321: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.025 V, 8.4% # Test item 1- 4- 2- 1

T AMCA: MESE 2321: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.032 V, 32.2% # Test item 1- 4- 2- 2

T AMCA: MESE 2321: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.961 V, 13.0% # Test item 1- 4- 2- 3

T AMCA: MESE 2322: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.025 V, 8.3% # Test item 2- 4- 2- 1

T AMCA: MESE 2322: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.031 V, 31.0% # Test item 2- 4- 2- 2

T AMCA: MESE 2322: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.963 V, 12.5% # Test item 2- 4- 2- 3

T AMCA: MESE 2323: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.025 V, 8.5% # Test item 3- 4- 2- 1

T AMCA: MESE 2323: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.034 V, 34.1% # Test item 3- 4- 2- 2

T AMCA: MESE 2323: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.957 V, 14.4% # Test item 3- 4- 2- 3

T AMCA: MESE 2324: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.027 V, 8.9% # Test item 4- 4- 2- 1

T AMCA: MESE 2324: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.033 V, 33.1% # Test item 4- 4- 2- 2

T AMCA: MESE 2324: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.960 V, 13.2% # Test item 4- 4- 2- 3

T AMCA: MESE 2325: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.027 V, 9.1% # Test item 5- 4- 2- 1

T AMCA: MESE 2325: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.033 V, 33.1% # Test item 5- 4- 2- 2

T AMCA: MESE 2325: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.960 V, 13.3% # Test item 5- 4- 2- 3

T AMCA: MESE 2326: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.028 V, 9.2% # Test item 6- 4- 2- 1

T AMCA: MESE 2326: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.031 V, 31.3% # Test item 6- 4- 2- 2

T AMCA: MESE 2326: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.964 V, 12.0% # Test item 6- 4- 2- 3

T AMCA: MESE 2327: Precision\_ADC\_test\_+3.0\_V\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.024 V, 8.0% # Test item 7- 4- 2- 1

T AMCA: MESE 2327: Precision\_ADC\_test\_\_0.0\_V\_test: V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.031 V, 30.6% # Test item 7- 4- 2- 2

T AMCA: MESE 2327: Precision\_ADC\_test\_-3.0\_V\_test: V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.962 V, 12.5% # Test item 7- 4- 2- 3

T AMCA: MESE 2321: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.888 mA, 37.4% # Test item 1- 4- 3- 1

T AMCA: MESE 2321: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.012 mA, 8.0% # Test item 1- 4- 3- 2

T AMCA: MESE 2321: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.829 mA, 38.0% # Test item 1- 4- 3- 3

T AMCA: MESE 2321: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.006 mA, 4.2% # Test item 1- 4- 3- 4

T AMCA: MESE 2322: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.887 mA, 37.8% # Test item 2- 4- 3- 1

T AMCA: MESE 2322: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.010 mA, 7.0% # Test item 2- 4- 3- 2

T AMCA: MESE 2322: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.831 mA, 37.6% # Test item 2- 4- 3- 3

T AMCA: MESE 2322: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.008 mA, 5.4% # Test item 2- 4- 3- 4

T AMCA: MESE 2323: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.889 mA, 36.9% # Test item 3- 4- 3- 1

T AMCA: MESE 2323: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.016 mA, 10.5% # Test item 3- 4- 3- 2

T AMCA: MESE 2323: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.824 mA, 39.1% # Test item 3- 4- 3- 3

T AMCA: MESE 2323: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.004 mA, 2.7% # Test item 3- 4- 3- 4

T AMCA: MESE 2324: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.891 mA, 36.4% # Test item 4- 4- 3- 1

T AMCA: MESE 2324: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.013 mA, 9.0% # Test item 4- 4- 3- 2

T AMCA: MESE 2324: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.828 mA, 38.2% # Test item 4- 4- 3- 3

T AMCA: MESE 2324: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.006 mA, 3.7% # Test item 4- 4- 3- 4

T AMCA: MESE 2325: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.890 mA, 36.7% # Test item 5- 4- 3- 1

T AMCA: MESE 2325: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.014 mA, 9.5% # Test item 5- 4- 3- 2

T AMCA: MESE 2325: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.826 mA, 38.7% # Test item 5- 4- 3- 3

T AMCA: MESE 2325: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.004 mA, 2.8% # Test item 5- 4- 3- 4

T AMCA: MESE 2326: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.890 mA, 36.6% # Test item 6- 4- 3- 1

T AMCA: MESE 2326: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.010 mA, 6.7% # Test item 6- 4- 3- 2

T AMCA: MESE 2326: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.830 mA, 37.8% # Test item 6- 4- 3- 3

T AMCA: MESE 2326: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.008 mA, 5.4% # Test item 6- 4- 3- 4

T AMCA: MESE 2327: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.887 mA, 37.8% # Test item 7- 4- 3- 1

T AMCA: MESE 2327: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.010 mA, 6.9% # Test item 7- 4- 3- 2

T AMCA: MESE 2327: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.828 mA, 38.2% # Test item 7- 4- 3- 3

T AMCA: MESE 2327: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.006 mA, 4.1% # Test item 7- 4- 3- 4

T AMCA: MESE 2328: V\_force\_I\_measure\_test\_+3.0\_V\_test: I\_exp = 3.000 mA, [2.700 ... 3.300 mA] : I\_meas = 2.888 mA, 37.4% # Test item 8- 4- 3- 1

T AMCA: MESE 2328: V\_force\_I\_measure\_test\_+3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = 0.009 mA, 6.2% # Test item 8- 4- 3- 2

T AMCA: MESE 2328: V\_force\_I\_measure\_test\_-3.0\_V\_test: I\_exp = -3.000 mA, [-3.450 ... -2.550 mA] : I\_meas = -2.831 mA, 37.5% # Test item 8- 4- 3- 3

T AMCA: MESE 2328: V\_force\_I\_measure\_test\_-3.0\_V\_(267\_ohm\_shunt)\_test: I\_exp = 0.000 mA, [-0.150 ... 0.150 mA] : I\_meas = -0.010 mA, 6.8% # Test item 8- 4- 3- 4

T AMCA: MESE 2321: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.979 V, 7.1% # Test item 1- 4- 4- 1

T AMCA: MESE 2321: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.274 V, 6.6% # Test item 1- 4- 4- 2

T AMCA: MESE 2321: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.020 V, 4.6% # Test item 1- 4- 4- 3

T AMCA: MESE 2321: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.274 V, 7.1% # Test item 1- 4- 4- 4

T AMCA: MESE 2322: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.980 V, 6.6% # Test item 2- 4- 4- 1

T AMCA: MESE 2322: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.275 V, 9.9% # Test item 2- 4- 4- 2

T AMCA: MESE 2322: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.023 V, 5.1% # Test item 2- 4- 4- 3

T AMCA: MESE 2322: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.275 V, 9.4% # Test item 2- 4- 4- 4

T AMCA: MESE 2323: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.978 V, 7.2% # Test item 3- 4- 4- 1

T AMCA: MESE 2323: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.276 V, 14.0% # Test item 3- 4- 4- 2

T AMCA: MESE 2323: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.017 V, 3.7% # Test item 3- 4- 4- 3

T AMCA: MESE 2323: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.271 V, 4.5% # Test item 3- 4- 4- 4

T AMCA: MESE 2324: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.980 V, 6.6% # Test item 4- 4- 4- 1

T AMCA: MESE 2324: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.274 V, 7.5% # Test item 4- 4- 4- 2

T AMCA: MESE 2324: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.020 V, 4.5% # Test item 4- 4- 4- 3

T AMCA: MESE 2324: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.272 V, 0.1% # Test item 4- 4- 4- 4

T AMCA: MESE 2325: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.982 V, 5.9% # Test item 5- 4- 4- 1

T AMCA: MESE 2325: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.275 V, 10.3% # Test item 5- 4- 4- 2

T AMCA: MESE 2325: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.022 V, 4.9% # Test item 5- 4- 4- 3

T AMCA: MESE 2325: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.271 V, 2.2% # Test item 5- 4- 4- 4

T AMCA: MESE 2326: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.981 V, 6.2% # Test item 6- 4- 4- 1

T AMCA: MESE 2326: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.271 V, 2.2% # Test item 6- 4- 4- 2

T AMCA: MESE 2326: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.031 V, 6.9% # Test item 6- 4- 4- 3

T AMCA: MESE 2326: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.275 V, 9.9% # Test item 6- 4- 4- 4

T AMCA: MESE 2327: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.977 V, 7.6% # Test item 7- 4- 4- 1

T AMCA: MESE 2327: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.273 V, 3.8% # Test item 7- 4- 4- 2

T AMCA: MESE 2327: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.021 V, 4.8% # Test item 7- 4- 4- 3

T AMCA: MESE 2327: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.274 V, 6.2% # Test item 7- 4- 4- 4

T AMCA: MESE 2328: I\_force\_V\_measure\_test\_+3.0\_mA\_test: V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 2.978 V, 7.4% # Test item 8- 4- 4- 1

T AMCA: MESE 2328: I\_force\_V\_measure\_test\_+3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = 0.272 V, [0.245 ... 0.299 V] : V\_meas = 0.272 V, 1.3% # Test item 8- 4- 4- 2

T AMCA: MESE 2328: I\_force\_V\_measure\_test\_-3.0\_mA\_test: V\_exp = -3.000 V, [-3.450 ... -2.550 V] : V\_meas = -3.025 V, 5.6% # Test item 8- 4- 4- 3

T AMCA: MESE 2328: I\_force\_V\_measure\_test\_-3.0\_mA\_(267\_ohm\_shunt)\_test: V\_exp = -0.272 V, [-0.299 ... -0.245 V] : V\_meas = -0.274 V, 7.1% # Test item 8- 4- 4- 4

T AMCA: MESE 2321: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.992 kOhm, 8.2% # Test item 1- 4- 5- 1

T AMCA: MESE 2322: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.993 kOhm, 7.0% # Test item 2- 4- 5- 1

T AMCA: MESE 2323: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.992 kOhm, 8.3% # Test item 3- 4- 5- 1

T AMCA: MESE 2324: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.993 kOhm, 7.4% # Test item 4- 4- 5- 1

T AMCA: MESE 2325: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.993 kOhm, 7.0% # Test item 5- 4- 5- 1

T AMCA: MESE 2326: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.993 kOhm, 6.9% # Test item 6- 4- 5- 1

T AMCA: MESE 2327: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.992 kOhm, 8.5% # Test item 7- 4- 5- 1

T AMCA: MESE 2328: Precision\_ADC\_input\_impedance\_test\_+3.0\_mA\_test: R\_exp = 1.000 kOhm, [0.900 ... 1.100 kOhm] : R\_meas = 0.992 kOhm, 7.9% # Test item 8- 4- 5- 1

T AMCA: MCE 2320: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_1: R\_exp = 10220.000 Ohm, [9198.000 ... 11242.000 Ohm] : R\_meas = 10014.991 Ohm, 20.1% >> vMeas = 2.765V, vOffset = -0.240V, usedUnit = 1 # Test item 0- 5- 1- 1

T AMCA: MCE 2320: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_1\_(with\_GND): R\_exp = 59.000 Ohm, [49.000 ... 69.000 Ohm] : R\_meas = 58.704 Ohm, 3.0% >> vMeas = 0.180V, vOffset = 0.003V, usedUnit = 1 # Test item 0- 5- 1- 2

T AMCA: MCE 2320: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_2: R\_exp = 10220.000 Ohm, [9198.000 ... 11242.000 Ohm] : R\_meas = 10013.312 Ohm, 20.2% >> vMeas = 2.765V, vOffset = -0.239V, usedUnit = 1 # Test item 0- 5- 1- 3

T AMCA: MCE 2320: Common\_trigger\_input\_impedance\_test\_Common\_Trigger\_2\_(with\_GND): R\_exp = 59.000 Ohm, [49.000 ... 69.000 Ohm] : R\_meas = 58.788 Ohm, 2.1% >> vMeas = 0.179V, vOffset = 0.003V, usedUnit = 1 # Test item 0- 5- 1- 4

T AMCA: MCE 2320: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_\_0.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 1

T AMCA: MCE 2320: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_\_1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 2

T AMCA: MCE 2320: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_1\_(Ref\_DAC\_-1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 3

T AMCA: MCE 2320: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_\_0.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 4

T AMCA: MCE 2320: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_\_1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 5

T AMCA: MCE 2320: Common\_trigger\_input\_comparator\_test\_Common\_Trigger\_2\_(Ref\_DAC\_-1.0\_V): exp = passed: meas = passed >> usedUnit = 1 # Test item 0- 5- 2- 6

T AMCA: MESE 2321: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.498 V, 1.3% >> vOffset = -0.021V # Test item 1- 2- 9- 1

T AMCA: MESE 2321: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 21.4% >> vOffset = 0.003V # Test item 1- 2- 9- 2

T AMCA: MESE 2322: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 0.5% >> vOffset = -0.020V # Test item 2- 2- 9- 1

T AMCA: MESE 2322: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 23.9% >> vOffset = 0.002V # Test item 2- 2- 9- 2

T AMCA: MESE 2323: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.497 V, 2.2% >> vOffset = -0.018V # Test item 3- 2- 9- 1

T AMCA: MESE 2323: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 22.7% >> vOffset = 0.005V # Test item 3- 2- 9- 2

T AMCA: MESE 2324: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 0.8% >> vOffset = -0.020V # Test item 4- 2- 9- 1

T AMCA: MESE 2324: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 20.1% >> vOffset = 0.005V # Test item 4- 2- 9- 2

T AMCA: MESE 2325: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 0.9% >> vOffset = -0.019V # Test item 5- 2- 9- 1

T AMCA: MESE 2325: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 18.9% >> vOffset = 0.007V # Test item 5- 2- 9- 2

T AMCA: MESE 2326: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.502 V, 1.1% >> vOffset = -0.024V # Test item 6- 2- 9- 1

T AMCA: MESE 2326: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 21.4% >> vOffset = 0.003V # Test item 6- 2- 9- 2

T AMCA: MESE 2327: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.498 V, 1.6% >> vOffset = -0.020V # Test item 7- 2- 9- 1

T AMCA: MESE 2327: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 20.1% >> vOffset = 0.004V # Test item 7- 2- 9- 2

T AMCA: MESE 2328: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_OFF: V\_exp = 1.500 V, [1.350 ... 1.650 V] : V\_meas = 1.499 V, 0.9% >> vOffset = -0.021V # Test item 8- 2- 9- 1

T AMCA: MESE 2328: Unit\_GND\_connection\_relay\_test\_GND\_connection\_relay\_ON: V\_exp = 0.000 V, [-0.010 ... 0.010 V] : V\_meas = 0.002 V, 20.1% >> vOffset = 0.004V # Test item 8- 2- 9- 2

T AMCA: MESE 2321: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 1- 7- 1- 1

T AMCA: MESE 2322: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 2- 7- 1- 1

T AMCA: MESE 2323: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 3- 7- 1- 1

T AMCA: MESE 2324: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 4- 7- 1- 1

T AMCA: MESE 2325: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 5- 7- 1- 1

T AMCA: MESE 2326: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 6- 7- 1- 1

T AMCA: MESE 2327: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 7- 7- 1- 1

T AMCA: MESE 2328: SEQ\_FPGA\_memory\_access\_test\_Pattern\_1\_even\_0x5555555\_\_odd\_0xaaaaaaaa\_test: exp = passed: meas = passed # Test item 8- 7- 1- 1

T AMCA: MESE 2321: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 1- 7- 1- 2

T AMCA: MESE 2322: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 2- 7- 1- 2

T AMCA: MESE 2323: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 3- 7- 1- 2

T AMCA: MESE 2324: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 4- 7- 1- 2

T AMCA: MESE 2325: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 5- 7- 1- 2

T AMCA: MESE 2326: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 6- 7- 1- 2

T AMCA: MESE 2327: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 7- 7- 1- 2

T AMCA: MESE 2328: SEQ\_FPGA\_memory\_access\_test\_Pattern\_2\_even\_0xaaaaaaa\_\_odd\_0x55555555\_test: exp = passed: meas = passed # Test item 8- 7- 1- 2

T AMCA: MESE 2321: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 1- 7- 2- 1

T AMCA: MESE 2322: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 2- 7- 2- 1

T AMCA: MESE 2323: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 3- 7- 2- 1

T AMCA: MESE 2324: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 4- 7- 2- 1

T AMCA: MESE 2325: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 5- 7- 2- 1

T AMCA: MESE 2326: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 6- 7- 2- 1

T AMCA: MESE 2327: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 7- 7- 2- 1

T AMCA: MESE 2328: SDRAM\_test: Reg\_meas = 0x00000010 # Test item 8- 7- 2- 1

T AMCA: MESE 2321: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 1- 7- 3- 1

T AMCA: MESE 2321: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 1- 7- 3- 2

T AMCA: MESE 2322: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 2- 7- 3- 1

T AMCA: MESE 2322: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 2- 7- 3- 2

T AMCA: MESE 2323: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 3- 7- 3- 1

T AMCA: MESE 2323: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 3- 7- 3- 2

T AMCA: MESE 2324: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 4- 7- 3- 1

T AMCA: MESE 2324: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 4- 7- 3- 2

T AMCA: MESE 2325: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 5- 7- 3- 1

T AMCA: MESE 2325: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 5- 7- 3- 2

T AMCA: MESE 2326: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 6- 7- 3- 1

T AMCA: MESE 2326: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 6- 7- 3- 2

T AMCA: MESE 2327: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 7- 7- 3- 1

T AMCA: MESE 2327: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 7- 7- 3- 2

T AMCA: MESE 2328: SDRAM\_access\_test\_Walk\_1\_test: exp = passed: meas = passed # Test item 8- 7- 3- 1

T AMCA: MESE 2328: SDRAM\_access\_test\_Walk\_0\_test: exp = passed: meas = passed # Test item 8- 7- 3- 2

T AMCA: MESE 2321: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 1- 8- 1- 1

T AMCA: MESE 2321: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 1- 8- 1- 2

T AMCA: MESE 2321: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 1- 8- 1- 3

T AMCA: MESE 2321: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 4

T AMCA: MESE 2321: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 1- 8- 1- 5

T AMCA: MESE 2321: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 6

T AMCA: MESE 2321: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 1- 8- 1- 7

T AMCA: MESE 2321: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 8

T AMCA: MESE 2321: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 8- 1- 9

T AMCA: MESE 2322: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 2- 8- 1- 1

T AMCA: MESE 2322: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 2- 8- 1- 2

T AMCA: MESE 2322: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 2- 8- 1- 3

T AMCA: MESE 2322: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 4

T AMCA: MESE 2322: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 2- 8- 1- 5

T AMCA: MESE 2322: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 6

T AMCA: MESE 2322: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 2- 8- 1- 7

T AMCA: MESE 2322: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 8

T AMCA: MESE 2322: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 8- 1- 9

T AMCA: MESE 2323: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 3- 8- 1- 1

T AMCA: MESE 2323: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 3- 8- 1- 2

T AMCA: MESE 2323: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 3- 8- 1- 3

T AMCA: MESE 2323: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 4

T AMCA: MESE 2323: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 3- 8- 1- 5

T AMCA: MESE 2323: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 6

T AMCA: MESE 2323: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 3- 8- 1- 7

T AMCA: MESE 2323: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 8

T AMCA: MESE 2323: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 8- 1- 9

T AMCA: MESE 2324: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 4- 8- 1- 1

T AMCA: MESE 2324: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 4- 8- 1- 2

T AMCA: MESE 2324: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 4- 8- 1- 3

T AMCA: MESE 2324: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 4

T AMCA: MESE 2324: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 4- 8- 1- 5

T AMCA: MESE 2324: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 6

T AMCA: MESE 2324: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 4- 8- 1- 7

T AMCA: MESE 2324: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 8

T AMCA: MESE 2324: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 8- 1- 9

T AMCA: MESE 2325: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 5- 8- 1- 1

T AMCA: MESE 2325: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 5- 8- 1- 2

T AMCA: MESE 2325: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 5- 8- 1- 3

T AMCA: MESE 2325: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 4

T AMCA: MESE 2325: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 5- 8- 1- 5

T AMCA: MESE 2325: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 6

T AMCA: MESE 2325: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 5- 8- 1- 7

T AMCA: MESE 2325: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 8

T AMCA: MESE 2325: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 8- 1- 9

T AMCA: MESE 2326: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 6- 8- 1- 1

T AMCA: MESE 2326: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 6- 8- 1- 2

T AMCA: MESE 2326: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 6- 8- 1- 3

T AMCA: MESE 2326: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 4

T AMCA: MESE 2326: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 6- 8- 1- 5

T AMCA: MESE 2326: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 6

T AMCA: MESE 2326: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 6- 8- 1- 7

T AMCA: MESE 2326: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 8

T AMCA: MESE 2326: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 8- 1- 9

T AMCA: MESE 2327: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 7- 8- 1- 1

T AMCA: MESE 2327: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 7- 8- 1- 2

T AMCA: MESE 2327: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 7- 8- 1- 3

T AMCA: MESE 2327: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 4

T AMCA: MESE 2327: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 7- 8- 1- 5

T AMCA: MESE 2327: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 6

T AMCA: MESE 2327: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 7- 8- 1- 7

T AMCA: MESE 2327: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 8

T AMCA: MESE 2327: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 8- 1- 9

T AMCA: MESE 2328: Unit\_CTG\_register\_test\_deskew: Reg\_exp = 0x0000AAAA: Reg\_meas = 0x0000AAAA # Test item 8- 8- 1- 1

T AMCA: MESE 2328: Unit\_CTG\_register\_test\_cclk\_repeat\_s: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 8- 8- 1- 2

T AMCA: MESE 2328: Unit\_CTG\_register\_test\_cclk\_repeat\_r: Reg\_exp = 0x00015555: Reg\_meas = 0x00015555 # Test item 8- 8- 1- 3

T AMCA: MESE 2328: Unit\_CTG\_register\_test\_tv1sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 4

T AMCA: MESE 2328: Unit\_CTG\_register\_test\_synclk\_sel: Reg\_exp = 0x00000005: Reg\_meas = 0x00000005 # Test item 8- 8- 1- 5

T AMCA: MESE 2328: Unit\_CTG\_register\_test\_func\_sel: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 6

T AMCA: MESE 2328: Unit\_CTG\_register\_test\_sync\_reg\_out: Reg\_exp = 0x00000555: Reg\_meas = 0x00000555 # Test item 8- 8- 1- 7

T AMCA: MESE 2328: Unit\_CTG\_register\_test\_syncwire: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 8

T AMCA: MESE 2328: Unit\_CTG\_register\_test\_msdetect: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 8- 1- 9

T AMCA: MESE 2321: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 1, masterModule = 1, slaveUnit = 3, slaveModule = 1 # Test item 1- 8- 3- 1

T AMCA: MESE 2321: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 1, masterModule = 1, slaveUnit = 3, slaveModule = 1 # Test item 1- 8- 3- 2

T AMCA: MESE 2323: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 3, masterModule = 1, slaveUnit = 2, slaveModule = 2 # Test item 3- 8- 3- 3

T AMCA: MESE 2323: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 3, masterModule = 1, slaveUnit = 2, slaveModule = 2 # Test item 3- 8- 3- 4

T AMCA: MESE 2322: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 2, masterModule = 2, slaveUnit = 4, slaveModule = 2 # Test item 2- 8- 3- 5

T AMCA: MESE 2322: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 2, masterModule = 2, slaveUnit = 4, slaveModule = 2 # Test item 2- 8- 3- 6

T AMCA: MESE 2324: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 4, masterModule = 2, slaveUnit = 5, slaveModule = 3 # Test item 4- 8- 3- 7

T AMCA: MESE 2324: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 4, masterModule = 2, slaveUnit = 5, slaveModule = 3 # Test item 4- 8- 3- 8

T AMCA: MESE 2325: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 5, masterModule = 3, slaveUnit = 7, slaveModule = 3 # Test item 5- 8- 3- 9

T AMCA: MESE 2325: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 5, masterModule = 3, slaveUnit = 7, slaveModule = 3 # Test item 5- 8- 3-10

T AMCA: MESE 2327: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 7, masterModule = 3, slaveUnit = 8, slaveModule = 4 # Test item 7- 8- 3-11

T AMCA: MESE 2327: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 7, masterModule = 3, slaveUnit = 8, slaveModule = 4 # Test item 7- 8- 3-12

T AMCA: MESE 2328: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 8, masterModule = 4, slaveUnit = 6, slaveModule = 4 # Test item 8- 8- 3-13

T AMCA: MESE 2328: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 8, masterModule = 4, slaveUnit = 6, slaveModule = 4 # Test item 8- 8- 3-14

T AMCA: MESE 2326: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_HI: Reg\_exp = 0x00000002: Reg\_meas = 0x00000002 >> masterUnit = 6, masterModule = 4, slaveUnit = 1, slaveModule = 1 # Test item 6- 8- 3-15

T AMCA: MESE 2326: Unit\_CTG\_master/slave\_trigger\_bus\_line\_test\_M\_TRIG\_LO: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 >> masterUnit = 6, masterModule = 4, slaveUnit = 1, slaveModule = 1 # Test item 6- 8- 3-16

T AMCA: MESE 2321: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 10.076 ns, 2.5% >> short = 60773, long = 30077 # Test item 1- 8- 6- 1

T AMCA: MESE 2322: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.942 ns, 2.9% >> short = 60411, long = 30190 # Test item 2- 8- 6- 1

T AMCA: MESE 2323: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.965 ns, 1.7% >> short = 61854, long = 30510 # Test item 3- 8- 6- 1

T AMCA: MESE 2324: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.818 ns, 9.1% >> short = 60675, long = 30447 # Test item 4- 8- 6- 1

T AMCA: MESE 2325: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 10.164 ns, 5.5% >> short = 59500, long = 29633 # Test item 5- 8- 6- 1

T AMCA: MESE 2326: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 9.859 ns, 7.0% >> short = 62087, long = 30733 # Test item 6- 8- 6- 1

T AMCA: MESE 2327: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 10.148 ns, 4.9% >> short = 60053, long = 29793 # Test item 7- 8- 6- 1

T AMCA: MESE 2328: Unit\_CTG\_CCLK\_vernier\_test: t\_exp = 10.000 ns, [8.000 ... 13.000 ns] : t\_meas = 10.118 ns, 3.9% >> short = 60441, long = 29933 # Test item 8- 8- 6- 1

T AMCA: MESE 2321: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 1- 8- 7- 1

T AMCA: MESE 2321: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18514, 37.1% # Test item 1- 8- 7- 2

T AMCA: MESE 2321: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004852: Reg\_meas = 0x00004852 # Test item 1- 8- 7- 3

T AMCA: MESE 2321: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 8- 7- 4

T AMCA: MESE 2322: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 2- 8- 7- 1

T AMCA: MESE 2322: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18445, 38.9% # Test item 2- 8- 7- 2

T AMCA: MESE 2322: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000480D: Reg\_meas = 0x0000480D # Test item 2- 8- 7- 3

T AMCA: MESE 2322: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 8- 7- 4

T AMCA: MESE 2323: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 3- 8- 7- 1

T AMCA: MESE 2323: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17962, 50.9% # Test item 3- 8- 7- 2

T AMCA: MESE 2323: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000462A: Reg\_meas = 0x0000462A # Test item 3- 8- 7- 3

T AMCA: MESE 2323: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 8- 7- 4

T AMCA: MESE 2324: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 4- 8- 7- 1

T AMCA: MESE 2324: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18008, 49.8% # Test item 4- 8- 7- 2

T AMCA: MESE 2324: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004658: Reg\_meas = 0x00004658 # Test item 4- 8- 7- 3

T AMCA: MESE 2324: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 8- 7- 4

T AMCA: MESE 2325: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 5- 8- 7- 1

T AMCA: MESE 2325: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18477, 38.1% # Test item 5- 8- 7- 2

T AMCA: MESE 2325: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x0000482D: Reg\_meas = 0x0000482D # Test item 5- 8- 7- 3

T AMCA: MESE 2325: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 8- 7- 4

T AMCA: MESE 2326: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 6- 8- 7- 1

T AMCA: MESE 2326: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18353, 41.2% # Test item 6- 8- 7- 2

T AMCA: MESE 2326: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x000047B1: Reg\_meas = 0x000047B1 # Test item 6- 8- 7- 3

T AMCA: MESE 2326: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 8- 7- 4

T AMCA: MESE 2327: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 7- 8- 7- 1

T AMCA: MESE 2327: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 17972, 50.7% # Test item 7- 8- 7- 2

T AMCA: MESE 2327: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x00004634: Reg\_meas = 0x00004634 # Test item 7- 8- 7- 3

T AMCA: MESE 2327: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 8- 7- 4

T AMCA: MESE 2328: Unit\_CTG\_free-run\_CLK\_test\_Initial\_test: exp = 0: meas = 0 # Test item 8- 8- 7- 1

T AMCA: MESE 2328: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: exp = 20000, [16000 ... 24000] : meas = 18913, 27.2% # Test item 8- 8- 7- 2

T AMCA: MESE 2328: Unit\_CTG\_free-run\_CLK\_test\_Disable\_test: Reg\_exp = 0x000049E1: Reg\_meas = 0x000049E1 # Test item 8- 8- 7- 3

T AMCA: MESE 2328: Unit\_CTG\_free-run\_CLK\_test\_Reset\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 8- 7- 4

T AMCA: MESE 2321: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 9- 1- 1

T AMCA: MESE 2321: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 1- 9- 1- 2

T AMCA: MESE 2321: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 3

T AMCA: MESE 2321: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 4

T AMCA: MESE 2321: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 5

T AMCA: MESE 2321: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 6

T AMCA: MESE 2321: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1- 7

T AMCA: MESE 2321: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 1- 9- 1- 8

T AMCA: MESE 2321: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 1- 9- 1- 9

T AMCA: MESE 2321: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 1- 9- 1-10

T AMCA: MESE 2321: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 1- 9- 1-11

T AMCA: MESE 2321: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 1- 9- 1-12

T AMCA: MESE 2321: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 1- 9- 1-13

T AMCA: MESE 2321: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 1- 9- 1-14

T AMCA: MESE 2322: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 9- 1- 1

T AMCA: MESE 2322: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 2- 9- 1- 2

T AMCA: MESE 2322: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 3

T AMCA: MESE 2322: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 4

T AMCA: MESE 2322: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 5

T AMCA: MESE 2322: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 6

T AMCA: MESE 2322: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1- 7

T AMCA: MESE 2322: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 2- 9- 1- 8

T AMCA: MESE 2322: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 2- 9- 1- 9

T AMCA: MESE 2322: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 2- 9- 1-10

T AMCA: MESE 2322: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 2- 9- 1-11

T AMCA: MESE 2322: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 2- 9- 1-12

T AMCA: MESE 2322: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 2- 9- 1-13

T AMCA: MESE 2322: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 2- 9- 1-14

T AMCA: MESE 2323: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 9- 1- 1

T AMCA: MESE 2323: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 3- 9- 1- 2

T AMCA: MESE 2323: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 3

T AMCA: MESE 2323: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 4

T AMCA: MESE 2323: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 5

T AMCA: MESE 2323: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 6

T AMCA: MESE 2323: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1- 7

T AMCA: MESE 2323: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 3- 9- 1- 8

T AMCA: MESE 2323: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 3- 9- 1- 9

T AMCA: MESE 2323: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 3- 9- 1-10

T AMCA: MESE 2323: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 3- 9- 1-11

T AMCA: MESE 2323: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 3- 9- 1-12

T AMCA: MESE 2323: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 3- 9- 1-13

T AMCA: MESE 2323: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 3- 9- 1-14

T AMCA: MESE 2324: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 9- 1- 1

T AMCA: MESE 2324: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 4- 9- 1- 2

T AMCA: MESE 2324: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 3

T AMCA: MESE 2324: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 4

T AMCA: MESE 2324: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 5

T AMCA: MESE 2324: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 6

T AMCA: MESE 2324: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1- 7

T AMCA: MESE 2324: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 4- 9- 1- 8

T AMCA: MESE 2324: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 4- 9- 1- 9

T AMCA: MESE 2324: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 4- 9- 1-10

T AMCA: MESE 2324: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 4- 9- 1-11

T AMCA: MESE 2324: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 4- 9- 1-12

T AMCA: MESE 2324: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 4- 9- 1-13

T AMCA: MESE 2324: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 4- 9- 1-14

T AMCA: MESE 2325: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 9- 1- 1

T AMCA: MESE 2325: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 5- 9- 1- 2

T AMCA: MESE 2325: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 3

T AMCA: MESE 2325: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 4

T AMCA: MESE 2325: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 5

T AMCA: MESE 2325: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 6

T AMCA: MESE 2325: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1- 7

T AMCA: MESE 2325: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 5- 9- 1- 8

T AMCA: MESE 2325: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 5- 9- 1- 9

T AMCA: MESE 2325: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 5- 9- 1-10

T AMCA: MESE 2325: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 5- 9- 1-11

T AMCA: MESE 2325: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 5- 9- 1-12

T AMCA: MESE 2325: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 5- 9- 1-13

T AMCA: MESE 2325: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 5- 9- 1-14

T AMCA: MESE 2326: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 9- 1- 1

T AMCA: MESE 2326: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 6- 9- 1- 2

T AMCA: MESE 2326: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 3

T AMCA: MESE 2326: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 4

T AMCA: MESE 2326: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 5

T AMCA: MESE 2326: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 6

T AMCA: MESE 2326: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1- 7

T AMCA: MESE 2326: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 6- 9- 1- 8

T AMCA: MESE 2326: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 6- 9- 1- 9

T AMCA: MESE 2326: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 6- 9- 1-10

T AMCA: MESE 2326: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 6- 9- 1-11

T AMCA: MESE 2326: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 6- 9- 1-12

T AMCA: MESE 2326: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 6- 9- 1-13

T AMCA: MESE 2326: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 6- 9- 1-14

T AMCA: MESE 2327: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 9- 1- 1

T AMCA: MESE 2327: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 7- 9- 1- 2

T AMCA: MESE 2327: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 3

T AMCA: MESE 2327: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 4

T AMCA: MESE 2327: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 5

T AMCA: MESE 2327: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 6

T AMCA: MESE 2327: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1- 7

T AMCA: MESE 2327: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 7- 9- 1- 8

T AMCA: MESE 2327: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 7- 9- 1- 9

T AMCA: MESE 2327: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 7- 9- 1-10

T AMCA: MESE 2327: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 7- 9- 1-11

T AMCA: MESE 2327: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 7- 9- 1-12

T AMCA: MESE 2327: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 7- 9- 1-13

T AMCA: MESE 2327: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 7- 9- 1-14

T AMCA: MESE 2328: Sequencer\_instruction\_test\_Cclk\_flag\_clear\_test: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 9- 1- 1

T AMCA: MESE 2328: Sequencer\_instruction\_test\_initial\_test\_CTG\_status\_flag: Reg\_exp = 0x00000000: Reg\_meas = 0x00000000 # Test item 8- 9- 1- 2

T AMCA: MESE 2328: Sequencer\_instruction\_test\_initial\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 3

T AMCA: MESE 2328: Sequencer\_instruction\_test\_LOAD\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 4

T AMCA: MESE 2328: Sequencer\_instruction\_test\_LOAD\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 5

T AMCA: MESE 2328: Sequencer\_instruction\_test\_TRIGGER1\_test\_CTG\_status\_flag: Reg\_exp = 0x00310000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 6

T AMCA: MESE 2328: Sequencer\_instruction\_test\_TRIGGER1\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1- 7

T AMCA: MESE 2328: Sequencer\_instruction\_test\_TRIGGER1\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000000: Reg\_meas = 0x00000003 # Test item 8- 9- 1- 8

T AMCA: MESE 2328: Sequencer\_instruction\_test\_TRIGGER2\_test\_CTG\_status\_flag: Reg\_exp = 0x00200000: Reg\_meas = 0x00200000 # Test item 8- 9- 1- 9

T AMCA: MESE 2328: Sequencer\_instruction\_test\_TRIGGER2\_test\_SEQ\_status\_flag: Reg\_exp = 0x00080000: Reg\_meas = 0x00080000 # Test item 8- 9- 1-10

T AMCA: MESE 2328: Sequencer\_instruction\_test\_TRIGGER2\_test\_executed\_uPM\_addr: Reg\_exp = 0x00000001: Reg\_meas = 0x00000003 # Test item 8- 9- 1-11

T AMCA: MESE 2328: Sequencer\_instruction\_test\_ABORT\_test\_CTG\_status\_flag: Reg\_exp = 0x00A00000: Reg\_meas = 0x00200000 # Test item 8- 9- 1-12

T AMCA: MESE 2328: Sequencer\_instruction\_test\_ABORT\_test\_SEQ\_status\_flag: Reg\_exp = 0x000A0003: Reg\_meas = 0x00080000 # Test item 8- 9- 1-13

T AMCA: MESE 2328: Sequencer\_instruction\_test\_Cclk\_receive\_test: Reg\_exp = 0x00000001: Reg\_meas = 0x00000001 # Test item 8- 9- 1-14

T AMCA: MESE 2321: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.957 V, 0.9% # Test item 1-17- 1- 1

T AMCA: MESE 2321: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.957 V, 0.9% # Test item 1-17- 1- 2

T AMCA: MESE 2321: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.956 V, 1.8% # Test item 1-17- 1- 3

T AMCA: MESE 2321: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.949 V, 8.2% # Test item 1-17- 1- 4

T AMCA: MESE 2321: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.068 V, 0.0% # Test item 1-17- 1- 5

T AMCA: MESE 2321: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.975 V, 3.6% # Test item 1-17- 1- 6

T AMCA: MESE 2321: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.971 V, 1.8% # Test item 1-17- 1- 7

T AMCA: MESE 2321: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.897 V, 1.3% # Test item 1-17- 1- 8

T AMCA: MESE 2321: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.973 V, 5.5% # Test item 1-17- 1- 9

T AMCA: MESE 2321: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.989 V, 3.6% # Test item 1-17- 1-10

T AMCA: MESE 2321: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.977 V, 0.9% # Test item 1-17- 1-11

T AMCA: MESE 2322: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.960 V, 1.8% # Test item 2-17- 1- 1

T AMCA: MESE 2322: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.960 V, 1.8% # Test item 2-17- 1- 2

T AMCA: MESE 2322: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.959 V, 0.9% # Test item 2-17- 1- 3

T AMCA: MESE 2322: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.957 V, 0.9% # Test item 2-17- 1- 4

T AMCA: MESE 2322: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.060 V, 8.9% # Test item 2-17- 1- 5

T AMCA: MESE 2322: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.983 V, 3.6% # Test item 2-17- 1- 6

T AMCA: MESE 2322: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.969 V, 3.6% # Test item 2-17- 1- 7

T AMCA: MESE 2322: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.900 V, 0.7% # Test item 2-17- 1- 8

T AMCA: MESE 2322: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.971 V, 7.3% # Test item 2-17- 1- 9

T AMCA: MESE 2322: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.987 V, 5.5% # Test item 2-17- 1-10

T AMCA: MESE 2322: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.978 V, 1.8% # Test item 2-17- 1-11

T AMCA: MESE 2323: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.953 V, 4.5% # Test item 3-17- 1- 1

T AMCA: MESE 2323: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.956 V, 1.8% # Test item 3-17- 1- 2

T AMCA: MESE 2323: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.954 V, 3.6% # Test item 3-17- 1- 3

T AMCA: MESE 2323: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.952 V, 5.5% # Test item 3-17- 1- 4

T AMCA: MESE 2323: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.060 V, 7.3% # Test item 3-17- 1- 5

T AMCA: MESE 2323: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.981 V, 1.8% # Test item 3-17- 1- 6

T AMCA: MESE 2323: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.972 V, 0.9% # Test item 3-17- 1- 7

T AMCA: MESE 2323: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.632 V, 1.3% # Test item 3-17- 1- 8

T AMCA: MESE 2323: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.966 V, 11.8% # Test item 3-17- 1- 9

T AMCA: MESE 2323: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.720 V, 7.3% # Test item 3-17- 1-10

T AMCA: MESE 2324: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.954 V, 3.6% # Test item 4-17- 1- 1

T AMCA: MESE 2324: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.956 V, 1.8% # Test item 4-17- 1- 2

T AMCA: MESE 2324: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.956 V, 1.8% # Test item 4-17- 1- 3

T AMCA: MESE 2324: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.953 V, 4.5% # Test item 4-17- 1- 4

T AMCA: MESE 2324: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.060 V, 7.3% # Test item 4-17- 1- 5

T AMCA: MESE 2324: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.984 V, 4.5% # Test item 4-17- 1- 6

T AMCA: MESE 2324: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.966 V, 6.4% # Test item 4-17- 1- 7

T AMCA: MESE 2324: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.632 V, 1.3% # Test item 4-17- 1- 8

T AMCA: MESE 2324: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.974 V, 4.5% # Test item 4-17- 1- 9

T AMCA: MESE 2324: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.716 V, 10.9% # Test item 4-17- 1-10

T AMCA: MESE 2325: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.956 V, 1.8% # Test item 5-17- 1- 1

T AMCA: MESE 2325: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.958 V, 0.0% # Test item 5-17- 1- 2

T AMCA: MESE 2325: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.953 V, 4.5% # Test item 5-17- 1- 3

T AMCA: MESE 2325: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.962 V, 3.6% # Test item 5-17- 1- 4

T AMCA: MESE 2325: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.063 V, 5.6% # Test item 5-17- 1- 5

T AMCA: MESE 2325: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.979 V, 0.0% # Test item 5-17- 1- 6

T AMCA: MESE 2325: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.968 V, 4.5% # Test item 5-17- 1- 7

T AMCA: MESE 2325: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.891 V, 6.8% # Test item 5-17- 1- 8

T AMCA: MESE 2325: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.972 V, 6.4% # Test item 5-17- 1- 9

T AMCA: MESE 2325: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.994 V, 0.9% # Test item 5-17- 1-10

T AMCA: MESE 2325: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.962 V, 12.7% # Test item 5-17- 1-11

T AMCA: MESE 2326: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.957 V, 0.9% # Test item 6-17- 1- 1

T AMCA: MESE 2326: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.957 V, 0.9% # Test item 6-17- 1- 2

T AMCA: MESE 2326: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.960 V, 1.8% # Test item 6-17- 1- 3

T AMCA: MESE 2326: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.962 V, 3.6% # Test item 6-17- 1- 4

T AMCA: MESE 2326: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.065 V, 2.7% # Test item 6-17- 1- 5

T AMCA: MESE 2326: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.980 V, 0.9% # Test item 6-17- 1- 6

T AMCA: MESE 2326: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.968 V, 4.5% # Test item 6-17- 1- 7

T AMCA: MESE 2326: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.628 V, 5.0% # Test item 6-17- 1- 8

T AMCA: MESE 2326: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.979 V, 0.0% # Test item 6-17- 1- 9

T AMCA: MESE 2326: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.711 V, 15.5% # Test item 6-17- 1-10

T AMCA: MESE 2327: Digitizer\_power\_supply\_test\_+5VA\_2: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.948 V, 9.1% # Test item 7-17- 1- 1

T AMCA: MESE 2327: Digitizer\_power\_supply\_test\_+5VA\_22: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.954 V, 3.6% # Test item 7-17- 1- 2

T AMCA: MESE 2327: Digitizer\_power\_supply\_test\_-5VA\_2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.951 V, 6.4% # Test item 7-17- 1- 3

T AMCA: MESE 2327: Digitizer\_power\_supply\_test\_-5VA\_R2: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.955 V, 2.7% # Test item 7-17- 1- 4

T AMCA: MESE 2327: Digitizer\_power\_supply\_test\_+3.6VA\_2: V\_exp = 1.068 V, [0.958 ... 1.178 V] : V\_meas = 1.058 V, 9.1% # Test item 7-17- 1- 5

T AMCA: MESE 2327: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_22: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.982 V, 2.7% # Test item 7-17- 1- 6

T AMCA: MESE 2327: Digitizer\_power\_supply\_test\_+9VA\_2: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.965 V, 7.3% # Test item 7-17- 1- 7

T AMCA: MESE 2327: Digitizer\_power\_supply\_test\_+VTT2\_0V9: V\_exp = 0.633 V, [0.523 ... 0.743 V] : V\_meas = 0.619 V, 13.2% # Test item 7-17- 1- 8

T AMCA: MESE 2327: Digitizer\_power\_supply\_test\_+3.3VA\_R2: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.978 V, 0.9% # Test item 7-17- 1- 9

T AMCA: MESE 2327: Digitizer\_power\_supply\_test\_+1.1VD: V\_exp = 0.728 V, [0.618 ... 0.838 V] : V\_meas = 0.712 V, 14.5% # Test item 7-17- 1-10

T AMCA: MESE 2328: Digitizer\_power\_supply\_test\_+5VA\_1: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.954 V, 3.6% # Test item 8-17- 1- 1

T AMCA: MESE 2328: Digitizer\_power\_supply\_test\_+5VA\_12: V\_exp = 0.958 V, [0.848 ... 1.068 V] : V\_meas = 0.960 V, 1.8% # Test item 8-17- 1- 2

T AMCA: MESE 2328: Digitizer\_power\_supply\_test\_-5VA\_1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.954 V, 3.6% # Test item 8-17- 1- 3

T AMCA: MESE 2328: Digitizer\_power\_supply\_test\_-5VA\_R1: V\_exp = -0.958 V, [-1.068 ... -0.848 V] : V\_meas = -0.951 V, 6.4% # Test item 8-17- 1- 4

T AMCA: MESE 2328: Digitizer\_power\_supply\_test\_+3.6VA\_1: V\_exp = 1.068 V, [0.978 ... 1.158 V] : V\_meas = 1.056 V, 13.3% # Test item 8-17- 1- 5

T AMCA: MESE 2328: Digitizer\_power\_supply\_test\_+3.3VA\_CLK\_12: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.982 V, 2.7% # Test item 8-17- 1- 6

T AMCA: MESE 2328: Digitizer\_power\_supply\_test\_+9VA\_1: V\_exp = 0.973 V, [0.863 ... 1.083 V] : V\_meas = 0.956 V, 15.5% # Test item 8-17- 1- 7

T AMCA: MESE 2328: Digitizer\_power\_supply\_test\_+VTT1\_0V9: V\_exp = 0.898 V, [0.788 ... 1.108 V] : V\_meas = 0.888 V, 9.5% # Test item 8-17- 1- 8

T AMCA: MESE 2328: Digitizer\_power\_supply\_test\_+3.3VA\_R1: V\_exp = 0.979 V, [0.869 ... 1.089 V] : V\_meas = 0.974 V, 4.5% # Test item 8-17- 1- 9

T AMCA: MESE 2328: Digitizer\_power\_supply\_test\_+1.5VA\_IF\_2: V\_exp = 0.993 V, [0.883 ... 1.103 V] : V\_meas = 0.996 V, 2.7% # Test item 8-17- 1-10

T AMCA: MESE 2328: Digitizer\_power\_supply\_test\_+1.8VD: V\_exp = 0.976 V, [0.866 ... 1.086 V] : V\_meas = 0.964 V, 10.9% # Test item 8-17- 1-11

T AMCA: MESE 2321: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.4% # Test item 1-15- 1- 1

T AMCA: MESE 2321: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.0% # Test item 1-15- 1- 2

T AMCA: MESE 2321: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.5% # Test item 1-15- 1- 3

T AMCA: MESE 2321: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.4% # Test item 1-15- 1- 4

T AMCA: MESE 2321: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.9% # Test item 1-15- 1- 5

T AMCA: MESE 2321: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.4% # Test item 1-15- 1- 6

T AMCA: MESE 2321: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.7% # Test item 1-15- 1- 7

T AMCA: MESE 2321: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.5% # Test item 1-15- 1- 8

T AMCA: MESE 2321: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.4% # Test item 1-15- 1- 9

T AMCA: MESE 2321: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.7% # Test item 1-15- 1-10

T AMCA: MESE 2321: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.4% # Test item 1-15- 1-11

T AMCA: MESE 2321: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.4% # Test item 1-15- 1-12

T AMCA: MESE 2322: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.8% # Test item 2-15- 1- 1

T AMCA: MESE 2322: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.3% # Test item 2-15- 1- 2

T AMCA: MESE 2322: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.006 V, 2.1% # Test item 2-15- 1- 3

T AMCA: MESE 2322: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 2.8% # Test item 2-15- 1- 4

T AMCA: MESE 2322: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.4% # Test item 2-15- 1- 5

T AMCA: MESE 2322: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.006 V, 2.1% # Test item 2-15- 1- 6

T AMCA: MESE 2322: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 3.9% # Test item 2-15- 1- 7

T AMCA: MESE 2322: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.7% # Test item 2-15- 1- 8

T AMCA: MESE 2322: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.3% # Test item 2-15- 1- 9

T AMCA: MESE 2322: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 3.9% # Test item 2-15- 1-10

T AMCA: MESE 2322: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.9% # Test item 2-15- 1-11

T AMCA: MESE 2322: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.3% # Test item 2-15- 1-12

T AMCA: MESE 2323: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.011 V, 3.5% # Test item 3-15- 1- 1

T AMCA: MESE 2323: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.9% # Test item 3-15- 1- 2

T AMCA: MESE 2323: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.002 V, 0.8% # Test item 3-15- 1- 3

T AMCA: MESE 2323: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.4% # Test item 3-15- 1- 4

T AMCA: MESE 2323: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.0% # Test item 3-15- 1- 5

T AMCA: MESE 2323: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.002 V, 0.8% # Test item 3-15- 1- 6

T AMCA: MESE 2323: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.3% # Test item 3-15- 1- 7

T AMCA: MESE 2323: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.9% # Test item 3-15- 1- 8

T AMCA: MESE 2323: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.002 V, 0.7% # Test item 3-15- 1- 9

T AMCA: MESE 2323: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.010 V, 3.2% # Test item 3-15- 1-10

T AMCA: MESE 2323: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.7% # Test item 3-15- 1-11

T AMCA: MESE 2323: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.002 V, 0.7% # Test item 3-15- 1-12

T AMCA: MESE 2324: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.4% # Test item 4-15- 1- 1

T AMCA: MESE 2324: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.2% # Test item 4-15- 1- 2

T AMCA: MESE 2324: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.005 V, 1.6% # Test item 4-15- 1- 3

T AMCA: MESE 2324: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.4% # Test item 4-15- 1- 4

T AMCA: MESE 2324: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.5% # Test item 4-15- 1- 5

T AMCA: MESE 2324: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.005 V, 1.5% # Test item 4-15- 1- 6

T AMCA: MESE 2324: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.8% # Test item 4-15- 1- 7

T AMCA: MESE 2324: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 7.8% # Test item 4-15- 1- 8

T AMCA: MESE 2324: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.003 V, 1.1% # Test item 4-15- 1- 9

T AMCA: MESE 2324: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.015 V, 4.9% # Test item 4-15- 1-10

T AMCA: MESE 2324: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.008 V, 7.7% # Test item 4-15- 1-11

T AMCA: MESE 2324: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.003 V, 1.2% # Test item 4-15- 1-12

T AMCA: MESE 2325: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.5% # Test item 5-15- 1- 1

T AMCA: MESE 2325: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.4% # Test item 5-15- 1- 2

T AMCA: MESE 2325: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.002 V, 0.6% # Test item 5-15- 1- 3

T AMCA: MESE 2325: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.017 V, 5.6% # Test item 5-15- 1- 4

T AMCA: MESE 2325: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 9.6% # Test item 5-15- 1- 5

T AMCA: MESE 2325: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.002 V, 0.5% # Test item 5-15- 1- 6

T AMCA: MESE 2325: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.018 V, 6.0% # Test item 5-15- 1- 7

T AMCA: MESE 2325: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 10.4% # Test item 5-15- 1- 8

T AMCA: MESE 2325: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.3% # Test item 5-15- 1- 9

T AMCA: MESE 2325: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.018 V, 5.9% # Test item 5-15- 1-10

T AMCA: MESE 2325: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.010 V, 10.3% # Test item 5-15- 1-11

T AMCA: MESE 2325: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.001 V, 0.3% # Test item 5-15- 1-12

T AMCA: MESE 2326: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.3% # Test item 6-15- 1- 1

T AMCA: MESE 2326: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.9% # Test item 6-15- 1- 2

T AMCA: MESE 2326: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.8% # Test item 6-15- 1- 3

T AMCA: MESE 2326: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.2% # Test item 6-15- 1- 4

T AMCA: MESE 2326: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.9% # Test item 6-15- 1- 5

T AMCA: MESE 2326: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.8% # Test item 6-15- 1- 6

T AMCA: MESE 2326: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.4% # Test item 6-15- 1- 7

T AMCA: MESE 2326: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.0% # Test item 6-15- 1- 8

T AMCA: MESE 2326: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.7% # Test item 6-15- 1- 9

T AMCA: MESE 2326: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.016 V, 5.3% # Test item 6-15- 1-10

T AMCA: MESE 2326: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.2% # Test item 6-15- 1-11

T AMCA: MESE 2326: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.7% # Test item 6-15- 1-12

T AMCA: MESE 2327: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.2% # Test item 7-15- 1- 1

T AMCA: MESE 2327: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.8% # Test item 7-15- 1- 2

T AMCA: MESE 2327: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.005 V, 1.7% # Test item 7-15- 1- 3

T AMCA: MESE 2327: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.2% # Test item 7-15- 1- 4

T AMCA: MESE 2327: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.8% # Test item 7-15- 1- 5

T AMCA: MESE 2327: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.005 V, 1.7% # Test item 7-15- 1- 6

T AMCA: MESE 2327: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 3.9% # Test item 7-15- 1- 7

T AMCA: MESE 2327: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.4% # Test item 7-15- 1- 8

T AMCA: MESE 2327: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.003 V, 1.0% # Test item 7-15- 1- 9

T AMCA: MESE 2327: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.012 V, 4.0% # Test item 7-15- 1-10

T AMCA: MESE 2327: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.5% # Test item 7-15- 1-11

T AMCA: MESE 2327: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.003 V, 1.0% # Test item 7-15- 1-12

T AMCA: MESE 2328: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.2% # Test item 8-15- 1- 1

T AMCA: MESE 2328: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.9% # Test item 8-15- 1- 2

T AMCA: MESE 2328: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.4% # Test item 8-15- 1- 3

T AMCA: MESE 2328: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.013 V, 4.2% # Test item 8-15- 1- 4

T AMCA: MESE 2328: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.8% # Test item 8-15- 1- 5

T AMCA: MESE 2328: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(DAC\_1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.008 V, 2.5% # Test item 8-15- 1- 6

T AMCA: MESE 2328: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.7% # Test item 8-15- 1- 7

T AMCA: MESE 2328: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.0% # Test item 8-15- 1- 8

T AMCA: MESE 2328: Audio\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.3% # Test item 8-15- 1- 9

T AMCA: MESE 2328: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.014 V, 4.6% # Test item 8-15- 1-10

T AMCA: MESE 2328: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.9% # Test item 8-15- 1-11

T AMCA: MESE 2328: Audio\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(DAC\_2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.007 V, 2.2% # Test item 8-15- 1-12

T AMCA: MESE 2321: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.686 MOhm, 20.9% # Test item 1-15- 2- 1

T AMCA: MESE 2321: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.834 MOhm, 11.1% # Test item 1-15- 2- 2

T AMCA: MESE 2321: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.637 MOhm, 24.2% # Test item 1-15- 2- 3

T AMCA: MESE 2321: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.825 MOhm, 11.7% # Test item 1-15- 2- 4

T AMCA: MESE 2322: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.741 MOhm, 17.3% # Test item 2-15- 2- 1

T AMCA: MESE 2322: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.673 MOhm, 21.8% # Test item 2-15- 2- 2

T AMCA: MESE 2322: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.741 MOhm, 17.3% # Test item 2-15- 2- 3

T AMCA: MESE 2322: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.776 MOhm, 14.9% # Test item 2-15- 2- 4

T AMCA: MESE 2323: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.777 MOhm, 14.9% # Test item 3-15- 2- 1

T AMCA: MESE 2323: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.762 MOhm, 15.8% # Test item 3-15- 2- 2

T AMCA: MESE 2323: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.679 MOhm, 21.4% # Test item 3-15- 2- 3

T AMCA: MESE 2323: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.802 MOhm, 13.2% # Test item 3-15- 2- 4

T AMCA: MESE 2324: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.698 MOhm, 20.1% # Test item 4-15- 2- 1

T AMCA: MESE 2324: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.802 MOhm, 13.2% # Test item 4-15- 2- 2

T AMCA: MESE 2324: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.828 MOhm, 11.5% # Test item 4-15- 2- 3

T AMCA: MESE 2324: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.902 MOhm, 6.5% # Test item 4-15- 2- 4

T AMCA: MESE 2325: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.692 MOhm, 20.6% # Test item 5-15- 2- 1

T AMCA: MESE 2325: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.818 MOhm, 12.1% # Test item 5-15- 2- 2

T AMCA: MESE 2325: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.701 MOhm, 19.9% # Test item 5-15- 2- 3

T AMCA: MESE 2325: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.799 MOhm, 13.4% # Test item 5-15- 2- 4

T AMCA: MESE 2326: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.741 MOhm, 17.3% # Test item 6-15- 2- 1

T AMCA: MESE 2326: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.799 MOhm, 13.4% # Test item 6-15- 2- 2

T AMCA: MESE 2326: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.783 MOhm, 14.5% # Test item 6-15- 2- 3

T AMCA: MESE 2326: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.762 MOhm, 15.8% # Test item 6-15- 2- 4

T AMCA: MESE 2327: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.783 MOhm, 14.5% # Test item 7-15- 2- 1

T AMCA: MESE 2327: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.852 MOhm, 9.9% # Test item 7-15- 2- 2

T AMCA: MESE 2327: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.589 MOhm, 27.4% # Test item 7-15- 2- 3

T AMCA: MESE 2327: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.756 MOhm, 16.3% # Test item 7-15- 2- 4

T AMCA: MESE 2328: Audio\_digitizer\_input\_impedance\_test\_Positive(S2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.778 MOhm, 14.8% # Test item 8-15- 2- 1

T AMCA: MESE 2328: Audio\_digitizer\_input\_impedance\_test\_Negative(S1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.779 MOhm, 14.7% # Test item 8-15- 2- 2

T AMCA: MESE 2328: Audio\_digitizer\_input\_impedance\_test\_Positive(T2): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.698 MOhm, 20.1% # Test item 8-15- 2- 3

T AMCA: MESE 2328: Audio\_digitizer\_input\_impedance\_test\_Negative(T1): R\_exp = 10.000 MOhm, [8.500 ... 11.500 MOhm] : R\_meas = 9.732 MOhm, 17.8% # Test item 8-15- 2- 4

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.027V, neg = 0.027V # Test item 1-15- 3- 1

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.714 V, 17.9% >> pos = 2.384V, neg = -2.330V # Test item 1-15- 3- 2

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.027V, neg = 0.027V # Test item 1-15- 3- 3

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 19.9% >> pos = 0.811V, neg = -0.757V # Test item 1-15- 3- 4

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.027V, neg = 0.027V # Test item 1-15- 3- 5

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.6% >> pos = 0.125V, neg = -0.071V # Test item 1-15- 3- 6

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 1-15- 3- 7

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.714 V, 18.0% >> pos = 2.330V, neg = -2.384V # Test item 1-15- 3- 8

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 1-15- 3- 9

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 20.0% >> pos = 0.757V, neg = -0.811V # Test item 1-15- 3-10

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 1-15- 3-11

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.6% >> pos = 0.071V, neg = -0.125V # Test item 1-15- 3-12

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.026V, neg = 0.026V # Test item 1-15- 3-13

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.713 V, 18.1% >> pos = 2.383V, neg = -2.330V # Test item 1-15- 3-14

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.026V, neg = 0.026V # Test item 1-15- 3-15

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 20.1% >> pos = 0.810V, neg = -0.757V # Test item 1-15- 3-16

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.027V, neg = 0.027V # Test item 1-15- 3-17

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.6% >> pos = 0.125V, neg = -0.071V # Test item 1-15- 3-18

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 1-15- 3-19

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.713 V, 18.1% >> pos = 2.330V, neg = -2.383V # Test item 1-15- 3-20

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 1-15- 3-21

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 20.1% >> pos = 0.757V, neg = -0.811V # Test item 1-15- 3-22

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 1-15- 3-23

T AMCA: MESE 2321: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.7% >> pos = 0.071V, neg = -0.125V # Test item 1-15- 3-24

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.027V, neg = 0.028V # Test item 2-15- 3- 1

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.712 V, 18.3% >> pos = 2.384V, neg = -2.328V # Test item 2-15- 3- 2

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.028V, neg = 0.028V # Test item 2-15- 3- 3

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.567 V, 20.3% >> pos = 0.811V, neg = -0.756V # Test item 2-15- 3- 4

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.028V, neg = 0.028V # Test item 2-15- 3- 5

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.0% >> pos = 0.126V, neg = -0.070V # Test item 2-15- 3- 6

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.028V, neg = -0.028V # Test item 2-15- 3- 7

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.713 V, 18.1% >> pos = 2.329V, neg = -2.384V # Test item 2-15- 3- 8

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 2-15- 3- 9

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 20.3% >> pos = 0.756V, neg = -0.811V # Test item 2-15- 3-10

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 2-15- 3-11

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.0% >> pos = 0.071V, neg = -0.126V # Test item 2-15- 3-12

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.026V, neg = 0.026V # Test item 2-15- 3-13

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.711 V, 18.5% >> pos = 2.382V, neg = -2.329V # Test item 2-15- 3-14

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.026V, neg = 0.026V # Test item 2-15- 3-15

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.567 V, 20.8% >> pos = 0.810V, neg = -0.757V # Test item 2-15- 3-16

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.026V, neg = 0.026V # Test item 2-15- 3-17

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.2% >> pos = 0.125V, neg = -0.072V # Test item 2-15- 3-18

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.026V, neg = -0.026V # Test item 2-15- 3-19

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.711 V, 18.5% >> pos = 2.329V, neg = -2.382V # Test item 2-15- 3-20

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.026V, neg = -0.026V # Test item 2-15- 3-21

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.567 V, 20.8% >> pos = 0.757V, neg = -0.810V # Test item 2-15- 3-22

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.026V, neg = -0.026V # Test item 2-15- 3-23

T AMCA: MESE 2322: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.1% >> pos = 0.072V, neg = -0.125V # Test item 2-15- 3-24

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.028V, neg = 0.028V # Test item 3-15- 3- 1

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.2% >> pos = 2.380V, neg = -2.323V # Test item 3-15- 3- 2

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.028V, neg = 0.028V # Test item 3-15- 3- 3

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.565 V, 22.2% >> pos = 0.810V, neg = -0.754V # Test item 3-15- 3- 4

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.028V, neg = 0.028V # Test item 3-15- 3- 5

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.0% >> pos = 0.126V, neg = -0.070V # Test item 3-15- 3- 6

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.028V, neg = -0.028V # Test item 3-15- 3- 7

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.702 V, 20.3% >> pos = 2.323V, neg = -2.379V # Test item 3-15- 3- 8

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.028V, neg = -0.028V # Test item 3-15- 3- 9

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.2% >> pos = 0.754V, neg = -0.810V # Test item 3-15- 3-10

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.028V, neg = -0.028V # Test item 3-15- 3-11

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 20.9% >> pos = 0.070V, neg = -0.126V # Test item 3-15- 3-12

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.028V, neg = 0.028V # Test item 3-15- 3-13

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.2% >> pos = 2.380V, neg = -2.323V # Test item 3-15- 3-14

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.028V, neg = 0.028V # Test item 3-15- 3-15

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.3% >> pos = 0.811V, neg = -0.754V # Test item 3-15- 3-16

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.028V, neg = 0.028V # Test item 3-15- 3-17

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.2% >> pos = 0.126V, neg = -0.069V # Test item 3-15- 3-18

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.028V, neg = -0.028V # Test item 3-15- 3-19

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.703 V, 20.2% >> pos = 2.323V, neg = -2.380V # Test item 3-15- 3-20

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.028V, neg = -0.028V # Test item 3-15- 3-21

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.3% >> pos = 0.754V, neg = -0.811V # Test item 3-15- 3-22

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.028V, neg = -0.028V # Test item 3-15- 3-23

T AMCA: MESE 2323: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.0% >> pos = 0.069V, neg = -0.126V # Test item 3-15- 3-24

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.027V, neg = 0.027V # Test item 4-15- 3- 1

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.716 V, 17.6% >> pos = 2.385V, neg = -2.331V # Test item 4-15- 3- 2

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.027V, neg = 0.027V # Test item 4-15- 3- 3

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.569 V, 19.6% >> pos = 0.811V, neg = -0.758V # Test item 4-15- 3- 4

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.027V, neg = 0.027V # Test item 4-15- 3- 5

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.3% >> pos = 0.125V, neg = -0.072V # Test item 4-15- 3- 6

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 4-15- 3- 7

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.716 V, 17.5% >> pos = 2.331V, neg = -2.385V # Test item 4-15- 3- 8

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 4-15- 3- 9

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.569 V, 19.6% >> pos = 0.758V, neg = -0.811V # Test item 4-15- 3-10

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.027V, neg = -0.027V # Test item 4-15- 3-11

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.4% >> pos = 0.071V, neg = -0.125V # Test item 4-15- 3-12

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 4-15- 3-13

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.716 V, 17.6% >> pos = 2.383V, neg = -2.332V # Test item 4-15- 3-14

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 4-15- 3-15

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 19.7% >> pos = 0.810V, neg = -0.759V # Test item 4-15- 3-16

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 4-15- 3-17

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.3% >> pos = 0.124V, neg = -0.073V # Test item 4-15- 3-18

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 4-15- 3-19

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.716 V, 17.5% >> pos = 2.332V, neg = -2.384V # Test item 4-15- 3-20

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 4-15- 3-21

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 19.7% >> pos = 0.759V, neg = -0.810V # Test item 4-15- 3-22

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 4-15- 3-23

T AMCA: MESE 2324: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.4% >> pos = 0.073V, neg = -0.124V # Test item 4-15- 3-24

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.024V, neg = 0.024V # Test item 5-15- 3- 1

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.715 V, 17.8% >> pos = 2.381V, neg = -2.334V # Test item 5-15- 3- 2

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.024V, neg = 0.024V # Test item 5-15- 3- 3

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.569 V, 19.7% >> pos = 0.808V, neg = -0.761V # Test item 5-15- 3- 4

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.024V, neg = 0.024V # Test item 5-15- 3- 5

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.5% >> pos = 0.122V, neg = -0.075V # Test item 5-15- 3- 6

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.024V, neg = -0.024V # Test item 5-15- 3- 7

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.714 V, 18.0% >> pos = 2.333V, neg = -2.381V # Test item 5-15- 3- 8

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.024V, neg = -0.024V # Test item 5-15- 3- 9

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 19.7% >> pos = 0.761V, neg = -0.808V # Test item 5-15- 3-10

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.024V, neg = -0.024V # Test item 5-15- 3-11

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.5% >> pos = 0.074V, neg = -0.122V # Test item 5-15- 3-12

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.023V, neg = 0.023V # Test item 5-15- 3-13

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.713 V, 18.0% >> pos = 2.380V, neg = -2.334V # Test item 5-15- 3-14

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.023V, neg = 0.023V # Test item 5-15- 3-15

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 20.1% >> pos = 0.807V, neg = -0.761V # Test item 5-15- 3-16

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.023V, neg = 0.023V # Test item 5-15- 3-17

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.8% >> pos = 0.121V, neg = -0.075V # Test item 5-15- 3-18

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.023V, neg = -0.023V # Test item 5-15- 3-19

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.715 V, 17.7% >> pos = 2.334V, neg = -2.381V # Test item 5-15- 3-20

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.023V, neg = -0.023V # Test item 5-15- 3-21

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 20.0% >> pos = 0.761V, neg = -0.807V # Test item 5-15- 3-22

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.023V, neg = -0.023V # Test item 5-15- 3-23

T AMCA: MESE 2325: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.8% >> pos = 0.075V, neg = -0.121V # Test item 5-15- 3-24

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.026V, neg = 0.025V # Test item 6-15- 3- 1

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.699 V, 21.0% >> pos = 2.375V, neg = -2.324V # Test item 6-15- 3- 2

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 6-15- 3- 3

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.563 V, 23.1% >> pos = 0.807V, neg = -0.756V # Test item 6-15- 3- 4

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 6-15- 3- 5

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.7% >> pos = 0.123V, neg = -0.072V # Test item 6-15- 3- 6

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.026V, neg = -0.026V # Test item 6-15- 3- 7

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.699 V, 21.0% >> pos = 2.324V, neg = -2.375V # Test item 6-15- 3- 8

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.026V, neg = -0.026V # Test item 6-15- 3- 9

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.563 V, 23.1% >> pos = 0.756V, neg = -0.807V # Test item 6-15- 3-10

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.026V, neg = -0.026V # Test item 6-15- 3-11

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.8% >> pos = 0.072V, neg = -0.124V # Test item 6-15- 3-12

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 6-15- 3-13

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.700 V, 20.9% >> pos = 2.375V, neg = -2.324V # Test item 6-15- 3-14

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 6-15- 3-15

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.8% >> pos = 0.807V, neg = -0.756V # Test item 6-15- 3-16

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 6-15- 3-17

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.2% >> pos = 0.123V, neg = -0.072V # Test item 6-15- 3-18

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 6-15- 3-19

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.700 V, 20.9% >> pos = 2.324V, neg = -2.376V # Test item 6-15- 3-20

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 6-15- 3-21

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.8% >> pos = 0.756V, neg = -0.807V # Test item 6-15- 3-22

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 6-15- 3-23

T AMCA: MESE 2326: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.3% >> pos = 0.072V, neg = -0.123V # Test item 6-15- 3-24

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 7-15- 3- 1

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.700 V, 20.8% >> pos = 2.375V, neg = -2.325V # Test item 7-15- 3- 2

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 7-15- 3- 3

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.7% >> pos = 0.806V, neg = -0.757V # Test item 7-15- 3- 4

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 7-15- 3- 5

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.7% >> pos = 0.122V, neg = -0.073V # Test item 7-15- 3- 6

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 7-15- 3- 7

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.701 V, 20.7% >> pos = 2.326V, neg = -2.375V # Test item 7-15- 3- 8

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 7-15- 3- 9

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.7% >> pos = 0.757V, neg = -0.807V # Test item 7-15- 3-10

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 7-15- 3-11

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.6% >> pos = 0.073V, neg = -0.123V # Test item 7-15- 3-12

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.024V, neg = 0.024V # Test item 7-15- 3-13

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.700 V, 20.8% >> pos = 2.374V, neg = -2.326V # Test item 7-15- 3-14

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.024V, neg = 0.024V # Test item 7-15- 3-15

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.8% >> pos = 0.806V, neg = -0.758V # Test item 7-15- 3-16

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.024V, neg = 0.024V # Test item 7-15- 3-17

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.4% >> pos = 0.122V, neg = -0.074V # Test item 7-15- 3-18

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.024V, neg = -0.024V # Test item 7-15- 3-19

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.701 V, 20.6% >> pos = 2.326V, neg = -2.375V # Test item 7-15- 3-20

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.024V, neg = -0.024V # Test item 7-15- 3-21

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.564 V, 22.7% >> pos = 0.758V, neg = -0.806V # Test item 7-15- 3-22

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.024V, neg = -0.024V # Test item 7-15- 3-23

T AMCA: MESE 2327: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 21.3% >> pos = 0.073V, neg = -0.122V # Test item 7-15- 3-24

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.026V, neg = 0.026V # Test item 8-15- 3- 1

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.713 V, 18.1% >> pos = 2.383V, neg = -2.330V # Test item 8-15- 3- 2

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.026V, neg = 0.026V # Test item 8-15- 3- 3

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 20.1% >> pos = 0.810V, neg = -0.758V # Test item 8-15- 3- 4

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.026V, neg = 0.026V # Test item 8-15- 3- 5

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.7% >> pos = 0.125V, neg = -0.072V # Test item 8-15- 3- 6

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.026V, neg = -0.026V # Test item 8-15- 3- 7

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.714 V, 18.0% >> pos = 2.330V, neg = -2.383V # Test item 8-15- 3- 8

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.026V, neg = -0.026V # Test item 8-15- 3- 9

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.568 V, 20.0% >> pos = 0.758V, neg = -0.810V # Test item 8-15- 3-10

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = -0.026V, neg = -0.026V # Test item 8-15- 3-11

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 18.8% >> pos = 0.072V, neg = -0.125V # Test item 8-15- 3-12

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 8-15- 3-13

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.712 V, 18.4% >> pos = 2.381V, neg = -2.330V # Test item 8-15- 3-14

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.025V, neg = 0.025V # Test item 8-15- 3-15

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.567 V, 20.7% >> pos = 0.809V, neg = -0.758V # Test item 8-15- 3-16

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.026V, neg = 0.025V # Test item 8-15- 3-17

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.1% >> pos = 0.124V, neg = -0.073V # Test item 8-15- 3-18

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_6\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 8-15- 3-19

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_2.40\_V\_-\_range\_6\_Vpp): V\_exp = 4.800 V, [4.320 ... 5.280 V] : V\_meas = 4.713 V, 18.2% >> pos = 2.331V, neg = -2.382V # Test item 8-15- 3-20

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.025V # Test item 8-15- 3-21

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.567 V, 20.6% >> pos = 0.758V, neg = -0.809V # Test item 8-15- 3-22

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = -0.025V, neg = -0.026V # Test item 8-15- 3-23

T AMCA: MESE 2328: Audio\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.10\_V\_-\_range\_0.4\_Vpp): V\_exp = 0.200 V, [0.180 ... 0.220 V] : V\_meas = 0.196 V, 19.1% >> pos = 0.073V, neg = -0.124V # Test item 8-15- 3-24

T AMCA: MESE 2321: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 2.9% # Test item 1-16- 1- 1

T AMCA: MESE 2321: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.9% # Test item 1-16- 1- 2

T AMCA: MESE 2321: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.8% # Test item 1-16- 1- 3

T AMCA: MESE 2321: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.0% # Test item 1-16- 1- 4

T AMCA: MESE 2321: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.8% # Test item 1-16- 1- 5

T AMCA: MESE 2321: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.8% # Test item 1-16- 1- 6

T AMCA: MESE 2321: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.0% # Test item 1-16- 1- 7

T AMCA: MESE 2321: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.5% # Test item 1-16- 1- 8

T AMCA: MESE 2321: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.6% # Test item 1-16- 1- 9

T AMCA: MESE 2321: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.0% # Test item 1-16- 1-10

T AMCA: MESE 2321: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.9% # Test item 1-16- 1-11

T AMCA: MESE 2321: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.998 V, 0.7% # Test item 1-16- 1-12

T AMCA: MESE 2322: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 2.0% # Test item 2-16- 1- 1

T AMCA: MESE 2322: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.9% # Test item 2-16- 1- 2

T AMCA: MESE 2322: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 1.1% # Test item 2-16- 1- 3

T AMCA: MESE 2322: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 2.1% # Test item 2-16- 1- 4

T AMCA: MESE 2322: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.2% # Test item 2-16- 1- 5

T AMCA: MESE 2322: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 1.1% # Test item 2-16- 1- 6

T AMCA: MESE 2322: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 2.0% # Test item 2-16- 1- 7

T AMCA: MESE 2322: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 4.8% # Test item 2-16- 1- 8

T AMCA: MESE 2322: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 1.1% # Test item 2-16- 1- 9

T AMCA: MESE 2322: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.006 V, 2.0% # Test item 2-16- 1-10

T AMCA: MESE 2322: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.0% # Test item 2-16- 1-11

T AMCA: MESE 2322: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 1.1% # Test item 2-16- 1-12

T AMCA: MESE 2323: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.3% # Test item 3-16- 1- 1

T AMCA: MESE 2323: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.3% # Test item 3-16- 1- 2

T AMCA: MESE 2323: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.993 V, 2.3% # Test item 3-16- 1- 3

T AMCA: MESE 2323: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.4% # Test item 3-16- 1- 4

T AMCA: MESE 2323: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.2% # Test item 3-16- 1- 5

T AMCA: MESE 2323: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.993 V, 2.2% # Test item 3-16- 1- 6

T AMCA: MESE 2323: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.3% # Test item 3-16- 1- 7

T AMCA: MESE 2323: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.9% # Test item 3-16- 1- 8

T AMCA: MESE 2323: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.993 V, 2.2% # Test item 3-16- 1- 9

T AMCA: MESE 2323: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.3% # Test item 3-16- 1-10

T AMCA: MESE 2323: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.0% # Test item 3-16- 1-11

T AMCA: MESE 2323: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.993 V, 2.2% # Test item 3-16- 1-12

T AMCA: MESE 2324: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.5% # Test item 4-16- 1- 1

T AMCA: MESE 2324: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.4% # Test item 4-16- 1- 2

T AMCA: MESE 2324: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.996 V, 1.2% # Test item 4-16- 1- 3

T AMCA: MESE 2324: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.3% # Test item 4-16- 1- 4

T AMCA: MESE 2324: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.9% # Test item 4-16- 1- 5

T AMCA: MESE 2324: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.996 V, 1.2% # Test item 4-16- 1- 6

T AMCA: MESE 2324: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.4% # Test item 4-16- 1- 7

T AMCA: MESE 2324: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.5% # Test item 4-16- 1- 8

T AMCA: MESE 2324: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.997 V, 1.1% # Test item 4-16- 1- 9

T AMCA: MESE 2324: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.4% # Test item 4-16- 1-10

T AMCA: MESE 2324: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 5.7% # Test item 4-16- 1-11

T AMCA: MESE 2324: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.996 V, 1.2% # Test item 4-16- 1-12

T AMCA: MESE 2325: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.011 V, 3.7% # Test item 5-16- 1- 1

T AMCA: MESE 2325: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.9% # Test item 5-16- 1- 2

T AMCA: MESE 2325: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.994 V, 1.9% # Test item 5-16- 1- 3

T AMCA: MESE 2325: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.011 V, 3.6% # Test item 5-16- 1- 4

T AMCA: MESE 2325: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.1% # Test item 5-16- 1- 5

T AMCA: MESE 2325: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.994 V, 2.0% # Test item 5-16- 1- 6

T AMCA: MESE 2325: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.011 V, 3.6% # Test item 5-16- 1- 7

T AMCA: MESE 2325: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.9% # Test item 5-16- 1- 8

T AMCA: MESE 2325: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.994 V, 1.9% # Test item 5-16- 1- 9

T AMCA: MESE 2325: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.011 V, 3.7% # Test item 5-16- 1-10

T AMCA: MESE 2325: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 9.1% # Test item 5-16- 1-11

T AMCA: MESE 2325: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.994 V, 2.0% # Test item 5-16- 1-12

T AMCA: MESE 2326: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.4% # Test item 6-16- 1- 1

T AMCA: MESE 2326: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.8% # Test item 6-16- 1- 2

T AMCA: MESE 2326: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.003 V, 0.9% # Test item 6-16- 1- 3

T AMCA: MESE 2326: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.4% # Test item 6-16- 1- 4

T AMCA: MESE 2326: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.8% # Test item 6-16- 1- 5

T AMCA: MESE 2326: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.003 V, 0.9% # Test item 6-16- 1- 6

T AMCA: MESE 2326: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.4% # Test item 6-16- 1- 7

T AMCA: MESE 2326: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.6% # Test item 6-16- 1- 8

T AMCA: MESE 2326: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.003 V, 1.0% # Test item 6-16- 1- 9

T AMCA: MESE 2326: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.4% # Test item 6-16- 1-10

T AMCA: MESE 2326: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.9% # Test item 6-16- 1-11

T AMCA: MESE 2326: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.003 V, 0.9% # Test item 6-16- 1-12

T AMCA: MESE 2327: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.4% # Test item 7-16- 1- 1

T AMCA: MESE 2327: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.7% # Test item 7-16- 1- 2

T AMCA: MESE 2327: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.7% # Test item 7-16- 1- 3

T AMCA: MESE 2327: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.5% # Test item 7-16- 1- 4

T AMCA: MESE 2327: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.5% # Test item 7-16- 1- 5

T AMCA: MESE 2327: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.6% # Test item 7-16- 1- 6

T AMCA: MESE 2327: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.008 V, 2.5% # Test item 7-16- 1- 7

T AMCA: MESE 2327: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.4% # Test item 7-16- 1- 8

T AMCA: MESE 2327: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.6% # Test item 7-16- 1- 9

T AMCA: MESE 2327: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.007 V, 2.5% # Test item 7-16- 1-10

T AMCA: MESE 2327: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.006 V, 6.4% # Test item 7-16- 1-11

T AMCA: MESE 2327: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.995 V, 1.7% # Test item 7-16- 1-12

T AMCA: MESE 2328: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.0% # Test item 8-16- 1- 1

T AMCA: MESE 2328: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.0% # Test item 8-16- 1- 2

T AMCA: MESE 2328: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(S2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.1% # Test item 8-16- 1- 3

T AMCA: MESE 2328: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.1% # Test item 8-16- 1- 4

T AMCA: MESE 2328: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.0% # Test item 8-16- 1- 5

T AMCA: MESE 2328: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(S1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -2.999 V, 0.2% # Test item 8-16- 1- 6

T AMCA: MESE 2328: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.1% # Test item 8-16- 1- 7

T AMCA: MESE 2328: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.2% # Test item 8-16- 1- 8

T AMCA: MESE 2328: Video\_digitizer\_offset\_DAC\_path\_test\_Positive(T2)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.1% # Test item 8-16- 1- 9

T AMCA: MESE 2328: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_3\_V): V\_exp = 3.000 V, [2.700 ... 3.300 V] : V\_meas = 3.009 V, 3.0% # Test item 8-16- 1-10

T AMCA: MESE 2328: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(\_0\_V): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.005 V, 5.0% # Test item 8-16- 1-11

T AMCA: MESE 2328: Video\_digitizer\_offset\_DAC\_path\_test\_Negative(T1)\_(-3\_V): V\_exp = -3.000 V, [-3.300 ... -2.700 V] : V\_meas = -3.000 V, 0.1% # Test item 8-16- 1-12

T AMCA: MESE 2321: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9883.232 Ohm, 11.7% >> MV = 1.790V, offset = -0.187V # Test item 1-16- 2- 1

T AMCA: MESE 2321: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.194 Ohm, 13.9% >> MV = 0.174V, offset = 0.003V # Test item 1-16- 2- 2

T AMCA: MESE 2321: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.521 Ohm, 10.6% >> MV = 0.137V, offset = 0.003V # Test item 1-16- 2- 3

T AMCA: MESE 2321: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9881.344 Ohm, 11.9% >> MV = 1.790V, offset = -0.187V # Test item 1-16- 2- 4

T AMCA: MESE 2321: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.194 Ohm, 13.9% >> MV = 0.174V, offset = 0.003V # Test item 1-16- 2- 5

T AMCA: MESE 2321: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.731 Ohm, 6.0% >> MV = 0.137V, offset = 0.003V # Test item 1-16- 2- 6

T AMCA: MESE 2322: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9892.044 Ohm, 10.8% >> MV = 1.800V, offset = -0.179V # Test item 2-16- 2- 1

T AMCA: MESE 2322: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.774 Ohm, 21.1% >> MV = 0.172V, offset = 0.002V # Test item 2-16- 2- 2

T AMCA: MESE 2322: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.228 Ohm, 17.2% >> MV = 0.135V, offset = 0.002V # Test item 2-16- 2- 3

T AMCA: MESE 2322: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9895.821 Ohm, 10.4% >> MV = 1.798V, offset = -0.182V # Test item 2-16- 2- 4

T AMCA: MESE 2322: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.194 Ohm, 13.9% >> MV = 0.173V, offset = 0.002V # Test item 2-16- 2- 5

T AMCA: MESE 2322: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.647 Ohm, 7.8% >> MV = 0.136V, offset = 0.002V # Test item 2-16- 2- 6

T AMCA: MESE 2323: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9859.943 Ohm, 14.0% >> MV = 1.789V, offset = -0.183V # Test item 3-16- 2- 1

T AMCA: MESE 2323: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.571 Ohm, 7.4% >> MV = 0.177V, offset = 0.004V # Test item 3-16- 2- 2

T AMCA: MESE 2323: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 45.151 Ohm, 3.4% >> MV = 0.140V, offset = 0.004V # Test item 3-16- 2- 3

T AMCA: MESE 2323: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9858.056 Ohm, 14.2% >> MV = 1.790V, offset = -0.181V # Test item 3-16- 2- 4

T AMCA: MESE 2323: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.690 Ohm, 22.6% >> MV = 0.174V, offset = 0.004V # Test item 3-16- 2- 5

T AMCA: MESE 2323: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.186 Ohm, 18.1% >> MV = 0.137V, offset = 0.005V # Test item 3-16- 2- 6

T AMCA: MESE 2324: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9882.603 Ohm, 11.7% >> MV = 1.790V, offset = -0.187V # Test item 4-16- 2- 1

T AMCA: MESE 2324: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.732 Ohm, 21.9% >> MV = 0.174V, offset = 0.004V # Test item 4-16- 2- 2

T AMCA: MESE 2324: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.353 Ohm, 14.4% >> MV = 0.137V, offset = 0.004V # Test item 4-16- 2- 3

T AMCA: MESE 2324: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9880.085 Ohm, 12.0% >> MV = 1.791V, offset = -0.185V # Test item 4-16- 2- 4

T AMCA: MESE 2324: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.068 Ohm, 16.1% >> MV = 0.175V, offset = 0.004V # Test item 4-16- 2- 5

T AMCA: MESE 2324: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.521 Ohm, 10.6% >> MV = 0.138V, offset = 0.004V # Test item 4-16- 2- 6

T AMCA: MESE 2325: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9882.603 Ohm, 11.7% >> MV = 1.795V, offset = -0.181V # Test item 5-16- 2- 1

T AMCA: MESE 2325: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.446 Ohm, 9.6% >> MV = 0.178V, offset = 0.006V # Test item 5-16- 2- 2

T AMCA: MESE 2325: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.731 Ohm, 6.0% >> MV = 0.141V, offset = 0.007V # Test item 5-16- 2- 3

T AMCA: MESE 2325: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9883.861 Ohm, 11.6% >> MV = 1.800V, offset = -0.177V # Test item 5-16- 2- 4

T AMCA: MESE 2325: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.529 Ohm, 8.1% >> MV = 0.178V, offset = 0.006V # Test item 5-16- 2- 5

T AMCA: MESE 2325: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.731 Ohm, 6.0% >> MV = 0.141V, offset = 0.006V # Test item 5-16- 2- 6

T AMCA: MESE 2326: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9920.998 Ohm, 7.9% >> MV = 1.793V, offset = -0.191V # Test item 6-16- 2- 1

T AMCA: MESE 2326: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.446 Ohm, 9.6% >> MV = 0.174V, offset = 0.002V # Test item 6-16- 2- 2

T AMCA: MESE 2326: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.773 Ohm, 5.0% >> MV = 0.136V, offset = 0.002V # Test item 6-16- 2- 3

T AMCA: MESE 2326: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9919.109 Ohm, 8.1% >> MV = 1.787V, offset = -0.197V # Test item 6-16- 2- 4

T AMCA: MESE 2326: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.152 Ohm, 14.6% >> MV = 0.174V, offset = 0.002V # Test item 6-16- 2- 5

T AMCA: MESE 2326: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.479 Ohm, 11.6% >> MV = 0.136V, offset = 0.002V # Test item 6-16- 2- 6

T AMCA: MESE 2327: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9831.619 Ohm, 16.8% >> MV = 1.792V, offset = -0.175V # Test item 7-16- 2- 1

T AMCA: MESE 2327: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 57.446 Ohm, 9.6% >> MV = 0.175V, offset = 0.003V # Test item 7-16- 2- 2

T AMCA: MESE 2327: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.857 Ohm, 3.2% >> MV = 0.138V, offset = 0.004V # Test item 7-16- 2- 3

T AMCA: MESE 2327: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9831.619 Ohm, 16.8% >> MV = 1.791V, offset = -0.176V # Test item 7-16- 2- 4

T AMCA: MESE 2327: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.984 Ohm, 17.5% >> MV = 0.174V, offset = 0.003V # Test item 7-16- 2- 5

T AMCA: MESE 2327: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.563 Ohm, 9.7% >> MV = 0.137V, offset = 0.004V # Test item 7-16- 2- 6

T AMCA: MESE 2328: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9864.350 Ohm, 13.6% >> MV = 1.786V, offset = -0.187V # Test item 8-16- 2- 1

T AMCA: MESE 2328: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.774 Ohm, 21.1% >> MV = 0.173V, offset = 0.003V # Test item 8-16- 2- 2

T AMCA: MESE 2328: Video\_digitizer\_input\_impedance\_test\_Positive(S2)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.186 Ohm, 18.1% >> MV = 0.136V, offset = 0.003V # Test item 8-16- 2- 3

T AMCA: MESE 2328: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(10\_kOhm): R\_exp = 10000.000 Ohm, [9000.000 ... 11000.000 Ohm] : R\_meas = 9870.644 Ohm, 12.9% >> MV = 1.785V, offset = -0.189V # Test item 8-16- 2- 4

T AMCA: MESE 2328: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(50\_Ohm): R\_exp = 58.000 Ohm, [52.200 ... 63.800 Ohm] : R\_meas = 56.942 Ohm, 18.2% >> MV = 0.173V, offset = 0.003V # Test item 8-16- 2- 5

T AMCA: MESE 2328: Video\_digitizer\_input\_impedance\_test\_Negative(S1)\_(37.5\_Ohm): R\_exp = 45.000 Ohm, [40.500 ... 49.500 Ohm] : R\_meas = 44.437 Ohm, 12.5% >> MV = 0.136V, offset = 0.003V # Test item 8-16- 2- 6

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 4.0% >> pos = 0.089V, neg = 0.093V # Test item 1-16- 3- 1

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.391 V, 59.7% >> pos = 1.783V, neg = -1.608V # Test item 1-16- 3- 2

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.8% >> pos = 0.058V, neg = 0.061V # Test item 1-16- 3- 3

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.676 V, 47.3% >> pos = 0.897V, neg = -0.778V # Test item 1-16- 3- 4

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.7% >> pos = 0.043V, neg = 0.046V # Test item 1-16- 3- 5

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.841 V, 51.8% >> pos = 0.464V, neg = -0.377V # Test item 1-16- 3- 6

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.037V, neg = 0.037V # Test item 1-16- 3- 7

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.263 V, 50.9% >> pos = 0.168V, neg = -0.095V # Test item 1-16- 3- 8

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.6% >> pos = 0.034V, neg = 0.032V # Test item 1-16- 3- 9

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 26.7% >> pos = 0.086V, neg = -0.020V # Test item 1-16- 3-10

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.4% >> pos = 0.035V, neg = 0.032V # Test item 1-16- 3-11

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.363 V, 50.8% >> pos = 1.710V, neg = -1.652V # Test item 1-16- 3-12

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.014V, neg = 0.014V # Test item 1-16- 3-13

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.674 V, 46.3% >> pos = 0.838V, neg = -0.836V # Test item 1-16- 3-14

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = -0.012V, neg = -0.014V # Test item 1-16- 3-15

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.836 V, 45.1% >> pos = 0.403V, neg = -0.433V # Test item 1-16- 3-16

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.9% >> pos = -0.022V, neg = -0.020V # Test item 1-16- 3-17

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.264 V, 54.1% >> pos = 0.110V, neg = -0.154V # Test item 1-16- 3-18

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.0% >> pos = -0.025V, neg = -0.024V # Test item 1-16- 3-19

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.106 V, 30.0% >> pos = 0.028V, neg = -0.078V # Test item 1-16- 3-20

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.5% >> pos = 0.090V, neg = 0.092V # Test item 1-16- 3-21

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.382 V, 56.8% >> pos = 1.780V, neg = -1.602V # Test item 1-16- 3-22

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = 0.059V, neg = 0.059V # Test item 1-16- 3-23

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.682 V, 50.9% >> pos = 0.901V, neg = -0.781V # Test item 1-16- 3-24

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = 0.045V, neg = 0.044V # Test item 1-16- 3-25

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.840 V, 49.9% >> pos = 0.463V, neg = -0.377V # Test item 1-16- 3-26

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = 0.037V, neg = 0.036V # Test item 1-16- 3-27

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.264 V, 56.4% >> pos = 0.170V, neg = -0.095V # Test item 1-16- 3-28

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = 0.032V, neg = 0.033V # Test item 1-16- 3-29

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 25.6% >> pos = 0.086V, neg = -0.020V # Test item 1-16- 3-30

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.006 V, 6.4% >> pos = 0.027V, neg = 0.034V # Test item 1-16- 3-31

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.358 V, 49.5% >> pos = 1.710V, neg = -1.648V # Test item 1-16- 3-32

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.0% >> pos = 0.001V, neg = 0.001V # Test item 1-16- 3-33

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.675 V, 46.6% >> pos = 0.839V, neg = -0.835V # Test item 1-16- 3-34

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.0% >> pos = -0.016V, neg = -0.014V # Test item 1-16- 3-35

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.837 V, 46.8% >> pos = 0.405V, neg = -0.432V # Test item 1-16- 3-36

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = -0.021V, neg = -0.021V # Test item 1-16- 3-37

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.263 V, 50.3% >> pos = 0.110V, neg = -0.153V # Test item 1-16- 3-38

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = -0.024V, neg = -0.024V # Test item 1-16- 3-39

T AMCA: MESE 2321: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.106 V, 27.6% >> pos = 0.028V, neg = -0.078V # Test item 1-16- 3-40

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.4% >> pos = 0.081V, neg = 0.082V # Test item 2-16- 3- 1

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.371 V, 53.5% >> pos = 1.767V, neg = -1.604V # Test item 2-16- 3- 2

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.0% >> pos = 0.058V, neg = 0.055V # Test item 2-16- 3- 3

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.672 V, 44.8% >> pos = 0.892V, neg = -0.780V # Test item 2-16- 3- 4

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.5% >> pos = 0.041V, neg = 0.043V # Test item 2-16- 3- 5

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.838 V, 47.2% >> pos = 0.461V, neg = -0.376V # Test item 2-16- 3- 6

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = 0.036V, neg = 0.036V # Test item 2-16- 3- 7

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.262 V, 49.9% >> pos = 0.168V, neg = -0.095V # Test item 2-16- 3- 8

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.033V, neg = 0.033V # Test item 2-16- 3- 9

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.106 V, 28.8% >> pos = 0.085V, neg = -0.020V # Test item 2-16- 3-10

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = 0.024V, neg = 0.023V # Test item 2-16- 3-11

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.369 V, 52.9% >> pos = 1.709V, neg = -1.661V # Test item 2-16- 3-12

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.3% >> pos = 0.011V, neg = 0.011V # Test item 2-16- 3-13

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.674 V, 46.2% >> pos = 0.832V, neg = -0.842V # Test item 2-16- 3-14

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = -0.014V, neg = -0.014V # Test item 2-16- 3-15

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.837 V, 46.2% >> pos = 0.403V, neg = -0.434V # Test item 2-16- 3-16

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.2% >> pos = -0.021V, neg = -0.024V # Test item 2-16- 3-17

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.262 V, 46.4% >> pos = 0.108V, neg = -0.153V # Test item 2-16- 3-18

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = -0.025V, neg = -0.026V # Test item 2-16- 3-19

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.106 V, 30.1% >> pos = 0.028V, neg = -0.078V # Test item 2-16- 3-20

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = 0.079V, neg = 0.080V # Test item 2-16- 3-21

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.368 V, 52.4% >> pos = 1.768V, neg = -1.600V # Test item 2-16- 3-22

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.055V, neg = 0.054V # Test item 2-16- 3-23

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.673 V, 45.5% >> pos = 0.892V, neg = -0.781V # Test item 2-16- 3-24

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.0% >> pos = 0.044V, neg = 0.041V # Test item 2-16- 3-25

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.839 V, 49.3% >> pos = 0.463V, neg = -0.377V # Test item 2-16- 3-26

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.036V, neg = 0.035V # Test item 2-16- 3-27

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.261 V, 45.9% >> pos = 0.167V, neg = -0.095V # Test item 2-16- 3-28

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.033V, neg = 0.033V # Test item 2-16- 3-29

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 18.8% >> pos = 0.084V, neg = -0.020V # Test item 2-16- 3-30

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.9% >> pos = 0.020V, neg = 0.023V # Test item 2-16- 3-31

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.369 V, 52.9% >> pos = 1.707V, neg = -1.662V # Test item 2-16- 3-32

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.5% >> pos = -0.003V, neg = -0.004V # Test item 2-16- 3-33

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.674 V, 46.2% >> pos = 0.833V, neg = -0.841V # Test item 2-16- 3-34

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = -0.016V, neg = -0.017V # Test item 2-16- 3-35

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.838 V, 47.1% >> pos = 0.403V, neg = -0.435V # Test item 2-16- 3-36

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = -0.023V, neg = -0.023V # Test item 2-16- 3-37

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.263 V, 53.0% >> pos = 0.109V, neg = -0.154V # Test item 2-16- 3-38

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = -0.026V, neg = -0.027V # Test item 2-16- 3-39

T AMCA: MESE 2322: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 25.4% >> pos = 0.027V, neg = -0.078V # Test item 2-16- 3-40

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.006 V, 5.6% >> pos = 0.076V, neg = 0.082V # Test item 3-16- 3- 1

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.372 V, 53.8% >> pos = 1.765V, neg = -1.607V # Test item 3-16- 3- 2

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% >> pos = 0.059V, neg = 0.057V # Test item 3-16- 3- 3

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.671 V, 44.1% >> pos = 0.894V, neg = -0.777V # Test item 3-16- 3- 4

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.2% >> pos = 0.045V, neg = 0.042V # Test item 3-16- 3- 5

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.838 V, 47.9% >> pos = 0.463V, neg = -0.375V # Test item 3-16- 3- 6

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = 0.036V, neg = 0.037V # Test item 3-16- 3- 7

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.262 V, 49.5% >> pos = 0.168V, neg = -0.094V # Test item 3-16- 3- 8

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.034V, neg = 0.034V # Test item 3-16- 3- 9

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 20.1% >> pos = 0.085V, neg = -0.019V # Test item 3-16- 3-10

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.023V, neg = 0.023V # Test item 3-16- 3-11

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.377 V, 55.2% >> pos = 1.708V, neg = -1.668V # Test item 3-16- 3-12

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% >> pos = 0.009V, neg = 0.010V # Test item 3-16- 3-13

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.678 V, 48.8% >> pos = 0.832V, neg = -0.846V # Test item 3-16- 3-14

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = -0.020V, neg = -0.020V # Test item 3-16- 3-15

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.841 V, 51.1% >> pos = 0.401V, neg = -0.440V # Test item 3-16- 3-16

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = -0.024V, neg = -0.024V # Test item 3-16- 3-17

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.263 V, 53.1% >> pos = 0.107V, neg = -0.156V # Test item 3-16- 3-18

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = -0.029V, neg = -0.028V # Test item 3-16- 3-19

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 25.0% >> pos = 0.025V, neg = -0.080V # Test item 3-16- 3-20

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = 0.082V, neg = 0.082V # Test item 3-16- 3-21

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.371 V, 53.4% >> pos = 1.769V, neg = -1.602V # Test item 3-16- 3-22

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.6% >> pos = 0.056V, neg = 0.054V # Test item 3-16- 3-23

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.672 V, 45.2% >> pos = 0.893V, neg = -0.779V # Test item 3-16- 3-24

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.2% >> pos = 0.043V, neg = 0.045V # Test item 3-16- 3-25

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.838 V, 47.7% >> pos = 0.461V, neg = -0.378V # Test item 3-16- 3-26

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.036V, neg = 0.037V # Test item 3-16- 3-27

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.262 V, 46.7% >> pos = 0.167V, neg = -0.095V # Test item 3-16- 3-28

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = 0.034V, neg = 0.033V # Test item 3-16- 3-29

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.106 V, 30.6% >> pos = 0.086V, neg = -0.020V # Test item 3-16- 3-30

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = 0.023V, neg = 0.023V # Test item 3-16- 3-31

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.373 V, 54.2% >> pos = 1.708V, neg = -1.666V # Test item 3-16- 3-32

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = -0.006V, neg = -0.008V # Test item 3-16- 3-33

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.676 V, 47.2% >> pos = 0.832V, neg = -0.844V # Test item 3-16- 3-34

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.5% >> pos = -0.022V, neg = -0.020V # Test item 3-16- 3-35

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.836 V, 45.1% >> pos = 0.398V, neg = -0.438V # Test item 3-16- 3-36

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.4% >> pos = -0.025V, neg = -0.023V # Test item 3-16- 3-37

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.262 V, 46.7% >> pos = 0.107V, neg = -0.154V # Test item 3-16- 3-38

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = -0.027V, neg = -0.028V # Test item 3-16- 3-39

T AMCA: MESE 2323: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 25.7% >> pos = 0.026V, neg = -0.080V # Test item 3-16- 3-40

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.009 V, 9.1% >> pos = 0.065V, neg = 0.074V # Test item 4-16- 3- 1

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.372 V, 53.6% >> pos = 1.755V, neg = -1.616V # Test item 4-16- 3- 2

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.4% >> pos = 0.052V, neg = 0.049V # Test item 4-16- 3- 3

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.673 V, 45.7% >> pos = 0.886V, neg = -0.787V # Test item 4-16- 3- 4

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.8% >> pos = 0.038V, neg = 0.042V # Test item 4-16- 3- 5

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.836 V, 45.4% >> pos = 0.457V, neg = -0.379V # Test item 4-16- 3- 6

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.0% >> pos = 0.034V, neg = 0.031V # Test item 4-16- 3- 7

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.260 V, 41.8% >> pos = 0.165V, neg = -0.096V # Test item 4-16- 3- 8

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = 0.030V, neg = 0.032V # Test item 4-16- 3- 9

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.106 V, 27.6% >> pos = 0.084V, neg = -0.021V # Test item 4-16- 3-10

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = 0.014V, neg = 0.013V # Test item 4-16- 3-11

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.375 V, 54.8% >> pos = 1.700V, neg = -1.676V # Test item 4-16- 3-12

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = 0.008V, neg = 0.006V # Test item 4-16- 3-13

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.672 V, 45.1% >> pos = 0.827V, neg = -0.845V # Test item 4-16- 3-14

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = -0.020V, neg = -0.018V # Test item 4-16- 3-15

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.836 V, 45.4% >> pos = 0.400V, neg = -0.436V # Test item 4-16- 3-16

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = -0.025V, neg = -0.024V # Test item 4-16- 3-17

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.262 V, 46.7% >> pos = 0.107V, neg = -0.155V # Test item 4-16- 3-18

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.9% >> pos = -0.027V, neg = -0.026V # Test item 4-16- 3-19

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 18.0% >> pos = 0.027V, neg = -0.077V # Test item 4-16- 3-20

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.1% >> pos = 0.075V, neg = 0.075V # Test item 4-16- 3-21

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.380 V, 56.4% >> pos = 1.763V, neg = -1.617V # Test item 4-16- 3-22

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.050V, neg = 0.050V # Test item 4-16- 3-23

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.676 V, 47.4% >> pos = 0.887V, neg = -0.789V # Test item 4-16- 3-24

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = 0.040V, neg = 0.039V # Test item 4-16- 3-25

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.842 V, 51.9% >> pos = 0.460V, neg = -0.381V # Test item 4-16- 3-26

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.5% >> pos = 0.034V, neg = 0.035V # Test item 4-16- 3-27

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.263 V, 52.1% >> pos = 0.166V, neg = -0.097V # Test item 4-16- 3-28

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.0% >> pos = 0.032V, neg = 0.032V # Test item 4-16- 3-29

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 18.0% >> pos = 0.083V, neg = -0.021V # Test item 4-16- 3-30

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.3% >> pos = 0.012V, neg = 0.010V # Test item 4-16- 3-31

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.377 V, 55.2% >> pos = 1.699V, neg = -1.678V # Test item 4-16- 3-32

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = -0.009V, neg = -0.008V # Test item 4-16- 3-33

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.674 V, 46.4% >> pos = 0.830V, neg = -0.845V # Test item 4-16- 3-34

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = -0.020V, neg = -0.021V # Test item 4-16- 3-35

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.838 V, 46.9% >> pos = 0.399V, neg = -0.438V # Test item 4-16- 3-36

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = -0.023V, neg = -0.024V # Test item 4-16- 3-37

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.260 V, 40.5% >> pos = 0.106V, neg = -0.154V # Test item 4-16- 3-38

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.3% >> pos = -0.027V, neg = -0.026V # Test item 4-16- 3-39

T AMCA: MESE 2324: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.106 V, 32.3% >> pos = 0.027V, neg = -0.079V # Test item 4-16- 3-40

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.013 V, 12.8% >> pos = 0.068V, neg = 0.056V # Test item 5-16- 3- 1

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.407 V, 64.8% >> pos = 1.766V, neg = -1.642V # Test item 5-16- 3- 2

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.0% >> pos = 0.044V, neg = 0.043V # Test item 5-16- 3- 3

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.683 V, 51.6% >> pos = 0.885V, neg = -0.797V # Test item 5-16- 3- 4

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.3% >> pos = 0.037V, neg = 0.034V # Test item 5-16- 3- 5

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.851 V, 64.3% >> pos = 0.463V, neg = -0.388V # Test item 5-16- 3- 6

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = 0.032V, neg = 0.031V # Test item 5-16- 3- 7

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.259 V, 37.8% >> pos = 0.163V, neg = -0.096V # Test item 5-16- 3- 8

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% >> pos = 0.030V, neg = 0.030V # Test item 5-16- 3- 9

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 26.0% >> pos = 0.082V, neg = -0.023V # Test item 5-16- 3-10

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.1% >> pos = 0.009V, neg = 0.002V # Test item 5-16- 3-11

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.371 V, 53.5% >> pos = 1.692V, neg = -1.679V # Test item 5-16- 3-12

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.0% >> pos = 0.003V, neg = 0.000V # Test item 5-16- 3-13

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.675 V, 46.8% >> pos = 0.825V, neg = -0.850V # Test item 5-16- 3-14

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.3% >> pos = -0.018V, neg = -0.018V # Test item 5-16- 3-15

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.842 V, 52.1% >> pos = 0.403V, neg = -0.439V # Test item 5-16- 3-16

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.5% >> pos = -0.023V, neg = -0.024V # Test item 5-16- 3-17

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.264 V, 56.9% >> pos = 0.108V, neg = -0.156V # Test item 5-16- 3-18

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.2% >> pos = -0.026V, neg = -0.027V # Test item 5-16- 3-19

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.112 V, 59.6% >> pos = 0.030V, neg = -0.082V # Test item 5-16- 3-20

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.6% >> pos = 0.065V, neg = 0.058V # Test item 5-16- 3-21

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.406 V, 64.5% >> pos = 1.764V, neg = -1.642V # Test item 5-16- 3-22

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = 0.044V, neg = 0.042V # Test item 5-16- 3-23

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.686 V, 53.6% >> pos = 0.888V, neg = -0.798V # Test item 5-16- 3-24

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.004 V, 4.1% >> pos = 0.035V, neg = 0.031V # Test item 5-16- 3-25

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.842 V, 52.2% >> pos = 0.457V, neg = -0.385V # Test item 5-16- 3-26

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.3% >> pos = 0.029V, neg = 0.031V # Test item 5-16- 3-27

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.263 V, 53.0% >> pos = 0.163V, neg = -0.101V # Test item 5-16- 3-28

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = 0.030V, neg = 0.029V # Test item 5-16- 3-29

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 20.8% >> pos = 0.080V, neg = -0.024V # Test item 5-16- 3-30

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.009 V, 8.9% >> pos = 0.011V, neg = 0.002V # Test item 5-16- 3-31

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.361 V, 50.2% >> pos = 1.687V, neg = -1.673V # Test item 5-16- 3-32

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.7% >> pos = -0.011V, neg = -0.014V # Test item 5-16- 3-33

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.677 V, 48.4% >> pos = 0.829V, neg = -0.849V # Test item 5-16- 3-34

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.5% >> pos = -0.023V, neg = -0.020V # Test item 5-16- 3-35

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.838 V, 48.1% >> pos = 0.398V, neg = -0.440V # Test item 5-16- 3-36

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.7% >> pos = -0.021V, neg = -0.023V # Test item 5-16- 3-37

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.263 V, 50.1% >> pos = 0.108V, neg = -0.155V # Test item 5-16- 3-38

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.1% >> pos = -0.027V, neg = -0.026V # Test item 5-16- 3-39

T AMCA: MESE 2325: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.106 V, 29.1% >> pos = 0.028V, neg = -0.078V # Test item 5-16- 3-40

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.9% >> pos = 0.093V, neg = 0.091V # Test item 6-16- 3- 1

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.362 V, 50.5% >> pos = 1.774V, neg = -1.588V # Test item 6-16- 3- 2

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.0% >> pos = 0.061V, neg = 0.058V # Test item 6-16- 3- 3

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.667 V, 41.7% >> pos = 0.892V, neg = -0.774V # Test item 6-16- 3- 4

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.9% >> pos = 0.048V, neg = 0.047V # Test item 6-16- 3- 5

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.837 V, 46.5% >> pos = 0.464V, neg = -0.373V # Test item 6-16- 3- 6

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.7% >> pos = 0.036V, neg = 0.035V # Test item 6-16- 3- 7

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.261 V, 43.2% >> pos = 0.166V, neg = -0.095V # Test item 6-16- 3- 8

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.033V, neg = 0.033V # Test item 6-16- 3- 9

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.106 V, 28.6% >> pos = 0.087V, neg = -0.019V # Test item 6-16- 3-10

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.8% >> pos = 0.029V, neg = 0.030V # Test item 6-16- 3-11

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.341 V, 43.9% >> pos = 1.704V, neg = -1.637V # Test item 6-16- 3-12

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.4% >> pos = 0.017V, neg = 0.015V # Test item 6-16- 3-13

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.662 V, 38.9% >> pos = 0.834V, neg = -0.829V # Test item 6-16- 3-14

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = -0.009V, neg = -0.011V # Test item 6-16- 3-15

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.832 V, 40.0% >> pos = 0.405V, neg = -0.427V # Test item 6-16- 3-16

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.7% >> pos = -0.020V, neg = -0.022V # Test item 6-16- 3-17

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.263 V, 50.4% >> pos = 0.111V, neg = -0.151V # Test item 6-16- 3-18

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.8% >> pos = -0.024V, neg = -0.025V # Test item 6-16- 3-19

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 22.4% >> pos = 0.028V, neg = -0.077V # Test item 6-16- 3-20

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.5% >> pos = 0.092V, neg = 0.090V # Test item 6-16- 3-21

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.368 V, 52.5% >> pos = 1.772V, neg = -1.596V # Test item 6-16- 3-22

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 7.2% >> pos = 0.064V, neg = 0.057V # Test item 6-16- 3-23

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.667 V, 41.9% >> pos = 0.893V, neg = -0.774V # Test item 6-16- 3-24

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 2.1% >> pos = 0.048V, neg = 0.046V # Test item 6-16- 3-25

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.837 V, 46.1% >> pos = 0.466V, neg = -0.371V # Test item 6-16- 3-26

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% >> pos = 0.036V, neg = 0.037V # Test item 6-16- 3-27

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.260 V, 41.7% >> pos = 0.168V, neg = -0.093V # Test item 6-16- 3-28

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.3% >> pos = 0.034V, neg = 0.034V # Test item 6-16- 3-29

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.107 V, 36.7% >> pos = 0.085V, neg = -0.022V # Test item 6-16- 3-30

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.002 V, 1.8% >> pos = 0.032V, neg = 0.030V # Test item 6-16- 3-31

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.337 V, 42.8% >> pos = 1.701V, neg = -1.636V # Test item 6-16- 3-32

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.5% >> pos = 0.002V, neg = 0.004V # Test item 6-16- 3-33

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.659 V, 37.0% >> pos = 0.834V, neg = -0.826V # Test item 6-16- 3-34

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 2.6% >> pos = -0.012V, neg = -0.014V # Test item 6-16- 3-35

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.831 V, 39.4% >> pos = 0.403V, neg = -0.428V # Test item 6-16- 3-36

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 2.5% >> pos = -0.023V, neg = -0.020V # Test item 6-16- 3-37

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.262 V, 48.2% >> pos = 0.110V, neg = -0.152V # Test item 6-16- 3-38

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.5% >> pos = -0.025V, neg = -0.024V # Test item 6-16- 3-39

T AMCA: MESE 2326: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.108 V, 38.1% >> pos = 0.029V, neg = -0.078V # Test item 6-16- 3-40

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.7% >> pos = 0.071V, neg = 0.073V # Test item 7-16- 3- 1

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.365 V, 51.4% >> pos = 1.760V, neg = -1.605V # Test item 7-16- 3- 2

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.5% >> pos = 0.047V, neg = 0.048V # Test item 7-16- 3- 3

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.674 V, 46.3% >> pos = 0.885V, neg = -0.789V # Test item 7-16- 3- 4

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = 0.038V, neg = 0.037V # Test item 7-16- 3- 5

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.836 V, 45.3% >> pos = 0.457V, neg = -0.379V # Test item 7-16- 3- 6

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.032V, neg = 0.032V # Test item 7-16- 3- 7

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.261 V, 44.5% >> pos = 0.163V, neg = -0.098V # Test item 7-16- 3- 8

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.4% >> pos = 0.029V, neg = 0.029V # Test item 7-16- 3- 9

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 17.9% >> pos = 0.081V, neg = -0.023V # Test item 7-16- 3-10

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = 0.017V, neg = 0.016V # Test item 7-16- 3-11

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.363 V, 51.0% >> pos = 1.697V, neg = -1.667V # Test item 7-16- 3-12

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = 0.008V, neg = 0.008V # Test item 7-16- 3-13

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.669 V, 42.9% >> pos = 0.829V, neg = -0.840V # Test item 7-16- 3-14

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.0% >> pos = -0.015V, neg = -0.018V # Test item 7-16- 3-15

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.836 V, 45.2% >> pos = 0.402V, neg = -0.434V # Test item 7-16- 3-16

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = -0.020V, neg = -0.021V # Test item 7-16- 3-17

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.262 V, 46.4% >> pos = 0.109V, neg = -0.153V # Test item 7-16- 3-18

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = -0.025V, neg = -0.024V # Test item 7-16- 3-19

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.101 V, 6.0% >> pos = 0.027V, neg = -0.074V # Test item 7-16- 3-20

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = 0.071V, neg = 0.071V # Test item 7-16- 3-21

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.372 V, 53.7% >> pos = 1.760V, neg = -1.612V # Test item 7-16- 3-22

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 2.7% >> pos = 0.047V, neg = 0.050V # Test item 7-16- 3-23

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.670 V, 43.6% >> pos = 0.883V, neg = -0.787V # Test item 7-16- 3-24

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.004 V, 3.6% >> pos = 0.035V, neg = 0.039V # Test item 7-16- 3-25

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.835 V, 43.8% >> pos = 0.455V, neg = -0.380V # Test item 7-16- 3-26

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = 0.032V, neg = 0.033V # Test item 7-16- 3-27

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.261 V, 43.8% >> pos = 0.162V, neg = -0.099V # Test item 7-16- 3-28

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.8% >> pos = 0.028V, neg = 0.029V # Test item 7-16- 3-29

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 27.5% >> pos = 0.081V, neg = -0.025V # Test item 7-16- 3-30

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.007 V, 6.9% >> pos = 0.021V, neg = 0.015V # Test item 7-16- 3-31

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.372 V, 53.9% >> pos = 1.703V, neg = -1.670V # Test item 7-16- 3-32

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.000 V, 0.2% >> pos = -0.004V, neg = -0.004V # Test item 7-16- 3-33

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.668 V, 42.7% >> pos = 0.830V, neg = -0.838V # Test item 7-16- 3-34

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = -0.016V, neg = -0.015V # Test item 7-16- 3-35

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.840 V, 49.6% >> pos = 0.406V, neg = -0.433V # Test item 7-16- 3-36

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.7% >> pos = -0.022V, neg = -0.020V # Test item 7-16- 3-37

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.264 V, 56.2% >> pos = 0.111V, neg = -0.153V # Test item 7-16- 3-38

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.6% >> pos = -0.024V, neg = -0.024V # Test item 7-16- 3-39

T AMCA: MESE 2327: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 19.8% >> pos = 0.028V, neg = -0.076V # Test item 7-16- 3-40

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = 0.073V, neg = 0.072V # Test item 8-16- 3- 1

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.347 V, 46.0% >> pos = 1.750V, neg = -1.597V # Test item 8-16- 3- 2

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.006 V, 5.6% >> pos = 0.050V, neg = 0.056V # Test item 8-16- 3- 3

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.663 V, 39.3% >> pos = 0.882V, neg = -0.781V # Test item 8-16- 3- 4

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.2% >> pos = 0.038V, neg = 0.038V # Test item 8-16- 3- 5

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.831 V, 38.4% >> pos = 0.453V, neg = -0.378V # Test item 8-16- 3- 6

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.034V, neg = 0.035V # Test item 8-16- 3- 7

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.261 V, 44.4% >> pos = 0.165V, neg = -0.096V # Test item 8-16- 3- 8

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.0% >> pos = 0.029V, neg = 0.030V # Test item 8-16- 3- 9

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(S2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.104 V, 22.3% >> pos = 0.083V, neg = -0.022V # Test item 8-16- 3-10

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.1% >> pos = 0.016V, neg = 0.016V # Test item 8-16- 3-11

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.345 V, 45.3% >> pos = 1.686V, neg = -1.659V # Test item 8-16- 3-12

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 0.7% >> pos = 0.008V, neg = 0.009V # Test item 8-16- 3-13

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.658 V, 36.5% >> pos = 0.823V, neg = -0.835V # Test item 8-16- 3-14

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.3% >> pos = -0.018V, neg = -0.015V # Test item 8-16- 3-15

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.834 V, 42.9% >> pos = 0.399V, neg = -0.436V # Test item 8-16- 3-16

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = -0.021V, neg = -0.022V # Test item 8-16- 3-17

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.261 V, 42.1% >> pos = 0.109V, neg = -0.152V # Test item 8-16- 3-18

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 0.6% >> pos = -0.025V, neg = -0.025V # Test item 8-16- 3-19

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(S1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 23.6% >> pos = 0.026V, neg = -0.078V # Test item 8-16- 3-20

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.0% >> pos = 0.073V, neg = 0.074V # Test item 8-16- 3-21

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.348 V, 46.3% >> pos = 1.749V, neg = -1.599V # Test item 8-16- 3-22

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.001 V, 1.4% >> pos = 0.050V, neg = 0.051V # Test item 8-16- 3-23

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.660 V, 37.7% >> pos = 0.883V, neg = -0.778V # Test item 8-16- 3-24

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.003 V, 3.1% >> pos = 0.038V, neg = 0.035V # Test item 8-16- 3-25

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.831 V, 38.6% >> pos = 0.452V, neg = -0.379V # Test item 8-16- 3-26

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.1% >> pos = 0.035V, neg = 0.034V # Test item 8-16- 3-27

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.262 V, 46.5% >> pos = 0.166V, neg = -0.096V # Test item 8-16- 3-28

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.4% >> pos = 0.028V, neg = 0.031V # Test item 8-16- 3-29

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Positive(T2)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 25.3% >> pos = 0.084V, neg = -0.021V # Test item 8-16- 3-30

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_4\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.5% >> pos = 0.015V, neg = 0.014V # Test item 8-16- 3-31

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_1.60\_V\_-\_range\_4\_Vpp): V\_exp = 3.200 V, [2.880 ... 3.520 V] : V\_meas = 3.354 V, 48.1% >> pos = 1.693V, neg = -1.661V # Test item 8-16- 3-32

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_2\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.003 V, 3.2% >> pos = -0.007V, neg = -0.004V # Test item 8-16- 3-33

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 1.600 V, [1.440 ... 1.760 V] : V\_meas = 1.657 V, 35.5% >> pos = 0.822V, neg = -0.835V # Test item 8-16- 3-34

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_1\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.001 V, 1.4% >> pos = -0.019V, neg = -0.021V # Test item 8-16- 3-35

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.40\_V\_-\_range\_1\_Vpp): V\_exp = 0.800 V, [0.720 ... 0.880 V] : V\_meas = 0.834 V, 42.6% >> pos = 0.397V, neg = -0.437V # Test item 8-16- 3-36

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = 0.000 V, 0.4% >> pos = -0.021V, neg = -0.021V # Test item 8-16- 3-37

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.125\_V\_-\_range\_0.5\_Vpp): V\_exp = 0.250 V, [0.225 ... 0.275 V] : V\_meas = 0.260 V, 40.4% >> pos = 0.108V, neg = -0.152V # Test item 8-16- 3-38

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_0.00\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.000 V, [-0.100 ... 0.100 V] : V\_meas = -0.002 V, 1.5% >> pos = -0.027V, neg = -0.026V # Test item 8-16- 3-39

T AMCA: MESE 2328: Video\_digitizer\_input\_range\_and\_ADC\_path\_test\_Negative(T1)\_(\_+/-\_0.05\_V\_-\_range\_0.25\_Vpp): V\_exp = 0.100 V, [0.080 ... 0.120 V] : V\_meas = 0.105 V, 23.9% >> pos = 0.028V, neg = -0.076V # Test item 8-16- 3-40

T AMCA: MESE 2321: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.823 V, 22.6% >> POS = 0.878V, NEG = 0.056V # Test item 1-16- 4- 1

T AMCA: MESE 2321: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.800 V, 0.2% >> POS = 0.857V, NEG = 0.056V # Test item 1-16- 4- 2

T AMCA: MESE 2321: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.788 V, 12.2% >> POS = 0.830V, NEG = 0.042V # Test item 1-16- 4- 3

T AMCA: MESE 2321: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.837 V, 36.7% >> POS = 0.897V, NEG = 0.060V # Test item 1-16- 4- 4

T AMCA: MESE 2322: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.824 V, 23.7% >> POS = 0.875V, NEG = 0.051V # Test item 2-16- 4- 1

T AMCA: MESE 2322: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.803 V, 3.3% >> POS = 0.852V, NEG = 0.049V # Test item 2-16- 4- 2

T AMCA: MESE 2322: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.784 V, 16.2% >> POS = 0.825V, NEG = 0.041V # Test item 2-16- 4- 3

T AMCA: MESE 2322: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.835 V, 34.9% >> POS = 0.891V, NEG = 0.056V # Test item 2-16- 4- 4

T AMCA: MESE 2323: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.823 V, 23.1% >> POS = 0.874V, NEG = 0.051V # Test item 3-16- 4- 1

T AMCA: MESE 2323: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.804 V, 3.9% >> POS = 0.855V, NEG = 0.051V # Test item 3-16- 4- 2

T AMCA: MESE 2323: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.785 V, 15.2% >> POS = 0.824V, NEG = 0.040V # Test item 3-16- 4- 3

T AMCA: MESE 2323: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.837 V, 37.2% >> POS = 0.895V, NEG = 0.058V # Test item 3-16- 4- 4

T AMCA: MESE 2324: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.820 V, 19.5% >> POS = 0.865V, NEG = 0.046V # Test item 4-16- 4- 1

T AMCA: MESE 2324: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.802 V, 2.4% >> POS = 0.848V, NEG = 0.045V # Test item 4-16- 4- 2

T AMCA: MESE 2324: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.782 V, 17.9% >> POS = 0.817V, NEG = 0.034V # Test item 4-16- 4- 3

T AMCA: MESE 2324: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.841 V, 41.0% >> POS = 0.889V, NEG = 0.048V # Test item 4-16- 4- 4

T AMCA: MESE 2325: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.827 V, 27.3% >> POS = 0.867V, NEG = 0.040V # Test item 5-16- 4- 1

T AMCA: MESE 2325: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.809 V, 8.8% >> POS = 0.849V, NEG = 0.040V # Test item 5-16- 4- 2

T AMCA: MESE 2325: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.790 V, 9.7% >> POS = 0.820V, NEG = 0.029V # Test item 5-16- 4- 3

T AMCA: MESE 2325: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.844 V, 43.5% >> POS = 0.888V, NEG = 0.044V # Test item 5-16- 4- 4

T AMCA: MESE 2326: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.821 V, 20.5% >> POS = 0.874V, NEG = 0.054V # Test item 6-16- 4- 1

T AMCA: MESE 2326: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.803 V, 2.8% >> POS = 0.857V, NEG = 0.054V # Test item 6-16- 4- 2

T AMCA: MESE 2326: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.783 V, 17.2% >> POS = 0.827V, NEG = 0.044V # Test item 6-16- 4- 3

T AMCA: MESE 2326: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.836 V, 36.4% >> POS = 0.895V, NEG = 0.058V # Test item 6-16- 4- 4

T AMCA: MESE 2327: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.820 V, 20.3% >> POS = 0.867V, NEG = 0.046V # Test item 7-16- 4- 1

T AMCA: MESE 2327: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.806 V, 6.3% >> POS = 0.851V, NEG = 0.045V # Test item 7-16- 4- 2

T AMCA: MESE 2327: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.782 V, 17.5% >> POS = 0.817V, NEG = 0.035V # Test item 7-16- 4- 3

T AMCA: MESE 2327: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.838 V, 37.6% >> POS = 0.885V, NEG = 0.048V # Test item 7-16- 4- 4

T AMCA: MESE 2328: Video\_digitizer\_filter\_path\_test\_105MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.815 V, 15.3% >> POS = 0.863V, NEG = 0.047V # Test item 8-16- 4- 1

T AMCA: MESE 2328: Video\_digitizer\_filter\_path\_test\_15MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.798 V, 1.5% >> POS = 0.844V, NEG = 0.045V # Test item 8-16- 4- 2

T AMCA: MESE 2328: Video\_digitizer\_filter\_path\_test\_50MHz\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.777 V, 22.8% >> POS = 0.814V, NEG = 0.037V # Test item 8-16- 4- 3

T AMCA: MESE 2328: Video\_digitizer\_filter\_path\_test\_THRU\_filter\_(\_0.80\_V\_-\_range\_2\_Vpp): V\_exp = 0.800 V, [0.700 ... 0.900 V] : V\_meas = 0.830 V, 29.9% >> POS = 0.882V, NEG = 0.052V # Test item 8-16- 4- 4

T AMCA: MESE 2321: Signal\_Input/Output\_relay\_test\_Resistance\_A+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5024.322 Ohm, 0.1% >> vOffset = -0.110V, vMeas = 2.403V, iForce = 0.500mA # Test item 1-20- 1- 1

T AMCA: MESE 2321: Signal\_Input/Output\_relay\_test\_Resistance\_A-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5025.330 Ohm, 0.3% >> vOffset = -0.110V, vMeas = 2.403V, iForce = 0.500mA # Test item 1-20- 1- 2

T AMCA: MESE 2321: Signal\_Input/Output\_relay\_test\_Resistance\_AA+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1009.094 Ohm, 9.1% >> vOffset = -0.018V, vMeas = 0.487V, iForce = 0.500mA # Test item 1-20- 1- 3

T AMCA: MESE 2321: Signal\_Input/Output\_relay\_test\_Resistance\_AA-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1004.814 Ohm, 4.8% >> vOffset = -0.017V, vMeas = 0.485V, iForce = 0.500mA # Test item 1-20- 1- 4

T AMCA: MESE 2322: Signal\_Input/Output\_relay\_test\_Resistance\_B+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5029.106 Ohm, 1.0% >> vOffset = -0.108V, vMeas = 2.407V, iForce = 0.500mA # Test item 2-20- 1- 1

T AMCA: MESE 2322: Signal\_Input/Output\_relay\_test\_Resistance\_B-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5028.854 Ohm, 1.0% >> vOffset = -0.108V, vMeas = 2.407V, iForce = 0.500mA # Test item 2-20- 1- 2

T AMCA: MESE 2322: Signal\_Input/Output\_relay\_test\_Resistance\_BB+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1002.800 Ohm, 2.8% >> vOffset = -0.018V, vMeas = 0.484V, iForce = 0.500mA # Test item 2-20- 1- 3

T AMCA: MESE 2322: Signal\_Input/Output\_relay\_test\_Resistance\_BB-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1003.555 Ohm, 3.6% >> vOffset = -0.018V, vMeas = 0.484V, iForce = 0.500mA # Test item 2-20- 1- 4

T AMCA: MESE 2323: Signal\_Input/Output\_relay\_test\_Resistance\_C+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5015.259 Ohm, 1.7% >> vOffset = -0.107V, vMeas = 2.401V, iForce = 0.500mA # Test item 3-20- 1- 1

T AMCA: MESE 2323: Signal\_Input/Output\_relay\_test\_Resistance\_C-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5015.007 Ohm, 1.8% >> vOffset = -0.107V, vMeas = 2.400V, iForce = 0.500mA # Test item 3-20- 1- 2

T AMCA: MESE 2323: Signal\_Input/Output\_relay\_test\_Resistance\_CC+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1002.296 Ohm, 2.3% >> vOffset = -0.016V, vMeas = 0.485V, iForce = 0.500mA # Test item 3-20- 1- 3

T AMCA: MESE 2323: Signal\_Input/Output\_relay\_test\_Resistance\_CC-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1000.031 Ohm, 0.0% >> vOffset = -0.016V, vMeas = 0.484V, iForce = 0.500mA # Test item 3-20- 1- 4

T AMCA: MESE 2324: Signal\_Input/Output\_relay\_test\_Resistance\_D+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5024.826 Ohm, 0.2% >> vOffset = -0.109V, vMeas = 2.404V, iForce = 0.500mA # Test item 4-20- 1- 1

T AMCA: MESE 2324: Signal\_Input/Output\_relay\_test\_Resistance\_D-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5024.826 Ohm, 0.2% >> vOffset = -0.109V, vMeas = 2.404V, iForce = 0.500mA # Test item 4-20- 1- 2

T AMCA: MESE 2324: Signal\_Input/Output\_relay\_test\_Resistance\_DD+(T2): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1002.800 Ohm, 2.8% >> vOffset = -0.017V, vMeas = 0.485V, iForce = 0.500mA # Test item 4-20- 1- 3

T AMCA: MESE 2324: Signal\_Input/Output\_relay\_test\_Resistance\_DD-(T1): R\_exp = 1000.000 Ohm, [900.000 ... 1100.000 Ohm] : R\_meas = 1004.059 Ohm, 4.1% >> vOffset = -0.017V, vMeas = 0.485V, iForce = 0.500mA # Test item 4-20- 1- 4

T AMCA: MESE 2325: Signal\_Input/Output\_relay\_test\_Resistance\_E+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5027.344 Ohm, 0.7% >> vOffset = -0.108V, vMeas = 2.405V, iForce = 0.500mA # Test item 5-20- 1- 1

T AMCA: MESE 2325: Signal\_Input/Output\_relay\_test\_Resistance\_E-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5027.092 Ohm, 0.6% >> vOffset = -0.107V, vMeas = 2.406V, iForce = 0.500mA # Test item 5-20- 1- 2

T AMCA: MESE 2325: Signal\_Input/Output\_relay\_test\_Resistance\_EE+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5025.078 Ohm, 0.2% >> vOffset = -0.107V, vMeas = 2.405V, iForce = 0.500mA # Test item 5-20- 1- 3

T AMCA: MESE 2325: Signal\_Input/Output\_relay\_test\_Resistance\_EE-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5026.840 Ohm, 0.6% >> vOffset = -0.108V, vMeas = 2.406V, iForce = 0.500mA # Test item 5-20- 1- 4

T AMCA: MESE 2326: Signal\_Input/Output\_relay\_test\_Resistance\_F+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5035.904 Ohm, 2.4% >> vOffset = -0.117V, vMeas = 2.401V, iForce = 0.500mA # Test item 6-20- 1- 1

T AMCA: MESE 2326: Signal\_Input/Output\_relay\_test\_Resistance\_F-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5037.163 Ohm, 2.6% >> vOffset = -0.117V, vMeas = 2.401V, iForce = 0.500mA # Test item 6-20- 1- 2

T AMCA: MESE 2326: Signal\_Input/Output\_relay\_test\_Resistance\_FF+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5037.163 Ohm, 2.6% >> vOffset = -0.118V, vMeas = 2.401V, iForce = 0.500mA # Test item 6-20- 1- 3

T AMCA: MESE 2326: Signal\_Input/Output\_relay\_test\_Resistance\_FF-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5037.163 Ohm, 2.6% >> vOffset = -0.118V, vMeas = 2.401V, iForce = 0.500mA # Test item 6-20- 1- 4

T AMCA: MESE 2327: Signal\_Input/Output\_relay\_test\_Resistance\_G+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5011.482 Ohm, 2.5% >> vOffset = -0.106V, vMeas = 2.399V, iForce = 0.500mA # Test item 7-20- 1- 1

T AMCA: MESE 2327: Signal\_Input/Output\_relay\_test\_Resistance\_G-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5012.741 Ohm, 2.2% >> vOffset = -0.107V, vMeas = 2.399V, iForce = 0.500mA # Test item 7-20- 1- 2

T AMCA: MESE 2327: Signal\_Input/Output\_relay\_test\_Resistance\_GG+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5010.727 Ohm, 2.6% >> vOffset = -0.107V, vMeas = 2.399V, iForce = 0.500mA # Test item 7-20- 1- 3

T AMCA: MESE 2327: Signal\_Input/Output\_relay\_test\_Resistance\_GG-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5010.475 Ohm, 2.7% >> vOffset = -0.106V, vMeas = 2.399V, iForce = 0.500mA # Test item 7-20- 1- 4

T AMCA: MESE 2328: Signal\_Input/Output\_relay\_test\_Resistance\_H+(S2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5020.798 Ohm, 0.6% >> vOffset = -0.113V, vMeas = 2.397V, iForce = 0.500mA # Test item 8-20- 1- 1

T AMCA: MESE 2328: Signal\_Input/Output\_relay\_test\_Resistance\_H-(S1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5021.049 Ohm, 0.6% >> vOffset = -0.112V, vMeas = 2.398V, iForce = 0.500mA # Test item 8-20- 1- 2

T AMCA: MESE 2328: Signal\_Input/Output\_relay\_test\_Resistance\_HH+(T2): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5020.294 Ohm, 0.7% >> vOffset = -0.112V, vMeas = 2.398V, iForce = 0.500mA # Test item 8-20- 1- 3

T AMCA: MESE 2328: Signal\_Input/Output\_relay\_test\_Resistance\_HH-(T1): R\_exp = 5024.000 Ohm, [4521.600 ... 5526.400 Ohm] : R\_meas = 5022.057 Ohm, 0.4% >> vOffset = -0.113V, vMeas = 2.398V, iForce = 0.500mA # Test item 8-20- 1- 4

T AMCA: MCE 2320: Common\_trigger\_input\_relay\_test\_Common\_Trigger\_1: R\_exp = 470.000 Ohm, [369.000 ... 571.000 Ohm] : R\_meas = 470.558 Ohm, 0.6% >> vMeas = 1.406V, vOffset = -0.006V, usedUnit = 1 # Test item 0-21- 1- 1

T AMCA: MCE 2320: Common\_trigger\_input\_relay\_test\_Common\_Trigger\_2: R\_exp = 470.000 Ohm, [369.000 ... 571.000 Ohm] : R\_meas = 469.257 Ohm, 0.7% >> vMeas = 1.402V, vOffset = -0.006V, usedUnit = 1 # Test item 0-21- 1- 2

# Leaving T AMCA (MCA/MCB/MCC...): 15- Jan-2021 15:40:08.679

### Diagnostic finished at: Fri Jan 15 15:40:09 2021